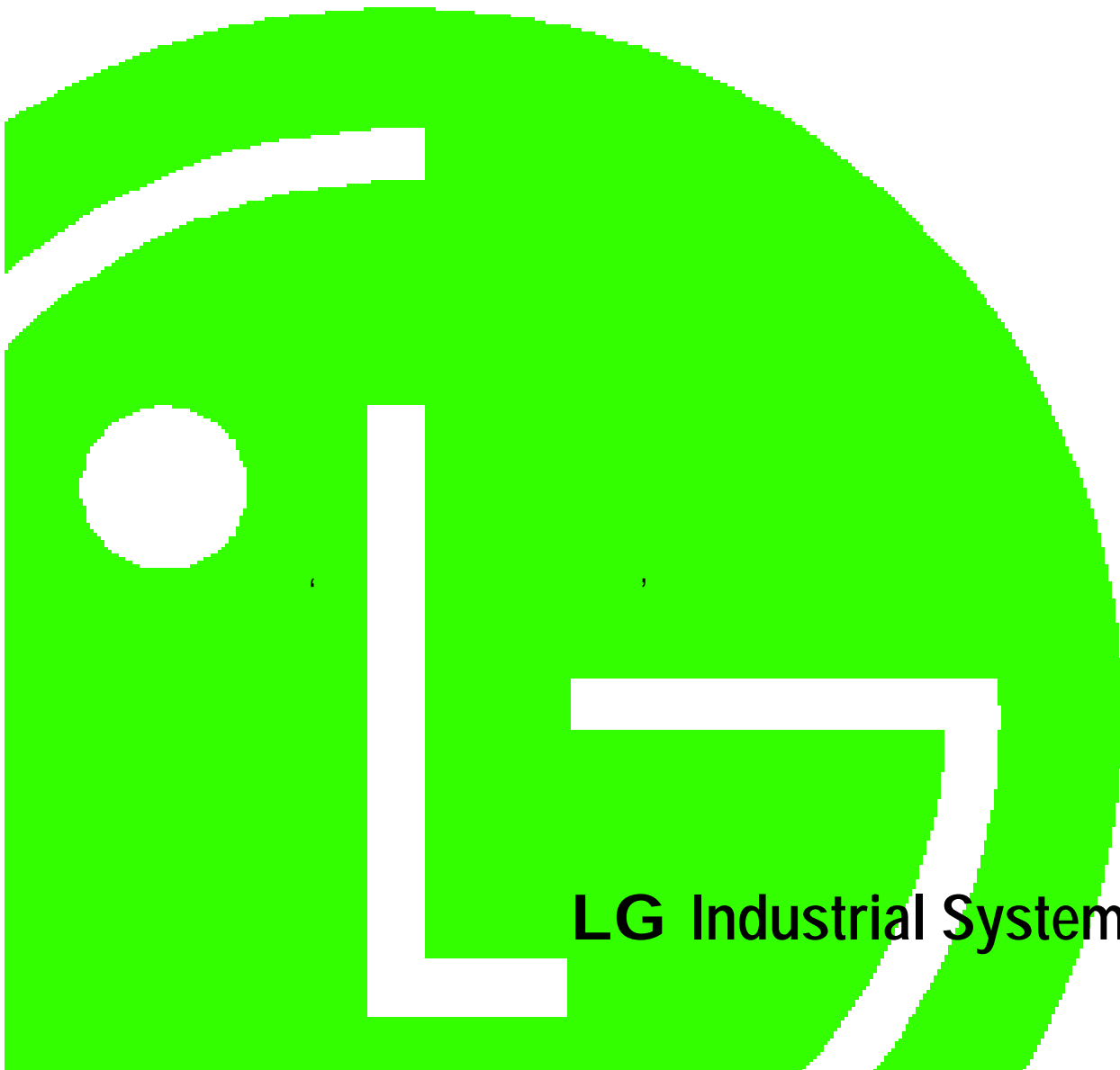




LG

GLOFA

GM4



LG Industrial Systems



...

.

?

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?

‘ ’ ‘ ’ 2가 ,

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가

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가

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?

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가

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가

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?

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, PLC
PLC / .

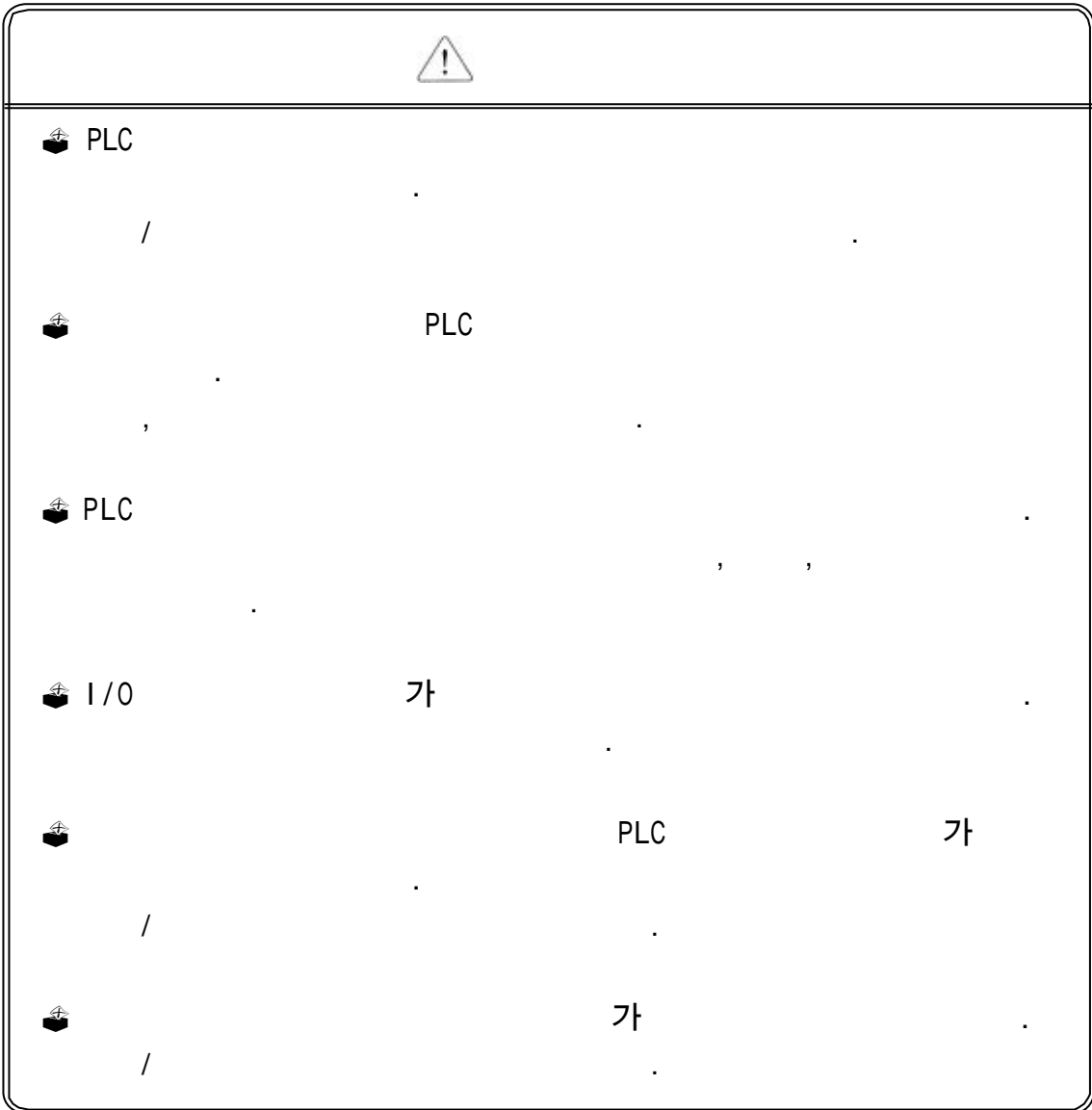
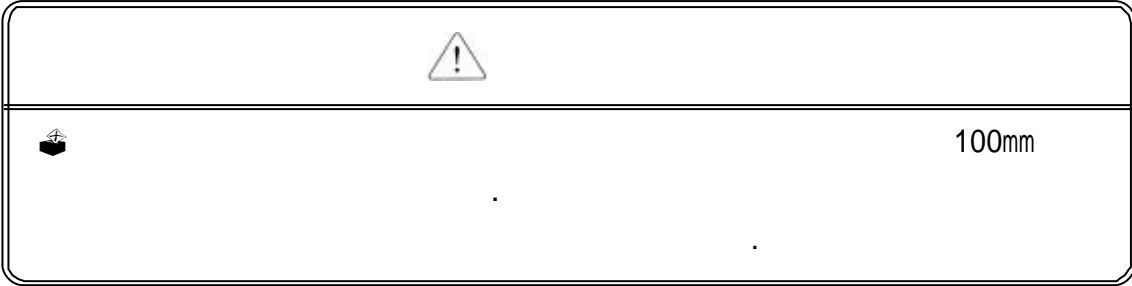
- PLC , / , /

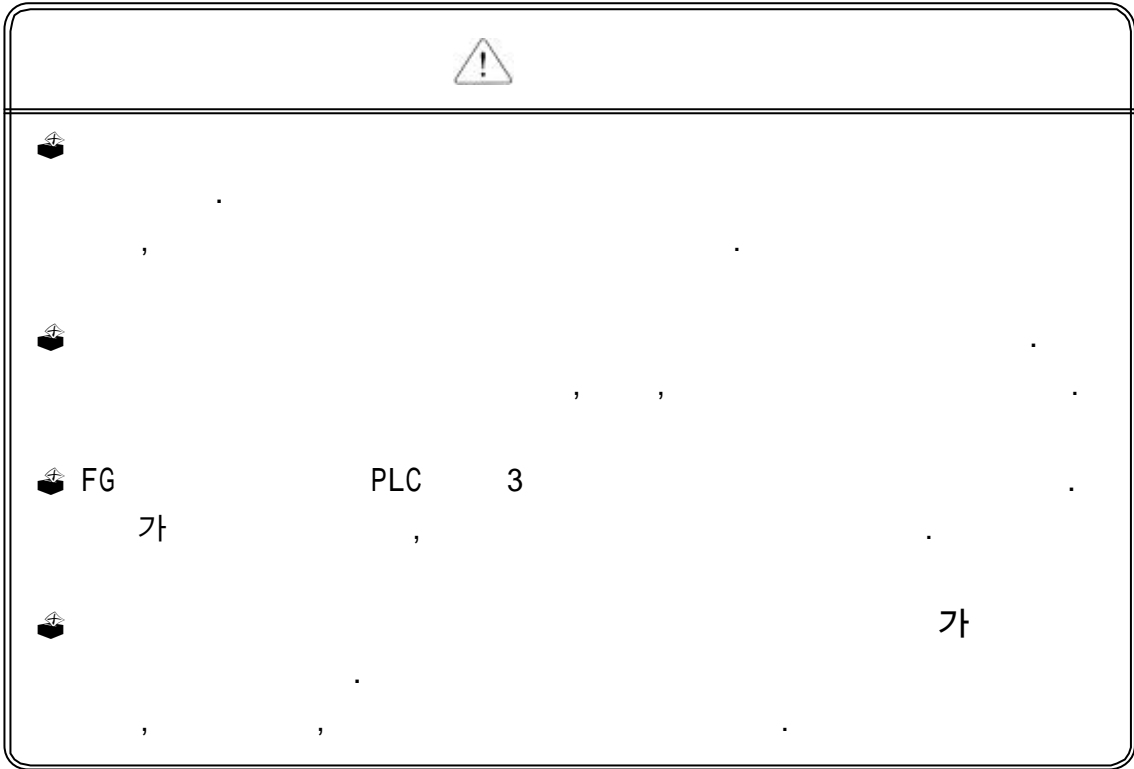
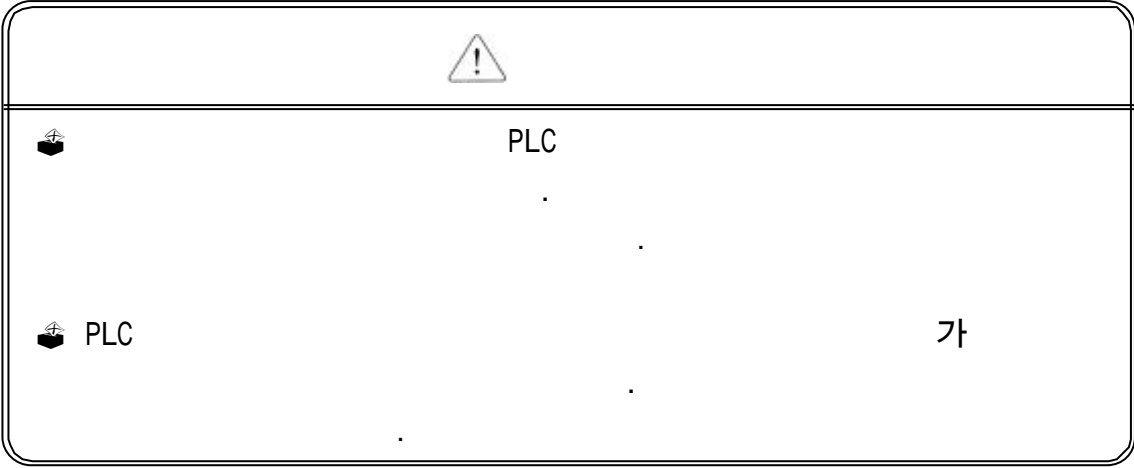
- PLC CPU가 , Off ,
CPU가 , TR On

가

PLC On


가 PLC ,
PLC ()









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 가 .


..


 , PLC Off

.


 , , 가 , Short ,

, , .




 PCB .


,


 PLC Off

.

 PLC 30cm

.



 , .

,

.



1 1-1 ~ 14

- 1.1 1-1
- 1.2 1-2
- 1.3 1-4

2 2-1 ~ 212

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- 2.2 2-2
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- 2.3 2-6
- 2.3.1 2-6
- 2.3.2 2-10
- 2.3.3 2-11

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---	-----

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5.3	5-1

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 12.4.2 12-10

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. 1-1 ~ 4-2

1 1-1

2 2-1

3 / 3-1

4 4-1

1.1

GLOFA-GM4

PLC

☞

No.		
1		
2		GLOFA-GM4
3		GM4
4	CPU	CPU ☞
5		CPU
6		
7		
8		
9	☞	
10		PLC
11	☞	PLC 가
12		
1		
2		
3	/	/
4		CPU,

1) /

1.2

1) GLOFA-GM

- (1) (IEC61131-3)
-
- IEC61131-3 (IL / LD / SFC)
- (2)
- (3)
- (4) PLC

2) GM4-CPUA 가 CPU

- (1) 0.2?s/Step
- (2)
- (3) , , 가
- (4) PLC
-
-
-
-
- (5) 가
- (6)
- (7) (23 59 59) PLC 가
- (8) GLOFA PLC CPU 가 가 ? 가 ?

1.3

(Module)	가)CPU ,
(Unit)	PLC , 가 PLC) ,
PLC (PLC System)	PLC 가 가	
(Cold Restart)	(가) PLC	
(Warm Restart)	Off , Off 가 가	
(Hot Restart)	Off 가 PLC	
	CPU	
(Watchdog Timer)	가	
(Function)	4 ,	
(Function Block)	,	

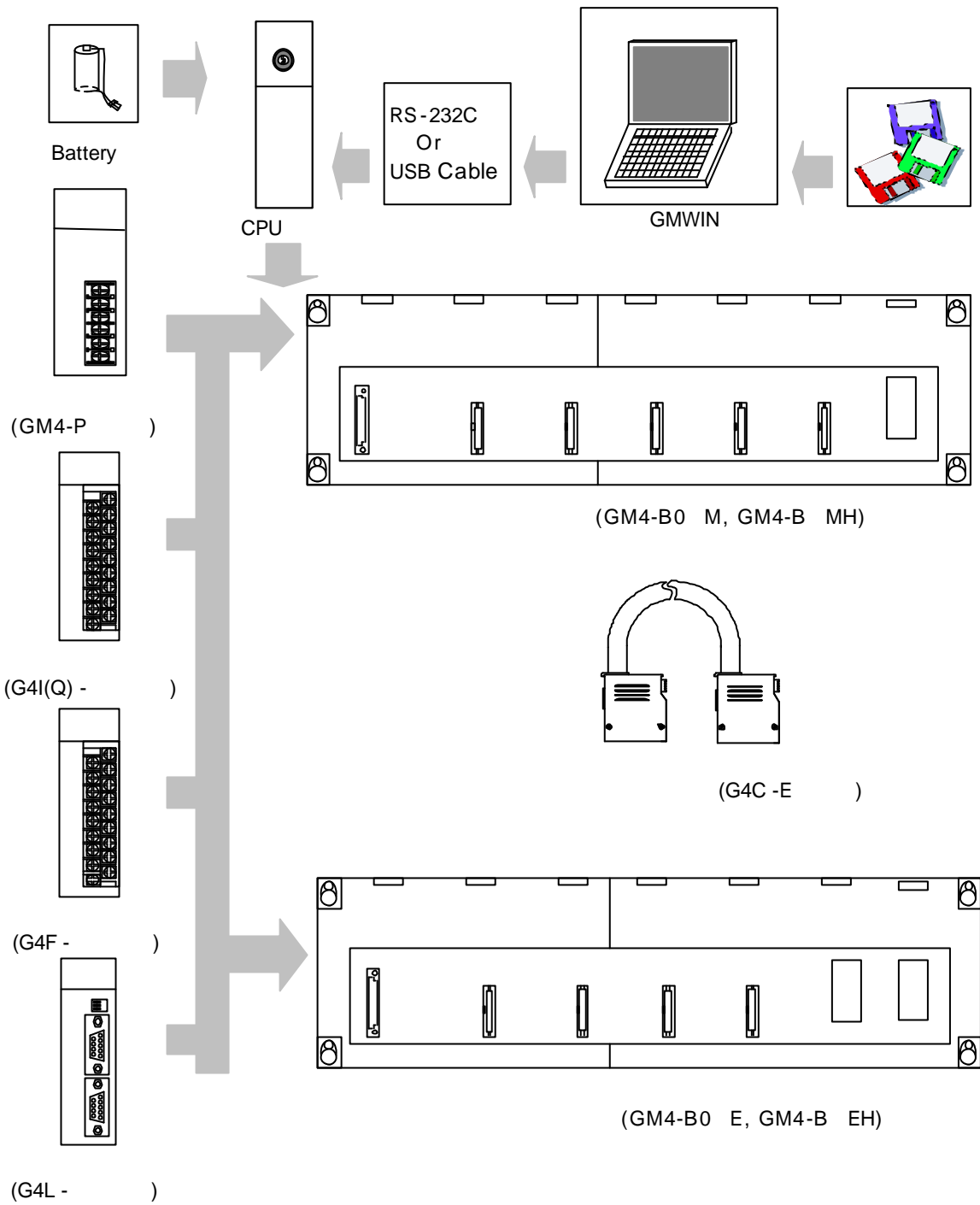
		I, Q, M) %IX0.0.2 %QW1.2.1 %MD1234
	가 , =%IX0.0.2, 'RESULT'=%MD1234 %MD1234 'INPUT_0' 'RESULT'	'INPUT_0' %IX0.0.2	
GMWIN	GM	GLOFA-	
FAM	Factory Automation Monitoring S/W	S/W	
(Task)	3 가		
RTC	Real Time Clock	IC	
(Sink)	가 On PLC 가		Z :
(Source)	가 On PLC 가		
	PLC On 가		

	<p>PLC On 가</p>	
Fnet	Fieldbus Network ()	
Cnet	Computer Network ()	
Enet	Ethernet Network ()	
Mnet	Mini-MAP Network ()	
Dnet	DeviceNet Network ()	
Pnet	Profibus Network ()	
ISA	Instrument Society of America	
GM4C	<p>GM4-CPUC (GM4-B4MH, GM4-B6MH, GM4-B8MH, GM4-B4EH, GM4-B6EH, GM4-B8EH)가 (2)</p>	

GLOFA-GM4

2.1

GLOFA-GM4



2.2

GLOFA-GM4

2.2.1 GM4

CPU	GM4-CPUA	?		: 2,048
	GM4-CPUB	?		: 2,048
	GM4-CPUC	?		: 3,584
	G4I-D22A	? DC12/24V	16	(/)
	G4I-D22B	? DC12/24V	16	()
	G4I-D22C	? DC24V	16	(/)
	G4I-D24A	? DC12/24V	32	(/)
	G4I-D24C	? DC24V	32	(/)
	G4I-D28A	? DC12/24V	64	(/)
	G4I-D24B	? DC12/24V	32	()
	G4I-A12A	? AC110V	16	
	G4I-A22A	? AC220V	16	
	G4Q-RY2A	?	16	(2A)
	G4Q-TR2A	?	16	(0.5A ,)
	G4Q-TR2B	?	16	(0.5A ,)
	G4Q-TR4A	?	32	(0.1A ,)
	G4Q-TR8A	?	64	(0.1A ,)
	G4Q-TR4B	?	32	(0.1A ,)
	G4Q-SS2A	?	16	(1A)
	G4Q-SS2B	?	16	(0.6A)
	G4H-DR2A	? DC12/24V ?	8 8	(/) (2A)
	G4H-DT2A	? DC12/24V ?	8 16	(/) (0.5A ,)
	GM4-B04M	? 4		
	GM4-B4MH	? 4	()	
	GM4-B06M	? 6		

()	GM4-B6MH	? 4	()	
	GM4-B08M	? 4		
	GM4-B8MH	? 4	()	
	GM4-B12M	? 4		
	GM4-B04E	? 4		
	GM4-B4EH	? 4	()	
	GM4-B06E	? 6		
	GM4-B6EH	? 6	()	
	GM4-B08E	? 8		
	GM4-B8EH	? 8	()	
	GM4-M032	? (32Kstep)		GM4-CPUA
	GM4-PA1A	AC110V	? DC5V : 4A, DC24V : 0.7A	
	GM4-PA2A	AC220V		
	GM4-PA1B	AC110V	? DC5V : 3A, DC24V : 0.5A	
	GM4-PA2B	AC220V		
	GM4-PA2C	AC220V	? DC5V : 8A	
	GM4-PD3A	DC24V	? DC5V : 4A	
	G4C-E041	? : 0.4 m		
	G4C-E061	? : 0.6 m		
	G4C-E121	? : 1.2 m		
	G4C-E301	? : 3 m		
	G4C-E601	? : 6 m		
	G4C-E102	? : 10 m		
	G4C-E152	? : 15 m		
A/D	G4F-AD2A	? / : 4 ? DC ?5 ~ +5V / ?10 ~ +10V / DC?20 ~ + 20mA		
	G4F-AD3A	? / : 8 ? DC 1~ 5V / 0 ~ 10V / DC 4 ~ 20mA		
D/A	G4F-DA1A	? / : 2 ? DC ?10 ~ +10V / DC 4 ~ 20mA		
	G4F-DA2V	? : 4 ? DC ?10 ~ +10V		

()	D/A ()	G4F-DA2I	? : 4 ? DC 4 ~ 20mA	
		G4F-DA3V	? : 8 ? DC ?10 ~ +10V	
		G4F-DA3I	? : 8 ? DC 4 ~ 20mA	
		G4F-HSCA	? : 0 ~ 16,777,215 ? 50KHz, 1	(24)
		G4F-H01A	? : -2,147,483,648 ~ 2,147,483,647 ? 200KHz, 2	(32)
		G4F-HD1A	? : -2,147,483,648 ~ 2,147,483,647 ? 500KHz, 2	(32)
		G4F-POPA	? , 1	
		G4F-POPB	? , 2	
		G4F-PPxO	? (Open Collector), x	x=1,2,3
		G4F-PP1D	? (Line Driver), x	x=1,2,3
		G4F-TC2A	? : 7 (K, J, E, T, B, R, S) ? : 4	
		G4F-RD2A	? : Pt 100, Jpt 100 ? : 4	
	PID	G4F-PIDA	? 8	
		G4F-PIDB	? 8 /	
		G4F-AT3A	? : 8 ? : 0.1 ~ 1.0 , 1 ~ 10 10 ~ 60 , 60 ~ 600	1 가
	G4F-TMCA	? 2 (/ , ,) ? 2 ? PID		
	G4F-INTA	? : 8		
	Enet I/F	G4L-EUTB	?10/100BASE-TX, 5	
		G4L-EUFB	?100BASE-FX, Fiber Optic	
		G4L-EU5B	?10BASE-5, AUI	
	Enet I/F	G4L-EUTC	?10/100BASE-TX, 5	Master
		G4L-EUFC	?10BASE-FX, Fiber Optic	
		G4L-EU5C	?10BASE-5, AUI	
		G4L-ERTC	?10/100BASE-TX, 5	Slave
		G4L-ERFC	?10BASE-FX, Fiber Optic	
		G4L-ER5C	?10BASE-5, AUI	

()	Fnet I / F	G4L-FUEA	? Fnet I / F ? 1 Mbps ?	
		GOL-FUEA		
	Fnet I / F	G4L-RBEA	? Fnet I / F ?	
	I / F	GOL-SMIA	? DC12/24V 16	
		GOL-SMQA	? 16 (1A)	
		GOL-SMHA	? DC12/24V 8 ? 8 (1A)	
		GOL-AD3A	? / : 8 ? DC 1 ~ 5V / 0 ~ 10V / DC 4 ~ 20mA	
		GOL-DA3I	? : 8 ? DC 4 ~ 20mA	
		GOL-FREA	? Fnet	
		GOL-FOEA	? ?	
		GOL-FAPA	?	
		GOL-FABA	?	
		GOL-FACA	?	
		GOL-FADA	?	
	Enet I/F	G4L-EUEA	? IEEE 802.3 ? 10 Base 5 / 10 Base T	
	Cnet I/F	G4L-CUEA	? RS-232C / RS-422 : 1	
	Dnet I/F	G4L-DUEA	? Dnet I/F ? ODVA (Open DeviceNet Vendor Association)2.0	
		GOL-DSIA	? Dnet I/F ? DC12/24V 16 ? ODVA (Open DeviceNet Vendor Association)2.0	
		GOL-DSQA	? Dnet I/F ? 16 ? ODVA (Open DeviceNet Vendor Association)2.0	
	Profibus-DP	G4L-PUEA	? Profibus-DP (I/O 1K)	
G4L-PUEB		? Profibus-DP (I/O 7K)		
	G4S-SW16	? GM4 16		
	GM4-DMMA	?		

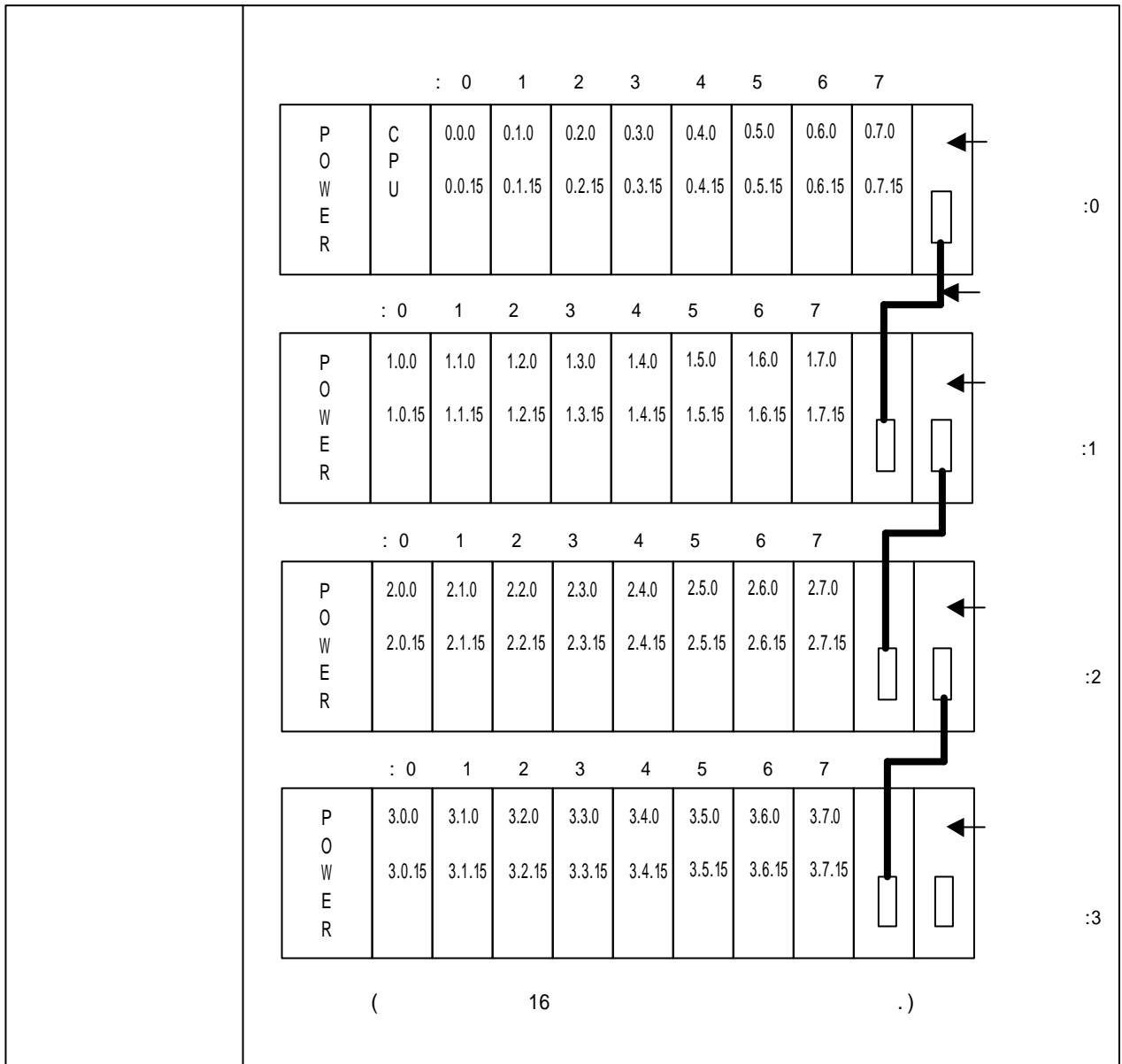
2.3

CPU / , Cnet I/F (G4L-CUEA)
Cnet I/F PLC

2.3.1

1)

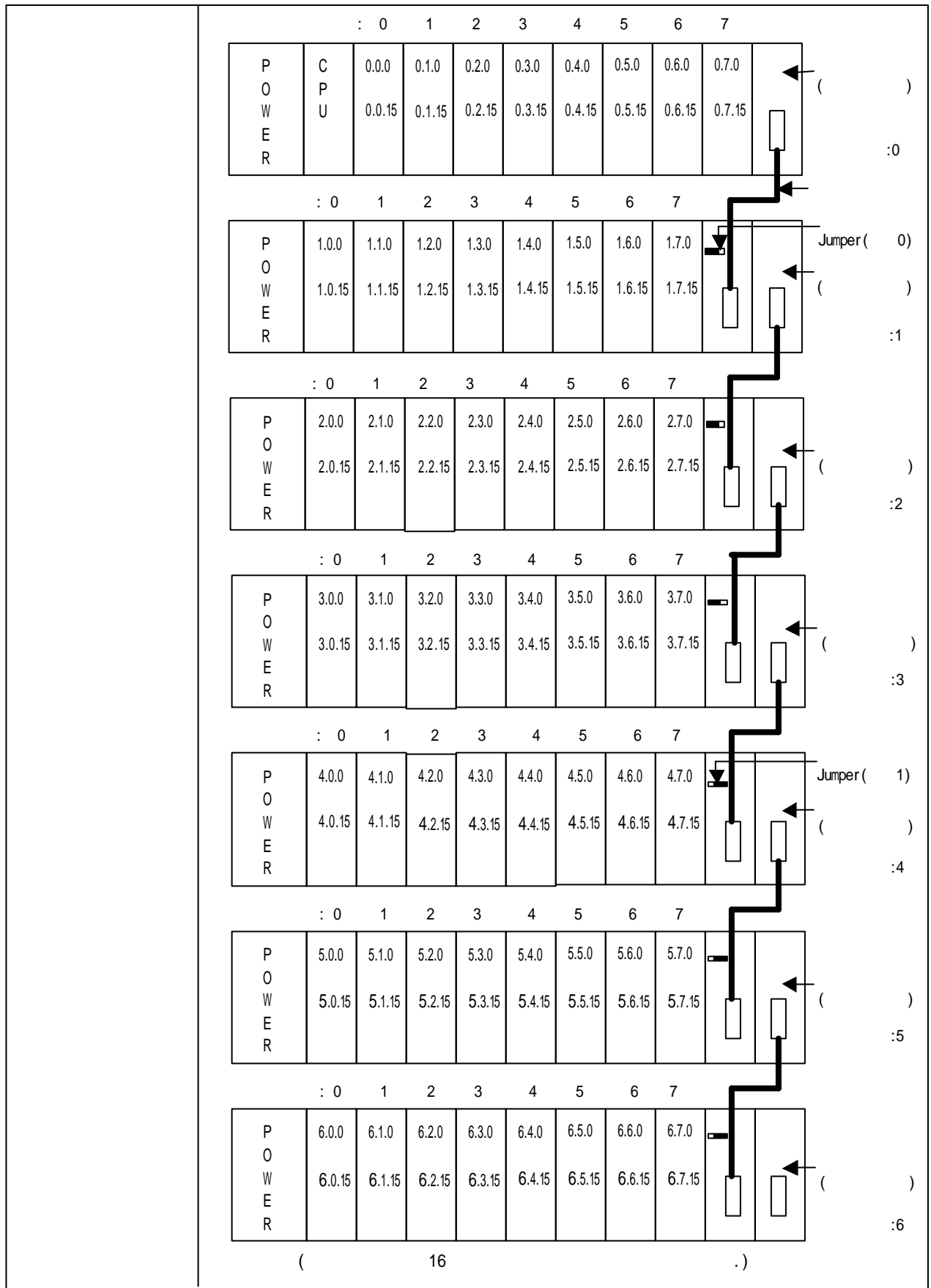
. GM4 CPU (GM4-CPUA, GM4-CPUB, GM4-CPUC) 가



2) GM4C
CPU GM4 -CPUC
B4EH/B6EH/B8EH)

(GM4-B4MH/B6MH/B8MH)

(GM4-



2.3.2 (Cnet I/F)

Cnet I/F Cnet I/F RS-232C, RS-422 (RS-485) . Cnet I/F GM4
 G4L-CUEA 가 CPU .
 Cnet I/F Cnet I/F

1) Cnet I/F 가 CPU

	Cnet	가
GM4 -CPUA	4	×
GM4 -CPUB	4	
GM4 -CPUC	8	

2.3.3

GLOFA-GM4

IEC / ISA
가 CPU

Fnet

. Fnet

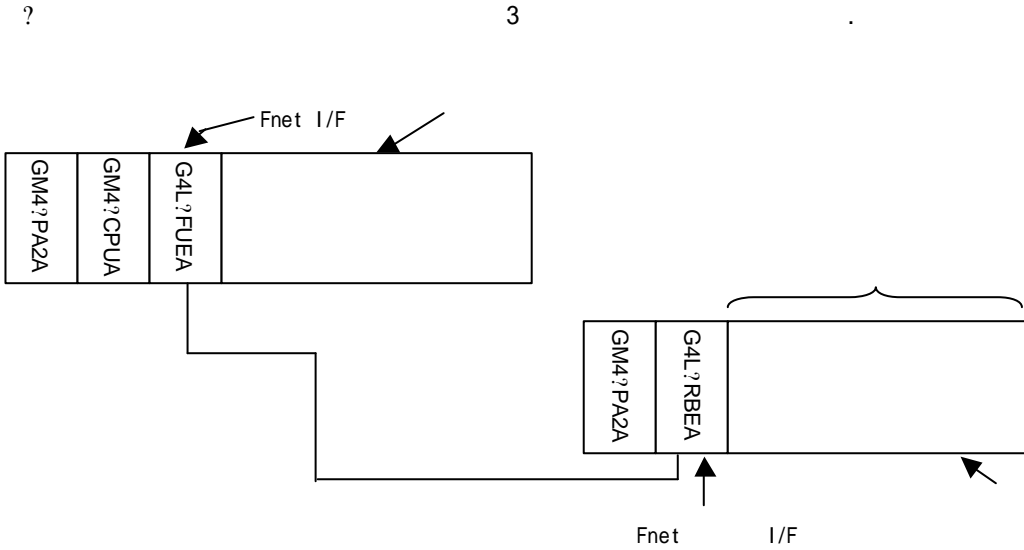
가

Enet I/F

, Dnet I/F

Profibus I/F

1)



2) Fnet I/F

		GM4
	PID	G4F-PIDA/ PIDB
		G4F-TMCA
		G4F-POPA/POPB
		G4F-PP10/20/30
		G4F-PP1D/2D/3D
		G4F-AT3A
	Fnet I/F	G4L-FUEA
	Cnet I/F	G4L-CUEA
	Dnet I/F	G4L-DUEA
	Pnet I/F	G4L-PUEA
		G4L-PUEB
	Enet I/F	G4L-EUEA
Rnet I/F	G4L-RUEA	

3) Fnet I/F

가

CPU

	Fnet	가
GM4-CPUA	2	×
GM4-CPUB	4	
GM4-CPUC	8	

4)

(1)

(2)

(3)

가
 GM4C : 32,000 (64)
 %IX0.0.0 ~ %IX63.7.63, %QX00.0 ~ %QX63.7.63

5)

?

32 ' 12 %QX12.0.0, '32 ' 12
 %IX12.0.0,
 가

6)

?

'64 ' 12 %IX0.0.0
 ~ %IX0.7.63, %QX0.0.0 ~ %QX0.7.63 , 12 %IX3.0.0
 ~ %IX3.7.63, %QX3.0.0 ~ %QX3.7.63

1)			
2)	(%IW, %QW)	On/Off	가
	(%MW)	가	

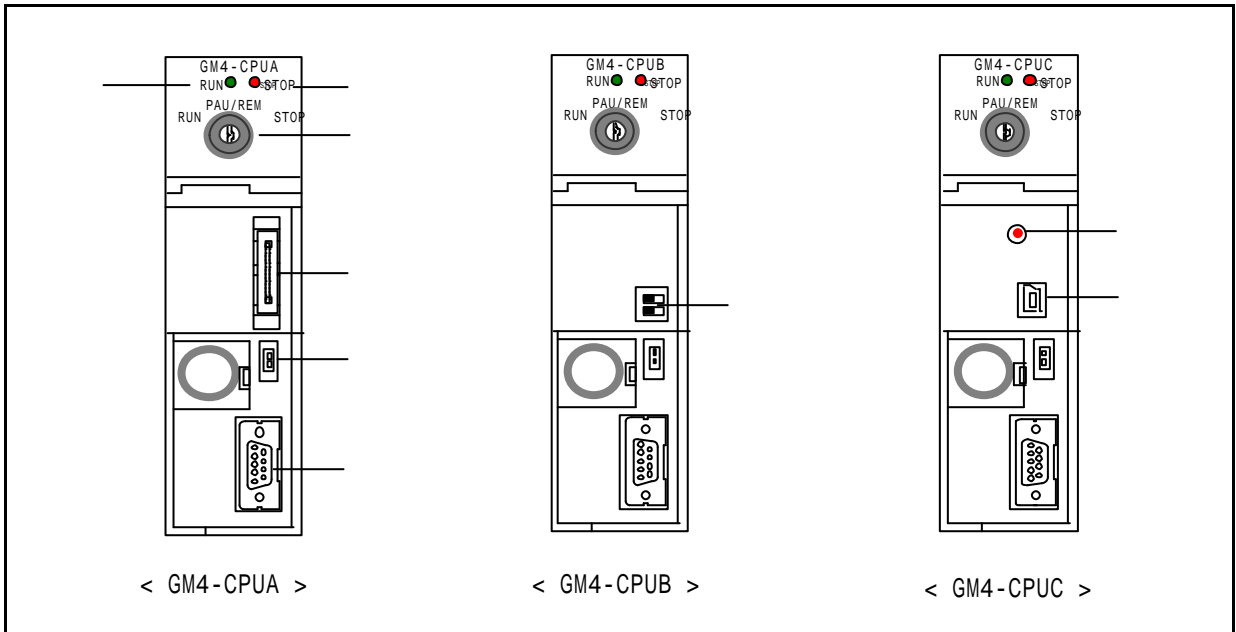
3.1

GLOFA?GM

No.					
1	0 ~ 55°C				
2	?25 ~ ?70 °C				
3	5 ~ 95%RH,				
4	5 ~ 95%RH,				
5				-	X, Y, Z 10 IEC 61131-2
	가				
	10 ? f ? 57Hz	?	0.075mm		
	57 ? f ? 150Hz	9.8m/s ² {1G}	?		
	가				
	10 ? f ? 57Hz	?	0.035mm		
		57 ? f ? 150Hz	4.9m/s ² {0.5G}	?	
6	? 가 : 147 m/s ² {15G} ? 가 : 11ms ? : (X, Y, Z 3 3)				IEC 61131-2
7	? 1,500 V				LG
	: 4kV ()				IEC 61131-2, IEC 1000-4-2
	27 ~ 500 MHz, 10 V/m				IEC 61131-2, IEC 1000-4-3
	/		(24V)	(24V)	IEC 61131-2 IEC 1000-4-4
		2kV	1kV	0.25kV	
8	가 , 가				
9	2,000m				
10	2				
11					

1) IEC(International Electrotechnical Commission :)
 :
 2)
 :
 2 ,

4.2



No.		
1	RUN LED	CPU On : 가 RUN Off : LED 가 Off CPU 가 STOP PAU / REM
2	STOP LED	On : 가 STOP Off : LED 가 Off 가 RUN PAUSE 가 RUN / PAUSE / DEBUG :
3		CPU RUN : STOP : PAU / REM : PAUSE : REMOTE :
4		PLC (GM4-CPUC)
5	RS?232C	(GMWIN)
6		CPU GM4-CPUB/GM4-CPUC
7		
8		(6.4):GM4-CPUB
9	USB	GMWIN

*3 : GM4-CPUC

5 ()	
5 ()	STOP LED 가 ,

LED

1) LED

	LED		
	Run	Stop	Remote
Run	On	Off	Off
Stop	Off	On	Off
Pause	Off	Off	Off
Run	On	Off	On
Stop	Off	On	On
Pause, Debug	Off	Off	On

2)

STOP	PAU / REM	Stop
PAU / REM	RUN	Run
RUN	PAU / REM	Pause
PAU / REM	STOP	Stop

? 가 Stop 가 .

4.3

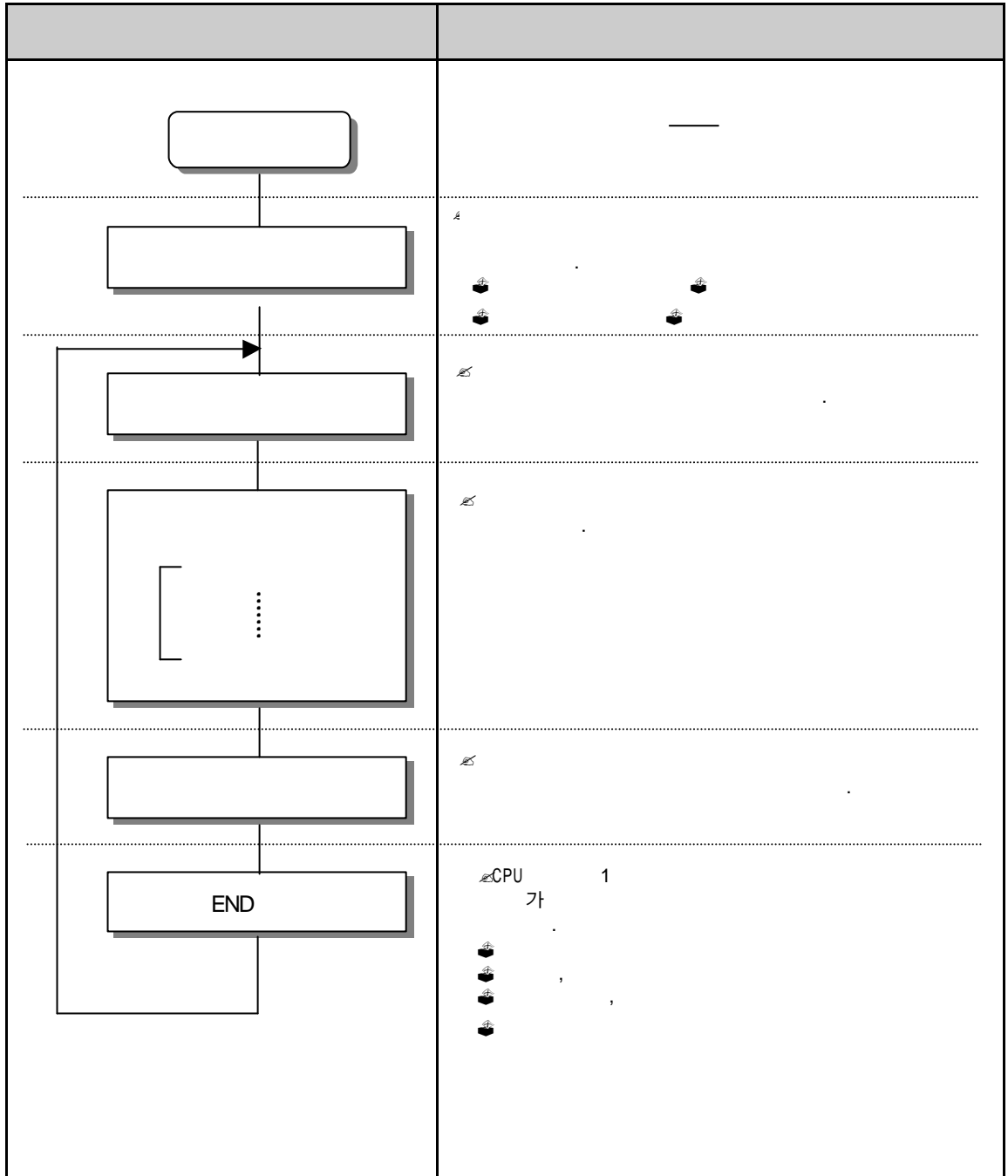
4.3.1

1)

PLC

PLC

가



4.3.3 (Scan Time)

0 0

1)

가

PLC

$$(1) \frac{\text{PLC}}{\text{PLC}} = \frac{\text{PLC}}{\text{PLC}} + \text{PLC}$$

$$\frac{\text{PLC}}{\text{PLC}} = \frac{\text{PLC}}{\text{PLC}} + \text{PLC}$$

(2)

가

2)

(1)

~~_SCAN_MAX~~ : (1ms)
~~_SCAN_MIN~~ : (1ms)
~~_SCAN_CUR~~ : (1ms)

(2)

(GM4-CPUC)

GMWIN

('_STSK_MAX, _STSK_MIN, _STSK_CUR' , '_STSK_NUM' , 2)

3)

(GM4-CPUC)

가

? : T_SLOW (:= T#10ms, := 0, := 0)
 PROC_1 (:= %MX0, := 3, := 48)
 E_INT1 (:= 0, := 2, := 32)

? : --> P0
 --> P1 (T_SLOW)
 --> P2 (PROC_1)
 --> P3 (E_INT1)

(1)

GMWIN

(P0)

(_SCAN_MAX)

(2)

, GMWIN , '_STSK_NUM' , '_STSK_NUM' , '_STSK_NUM' , '_STSK_MAX' , '_STSK_MIN' , '_STSK_CUR' , '0' , '_STSK_MAX'

- (3) , '_STSK_NUM' '48' , GMWIN
'_STSK_MAX' .
- (4) , '_STSK_NUM' '32' On
'_STSK_MAX' .
- (5) (2) ~ (4)
가 .
- (6) Tp0 = 17 ms, Tp1 = 2 ms, Tp2 = 7 ms, Tp3 = 2 ms
= 2 ms 가 24
ms (Tp0 + Tp2)가 가 28 ms(Tp0 + Tp2 + Tp1
x 2)가 . 가 30 ms(Tp0 + Tp2 + Tp1 x 2
+ Tp3)가 , 가 32 ms(Tp0
+ Tp2 + Tp1 x 3 + Tp3)가 .
가 32 ms
- (4.4.3 '8) , .)

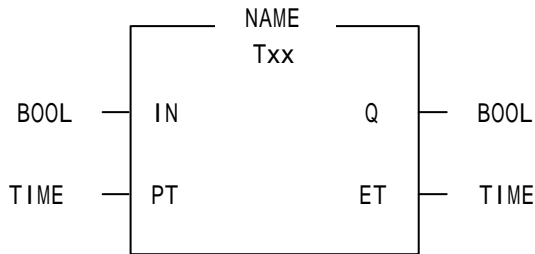
4.3.4 (Scan Watchdog Timer)

- 1) . (GMWIN .)
- 2) PLC Off , .
- 3) Watchdog Time) 가 'WDT_RST' . 'WDT_RST' (Scan
0 .
- 4) , STOP

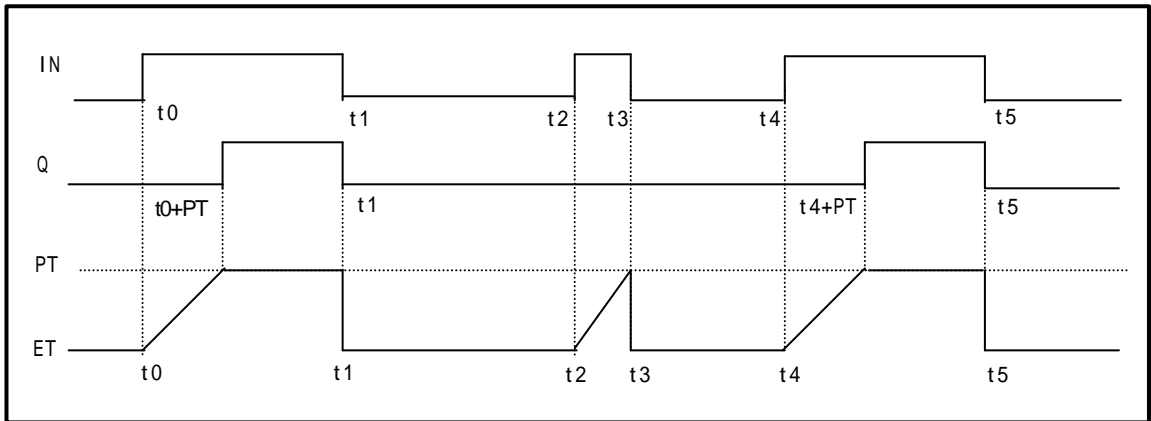
1)		1 ~ 65,535ms (1ms) .

4.3.5

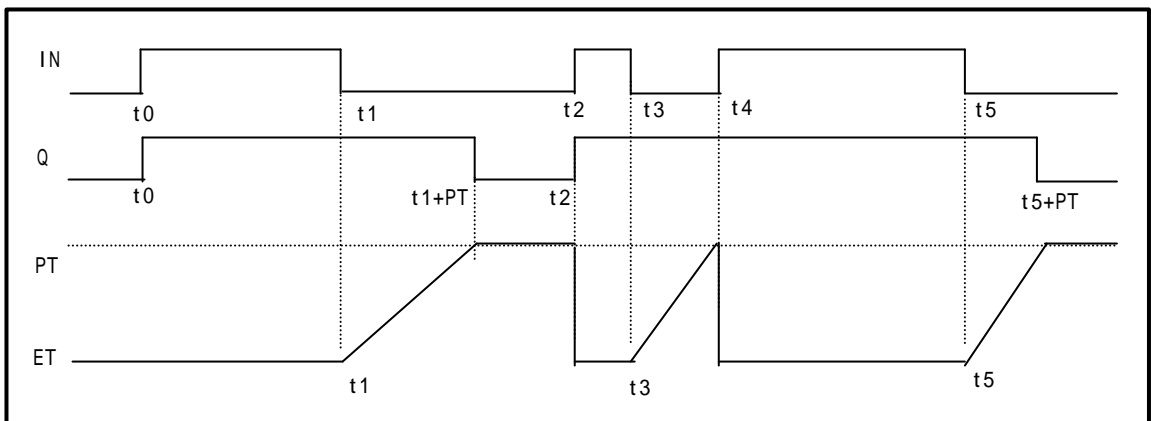
CPU (TON), Off (TOF), 가 가 On
 1ms 0.001 ~ 4,294,967.295 (1,193)
 'GLOFA?GM



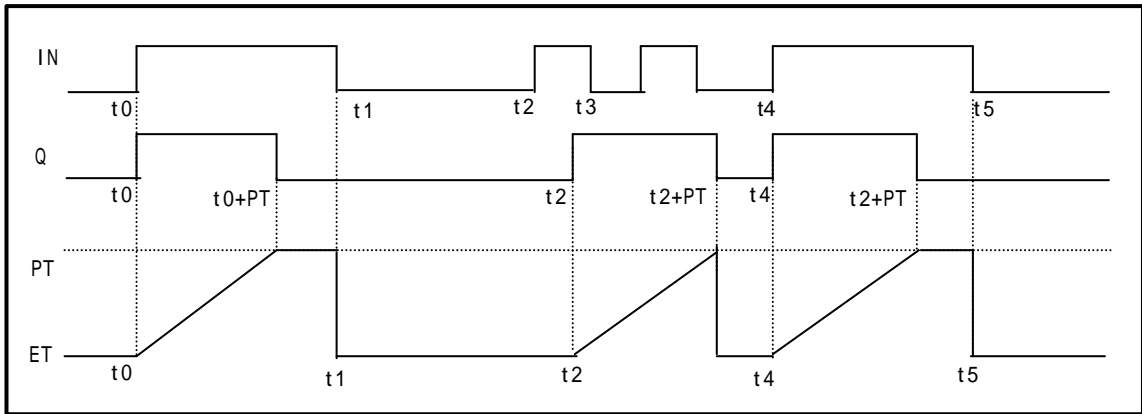
1) On On/Off
 (TON) (Q) On
 (=)



2) Off On/Off
 On (Q) On Off
 (TOF) (Q) Off Off
 =)



3) On/Off
 On (Q) On
 Off (TP) Off
 Off (Q) Off
 Off Off



4) _____ ' 1 + _____

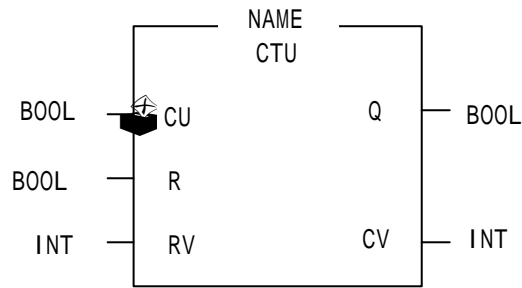
4.3.6

CPU 가 (CTU), (Off On) (CTD), 가 (CTUD) 3 가
 'GLOFA?GM',
 ? 가 가 가
 ?
 ? 가 2

1) On/Off

(1) 가

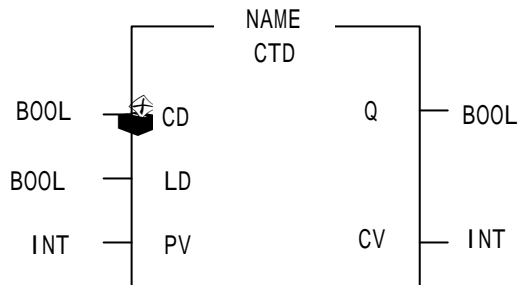
? (CU), (R) (PV)



? (CV) 가 (PV) (Q) On
 0 (Q) Off

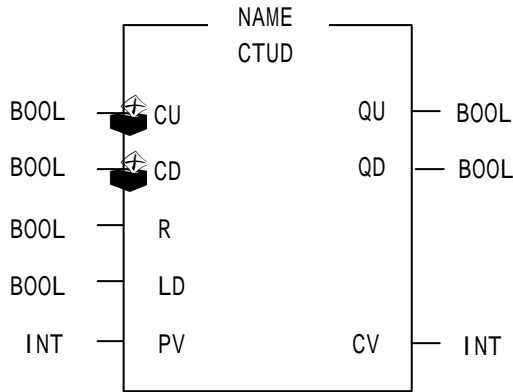
(2)

? (CD), (LD) (PV)



? LD 0 가 (Q) On
 (Q) Off

(3) 가 ? 2 , LD (PV)



? 0
 ? LD
 ? CU (CV) 1 가, CD (CV) 1
 (CV) (PV) QU 가 On , (CV) 0
 QD 가 On

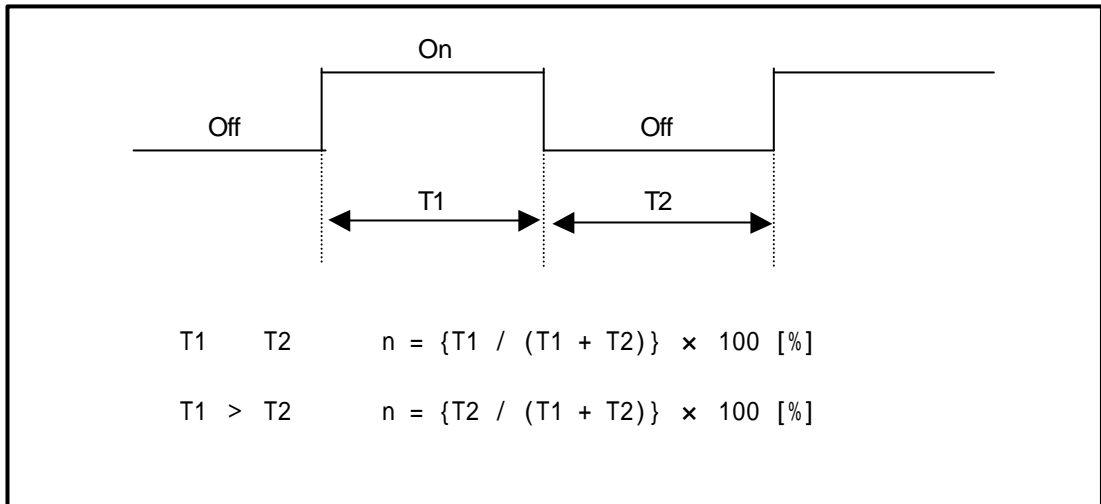
2)

? 가 가 , On Off

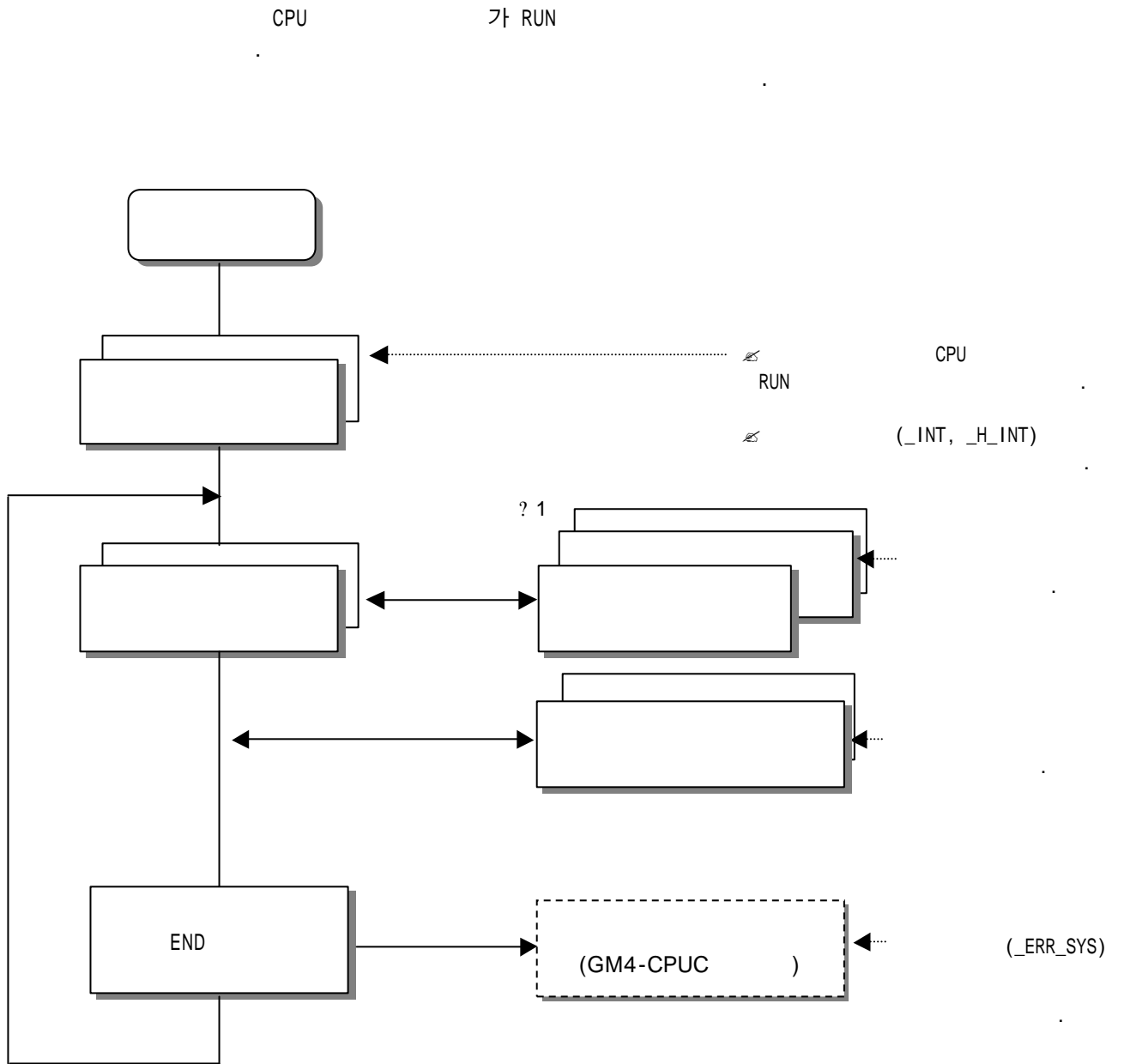
$$C_{max} = \frac{n}{100} \times X - \frac{1}{ts} [/s]$$

n : (%)
 ts : [s]

? (n) On, Off (%)



4.4.2



1) ? 1 : GLOFA PLC

S/W H/W

1)

(1)

(2)



/

: _INIT

: _H_INIT

(3)

/

_INIT

_INIT_DONE 가 On)

(

(4)

_H_INIT

_INIT_DONE 가 On)

(

1

가

(Direct)

(5)

_INIT_RUN 가 On

2)

(1)

?

0

?

?

(2)

?

180

(, .)

?

?

가 가

GMWIN

3)

(1)

?

(2)

?

3

:

32

가

:

16

가

:

8

가

?

?

?

1) 4.4.3

4)

(GM4-CPUC)

?

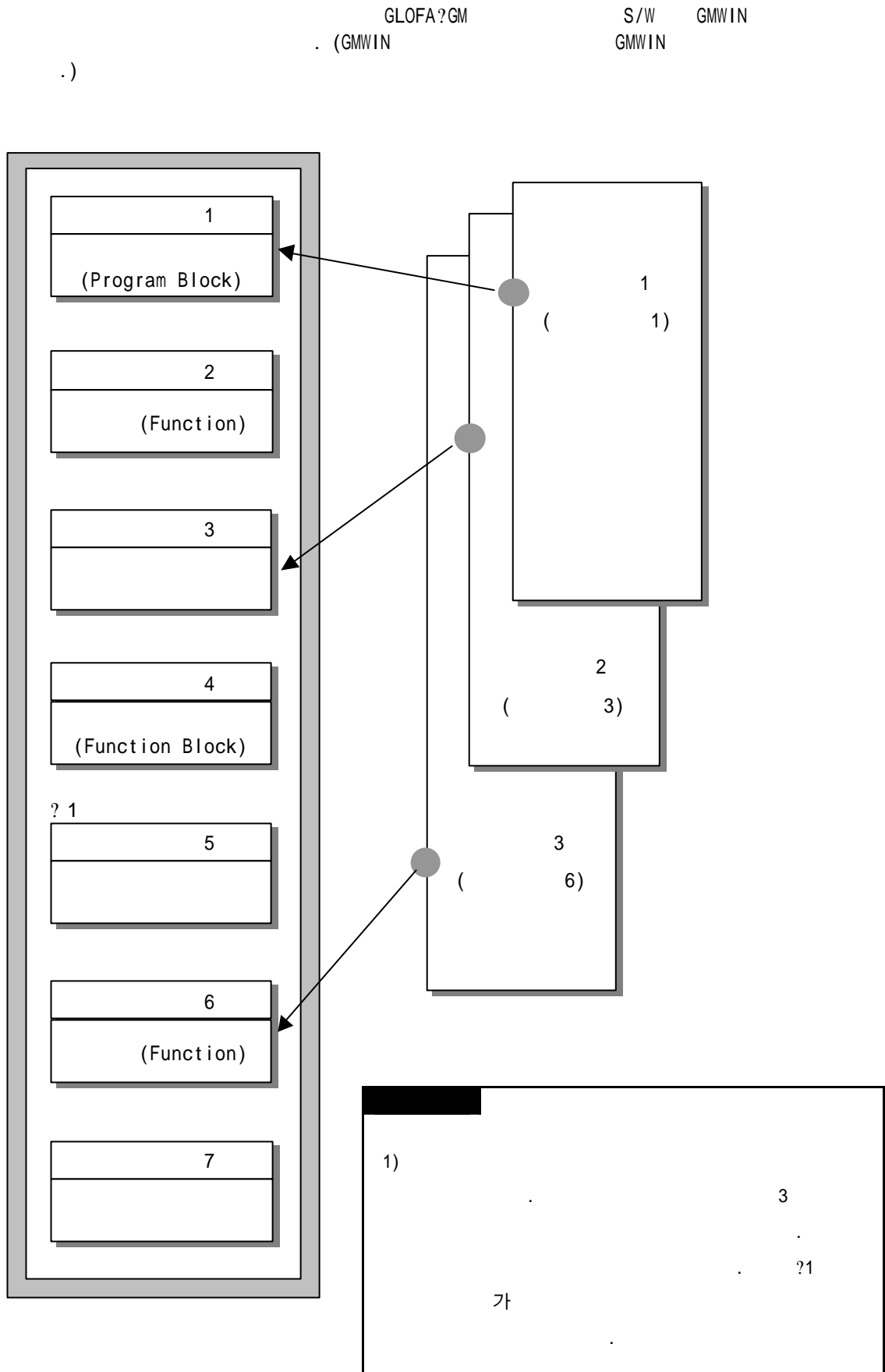
가

?

'_ERR_SYS'

(4.4.4 .)

4.4.3



1)

	()	()	()
	8 (GM4-CPUA/B) 32 (GM4-CPUC)	8	16
	(10ms 4,294,967.29 가)		(Bool) (0? 1)
	5 ms	5ms + (0.5ms)	
	0 ~ 7 (0 가 가)		
	0~31 가	32~39	48~63

2)

(1)

☞

☞

10
10 가 , 10
10 가

(2)

☞

가 가 가

☞

가 가

☞

(3)

가
가 2 , 4 , 10 , 20 4 , 20
4 가 가

4)

()

(1)

(Dip)

(2)

(3)

가 , 가 CPU

(4)

(`_TC_BMAP[n]`, `_TC_CNT[n]`)
가
(`_TASK_ERR`)
가

가 RUN PAUSE RUN , PAUSE
RUN

5)

() (I, Q, M)

(1)

(2)

CPU

가

1

(3)

☞ (,)

☞ '1' 가 (,)
Off ? On ? Off)

6)

☞ 20ms

7)

(1) 가 가? 가

(2) 가 가? 가
가 가

(3) 가? 가

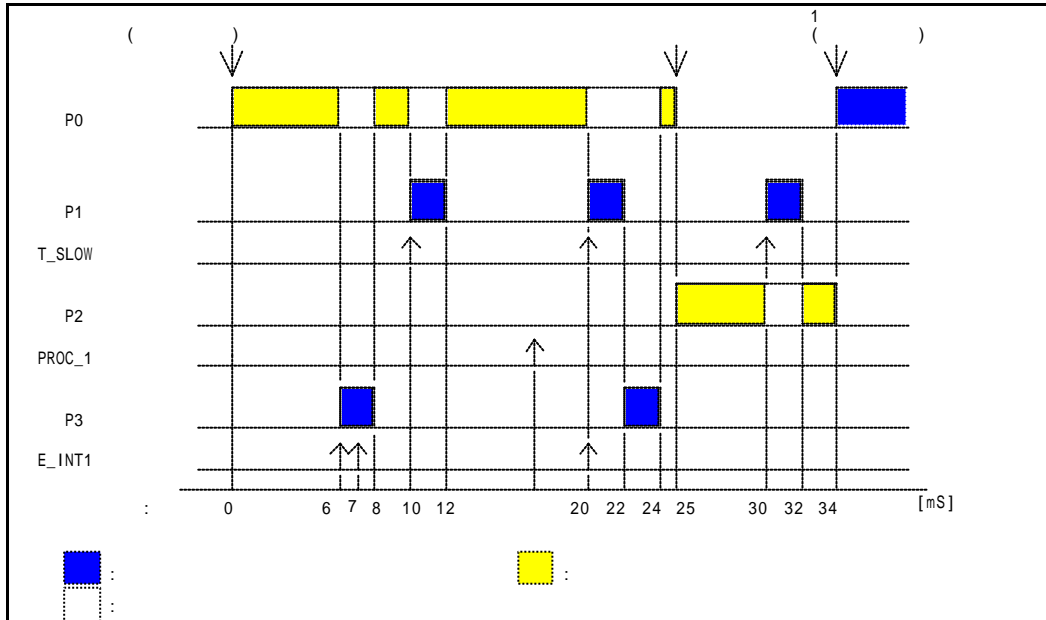
(4) 가 가? (가) 'DI', 'EI' 가

8)

```

?      : T_SLOW (           := T#10ms,           := 0 )
      PROC_1 (           := %MX0,           := 3 )
      E_INT1 (           := %IX0.0.1,           := 2 )
?      :
      --> P0
      --> P1 (           T_SLOW           )
      --> P2 (           PROC_1           )
      --> P3 (           E_INT1           )

?      : P0 = 17ms, P1 = 2ms, P2 = 7ms, P3 = 2ms
? E_INT1 :           6ms, 7 ms, 20ms
? PROC_1 :
    
```



(ms)	
0	P0
0 ~ 6	P0
6 ~ 8	P3 가 P0 P3 , 7[ms] E_INT1
8 ~ 10	P3 P0
10 ~ 12	P1 가 P0 P1
12 ~ 20	P1 P0
20	P1 P3 가 , P1 가 P1 P3
20 ~ 22	P0 P1
22 ~ 24	P1 P0 P3 가 P3
24 ~ 25	P3 P0
25	(P0) P2 P2
25 ~ 30	P2
30 ~ 32	P1 가 P2 P1
32 ~ 34	P1 P2
34	(P0)

4.4.4

1)

PLC

PLC

~~PLC~~

2)

PLC

(1) PLC

CPU

가

PLC 가

(2)

PLC

가

(3)

가

가

가

(4)

PLC

가

1)	
2)	2

4.4.5

가

1)

(1)

(2)

(

.)

GLOFA?GM

2)

1) (가)

3)

1)
2) , 'D/A
STOP

4)

~~가~~

가 가 가

5)

(1)

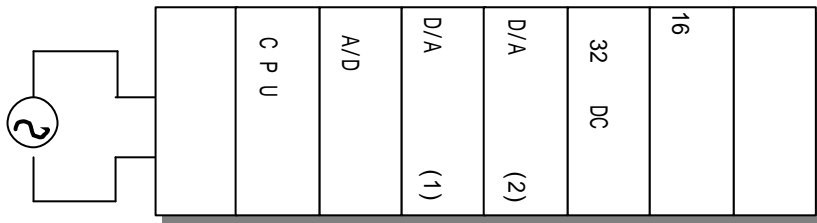
D/A

(1)

D/A

(2)

가



(6

)

~~가~~

/

/

~~가~~

D/A

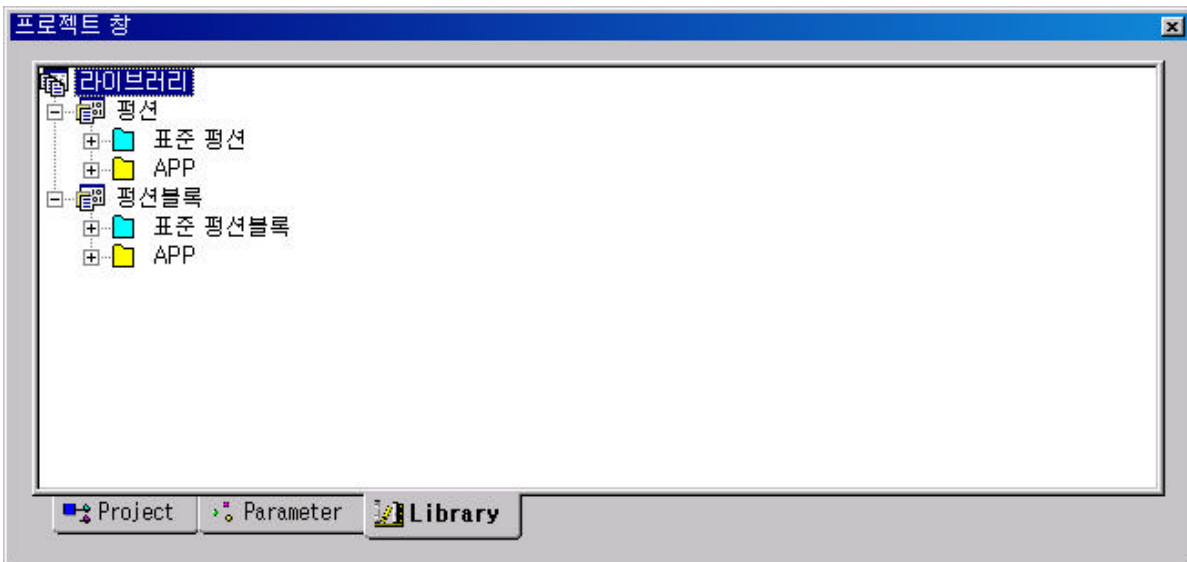
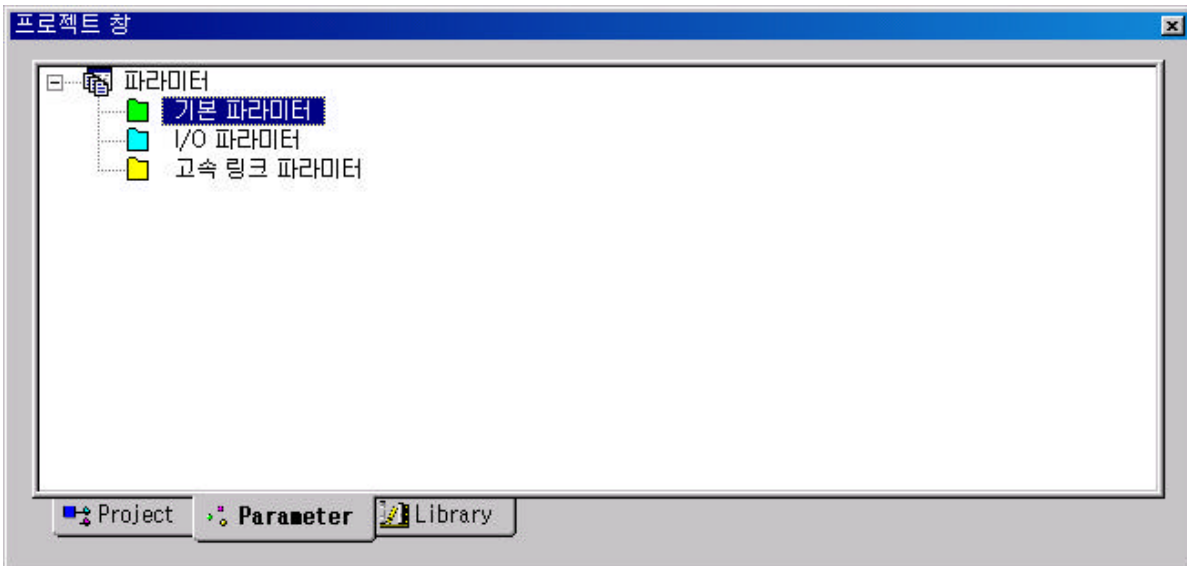
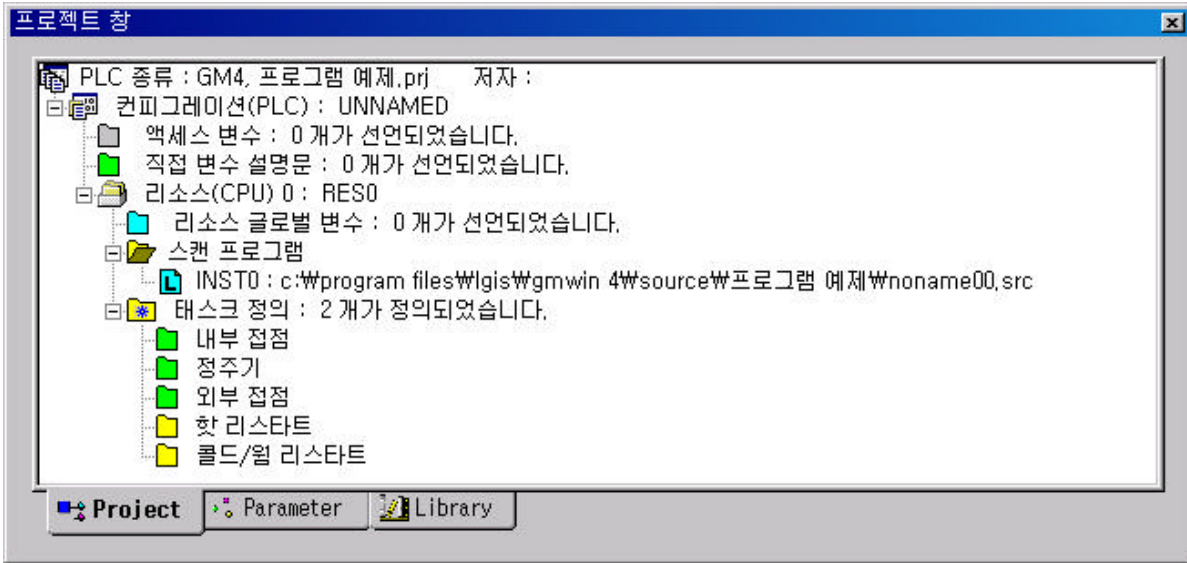
(2)

가

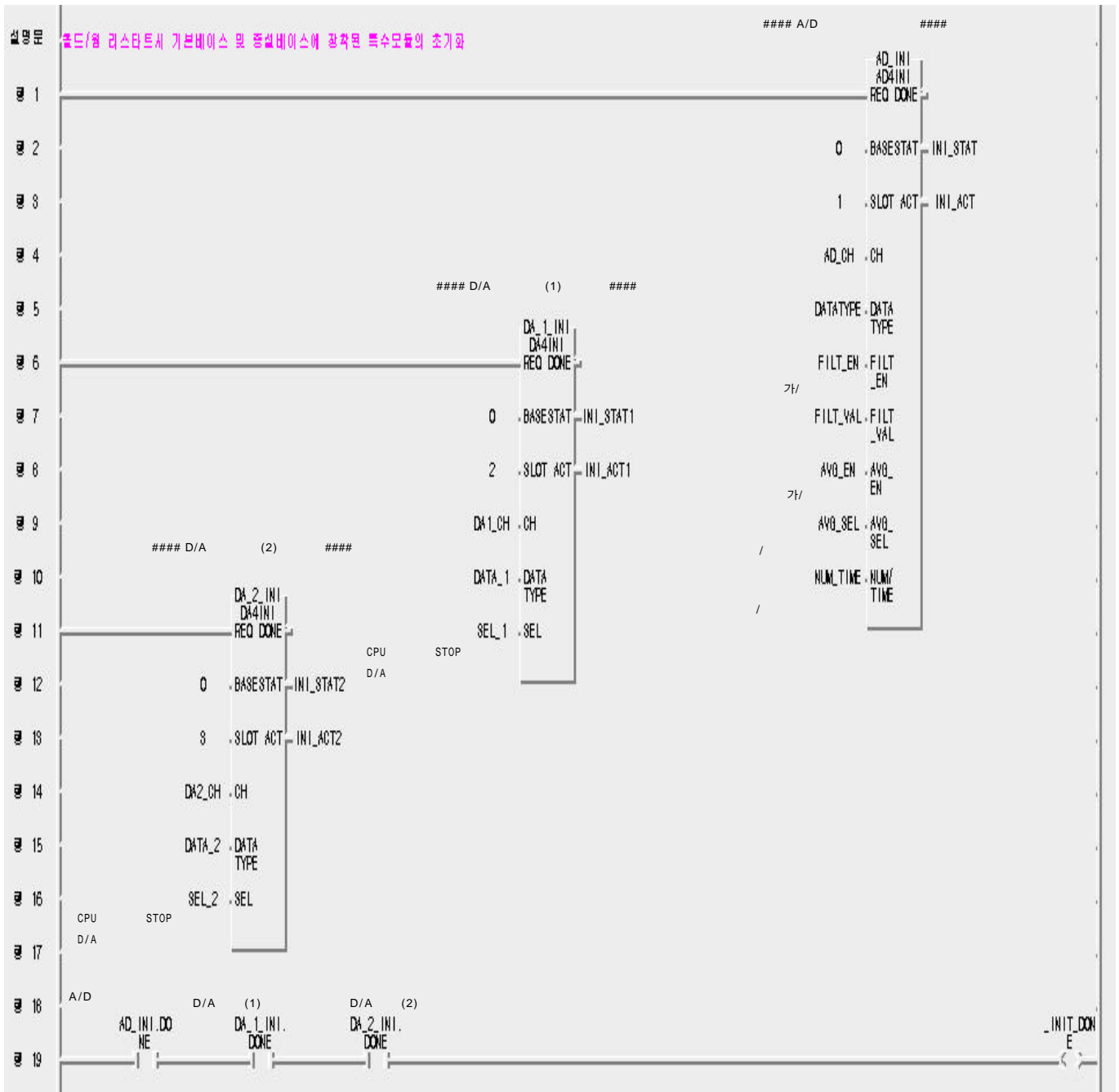
가

(2)

⚡ : .prj



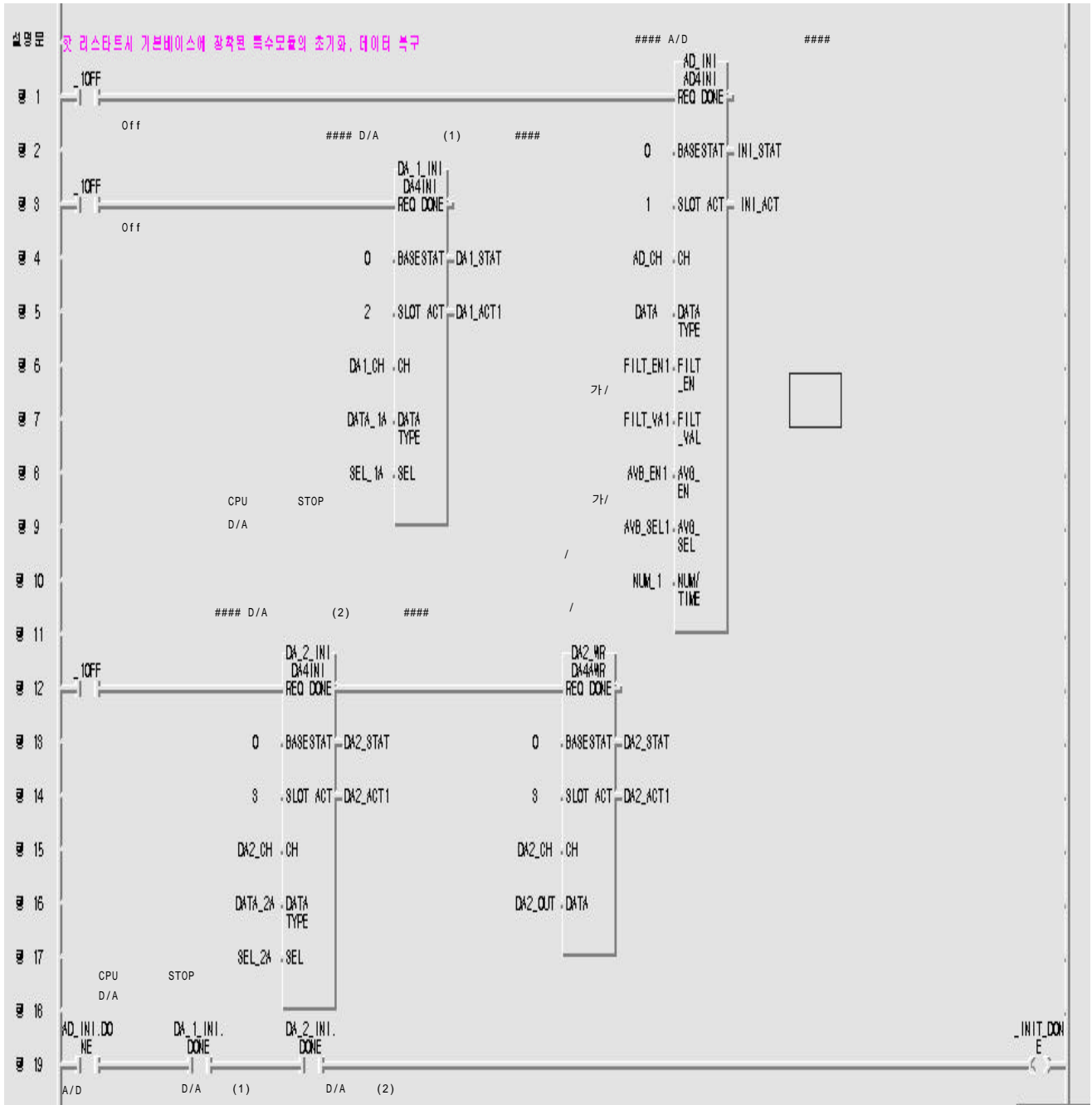
⌘ : - .src



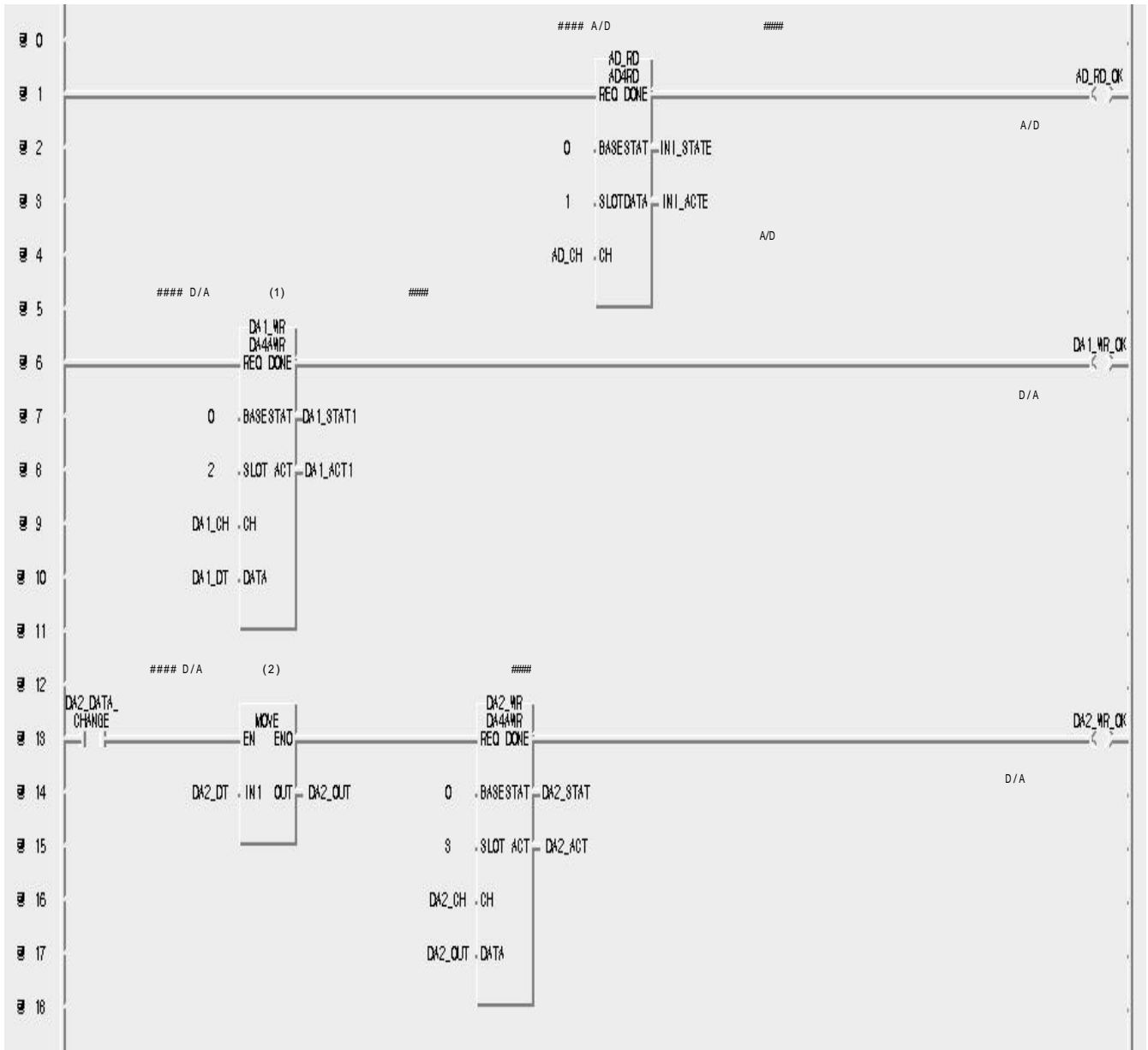


:

.SRC



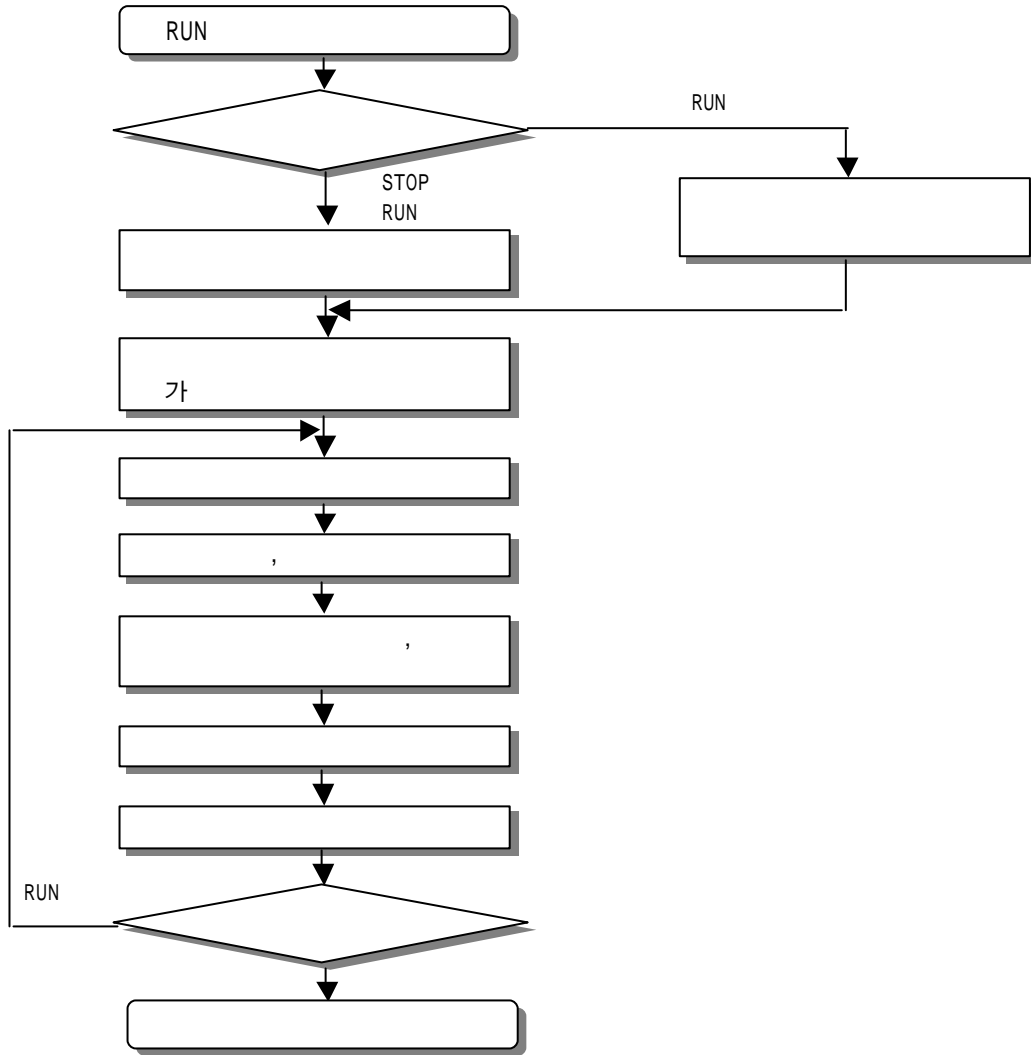
 : .src



4.5

CPU RUN , STOP , PAUSE , DEBUG 4 가 .

4.5.1 RUN



1)

(1) RUN : (, ,)
 (2) STOP ? RUN 가 : (,)
 (3)

2)

(1)
 (2)
 (3)

4.5.2 STOP

가 . STOP GWIN

1)

2)

- (1)
- (2)
- (3)

4.5.3 PAUSE

RUN

1)

2)

- (1)
- (2)
- (3)

4.5.4 DEBUG

가 , STOP

1)

- (1)
- (2)

2)

- (1)
- (2)
- (3)
- (3)
- (4)

3)

? 4 가 가 .

()	
(Break Point)	32 가 가 .
	(Read, Write, Value)

4)

(1) GMWIN

(2) (GMWIN 9 (Enable / Disable) .)

4.5.5

1)

- (1) CPU
- (2) CPU GMWIN
- (3) GMWIN Fnet CPU
- (4) FAM,
- (5) 'STOP'

2)

RUN	RUN
STOP	STOP
STOP ? PAU / REM	STOP
PAU / REM ? RUN ? 1	RUN
RUN ? PAU / REM	PAUSE
PAU / REM ? STOP	STOP

1) ? 1 : RUN RUN PLC

3)

가 STOP (가 STOP ? PAU / REM) 가 .

		GMWIN	FAM,
PAU / REM	STOP RUN		
	STOP PAUSE	X	X
	STOP DEBUG		
	RUN PAUSE		
	RUN STOP		
	RUN DEBUG	X	X
	PAUSE RUN		
	PAUSE STOP		
	PAUSE DEBUG	X	X
	DEBUG STOP		
	DEBUG RUN	X	X
	DEBUG PAUSE	X	X

4)

가

(Source) 가
 GMWIN
 PLC
 가 ' .
 . (1. .)

4.5.6

1)

PLC

- (1) STOP : 'RUN'
- (2) GMWIN : 'PAU/REM' GMWIN 'RUN'
- (3) : 'RUN' 가 'PAU/REM'

(4) : ' (Overall) ' 가 가 .
? : CPU . Off On
? (Overall) : CPU 5 .

2)

(1)
? CPU .
' STOP ' 10 가 .
?
?
? , GMWIN, Off .
?

(2)
?
'4.6.10 ' '4.6.11 ' .

(3)
? 가 , 가 '4.6.9

4.6

4.6.1

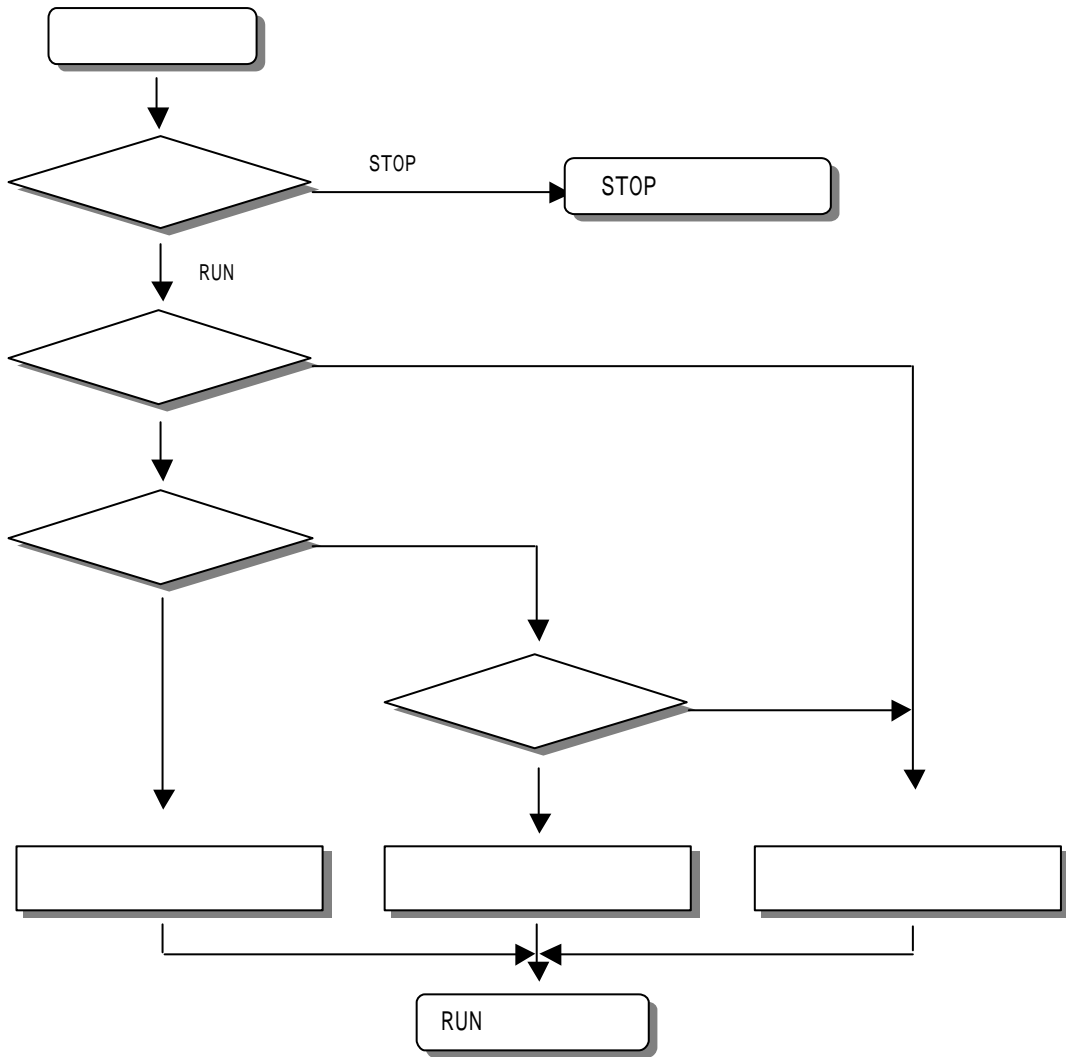
- 가 , RUN , GMWIN , 3
 4.5 , '4.5.1' , . (.)
- 1) (Cold Restart)
 - (1)
 - (2) '0' , .
 - (3)
 - (4) (GMWIN)
 - 2) (Warm Restart)
 - (1)
 - (2) '0' , .
 - (3)
 - (4) 가) (
 - 3) (Hot Restart)
 - (1) (RUN) RUN ,
 - (2)
 - (3) (/)
 - (4) (가)

1)

- (1)
- (2) 20ms

0.5

? CPU



4)

3 가

	(COLD)	(WARM)	(HOT)
	'0'	'0'	
	'0'		
&			

- 1) 3
- (1) (Default) :
- (2) (INIT) :
- (3) (Retain) :

4.6.2

- 1)
 (1) CPU PLC
 (2) PLC
- 2) 가 STOP LED 가
 ✂ : _CNF_ER
 ✂ : _CNF_WAR

1)	12	12.5
----	----	------

4.6.3

CPU (RTC)가 RTC Off 20ms

RTC
 . RTC

1)

	2
	1 ~ 12
	1 ~ 31
	0 ~ 23 (24)
	0 ~ 59
	0 ~ 59
1/100	0 ~ 99
	0 ~ 6 (~)

2)
 ? 5 / 1

3)
 GWIN (On-line) / PLC
 (GWIN 7.11 PLC .)

1) RTC 가 CPU
2) RTC 가 가 RTC 가
3) RTC 가 On RTC 가 _RTC_ERR 가 Off _RTC_ERR

4.6.4

- CPU (GMWIN,)
 CPU REMOTE
- 1) RUN / STOP
 - (1) RUN / STOP CPU 가 REMOTE RUN / STOP
 - (2) CPU CPU
 RUN / STOP

 - 2) PAUSE
 - (1) PAUSE CPU 가 REMOTE PAUSE
 PAUSE On / Off CPU
 - (2) CPU STOP On

 - 3) DEBUG
 - (1) DEBUG CPU 가 REMOTE DEBUG
 DEBUG
 - (2)

 - 4)
 - (1) CPU CPU 가
 CPU

1)	GMWIN	7

4.6.5 On / Off

1) On / Off

On / Off

(I), (Q)
I/O

가

2) On / Off

(1)

~~On / Off~~ (I)

On / Off

가

(2)

~~On / Off~~ (Q)

On / Off

가

On / Off

(3) On / Off

~~On / Off~~

(I), (Q)

On / Off

(4)

~~On / Off~~ Off On,

CPU

~~Stop~~

~~On / Off~~

~~On / Off~~

On / Off

On / Off

~~On / Off~~

1)	GMWIN	7	'	I/O	'

4.6.6 (Direct)

- 1)
 - 'DIREC_IN'
- 2)
 - 가 'DIREC_O' 가
- 3) On / Off
 - On / Off

1)	GLOFA?GM

4.6.7 (History Log? In)

- 가 , 16 가
- 1)
 - (1)
 - : 가
 - :
 - (2)
 - :
 - : ,
 - (3)
 - : RUN
 - : (16), (1)

2) GMWIN

1)	GMWIN 7 'PLC'

4.6.8

가 ,
(GMWIN)

1)

(1) () PLC , PLC

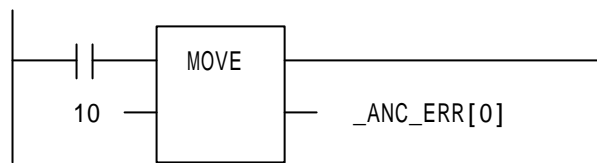
(2) () '_ANC_ERR[n]' , '_ANC_WB[n]'

2)

(1) , '_ANC_ERR[n]' 가
0 ,
가 , PLC Off '_CNF_ER' '_ANNUN_ER'
가 , PLC

(2) , GMWIN , '_ANC_ERR[n]

(3) '_ANC_ERR[n]' 16 (n: 0~15) 가 ,
가
1 65,535



3)

(1) , '_ANC_WB[n]'
On , '_ANC_WB[0]'
가 , '_CNF_WAR' '_ANNUN_WR'
가 , '_ANC_WAR[0]' '_ANC_WAR[7]'

(2) '_ANC_WB[n]' GMWIN , '_ANC_WAR[n]'

(3) , '_ANC_WB[n]'
'_ANC_WAR[n]' , '_CNF_WAR'
'_ANNUN_WR' 가



```

_ANNUN_WR = 1
_ANC_WAR[0] = 10
_ANC_WAR[1] = 0
_ANC_WAR[2] = 0
_ANC_WAR[3] = 0
_ANC_WAR[4] = 0
_ANC_WAR[5] = 0
_ANC_WAR[6] = 0
_ANC_WAR[7] = 0

_ANNUN_WR = 1
_ANC_WAR[0] = 10
_ANC_WAR[1] = 1
_ANC_WAR[2] = 2
_ANC_WAR[3] = 3
_ANC_WAR[4] = 15
_ANC_WAR[5] = 40
_ANC_WAR[6] = 50
_ANC_WAR[7] = 60

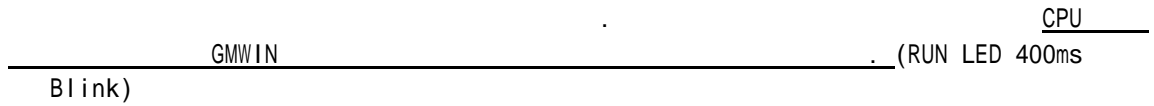
_ANNUN_WR = 1
_ANC_WAR[0] = 1
_ANC_WAR[1] = 2
_ANC_WAR[2] = 3
_ANC_WAR[3] = 15
_ANC_WAR[4] = 40
_ANC_WAR[5] = 50
_ANC_WAR[6] = 60
_ANC_WAR[7] = 75

_ANNUN_WR = 0
_ANC_WAR[0] = 0
_ANC_WAR[1] = 0
_ANC_WAR[2] = 0
_ANC_WAR[3] = 0
_ANC_WAR[4] = 0
_ANC_WAR[5] = 0
_ANC_WAR[6] = 0
_ANC_WAR[7] = 0
    
```

4.7 GM4-CPUC

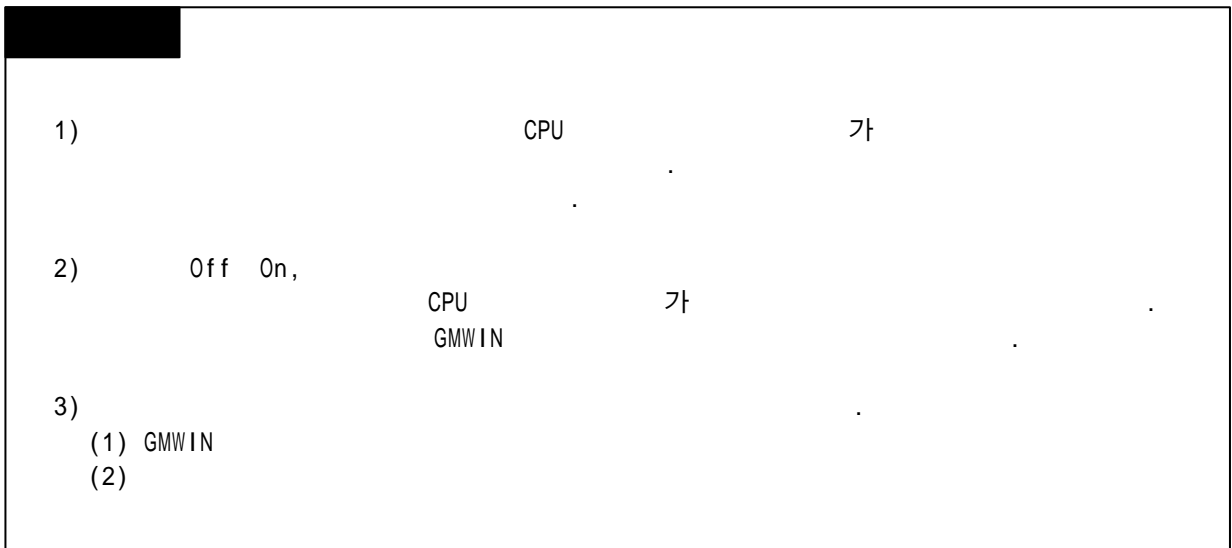
4.7.1

1)



2)

? 가 . ()
2)
? GMWIN 가 .
?



4.7.2

1)

2)

?
(GMWIN .)
? (I) On/Off
? Off , (Q) On/Off
?

1) Off On, CPU 가
GMWIN 가
2)
(1) GMWIN
(2)

4.7.3

GM4C

가 가 가

1)

?
?
?

?

가 가

G4F-AD2A, G4F-AD3A, G4F-DA1A, G4F-TC2A, G4F-RD2A
G4F-DA2V, G4F-DA2I, G4F-DA3V, G4F-DA3I, G4F-AD3A

2)

- (1) GMWIN
- (2) GMWIN
- (3)
- (4) GMWIN
- (5) GMWIN
- (6) ()

1) On/Off
2)

1) 가

3)

가 CPU

(1) GMWIN

?

‘(4)’

GMWIN

(G4F-DA2V, G4F-DA2I, G4F-DA3V, G4F-DA3I

가 .)

✍

4)

가가 가

4.7.4 I/O

1)

I/O I/O
 가
 ? DA

2)

I/O .
 (1) GMWIN 'I/O'
 (2) PLC
 (3) GMWIN I/O PLC
 (4) () I/O
 (5) H/W(PLC Reset Switch) Reset S/W(GMWIN) Reset Off->On
 (6)
 가
 (7) I/O 가
 (8) DA 가 ,
 (9) 가 I/O I/O
 (10) 가 , ,
 I/O . 2-7

- (1) GM4-CPUC O/S : V2.1
- (2) GMWIN : V4.03
- (3) GM4 (GM4-BxxH)

1) I/O , 가 가

4.7.5 FEnet Reset

1)

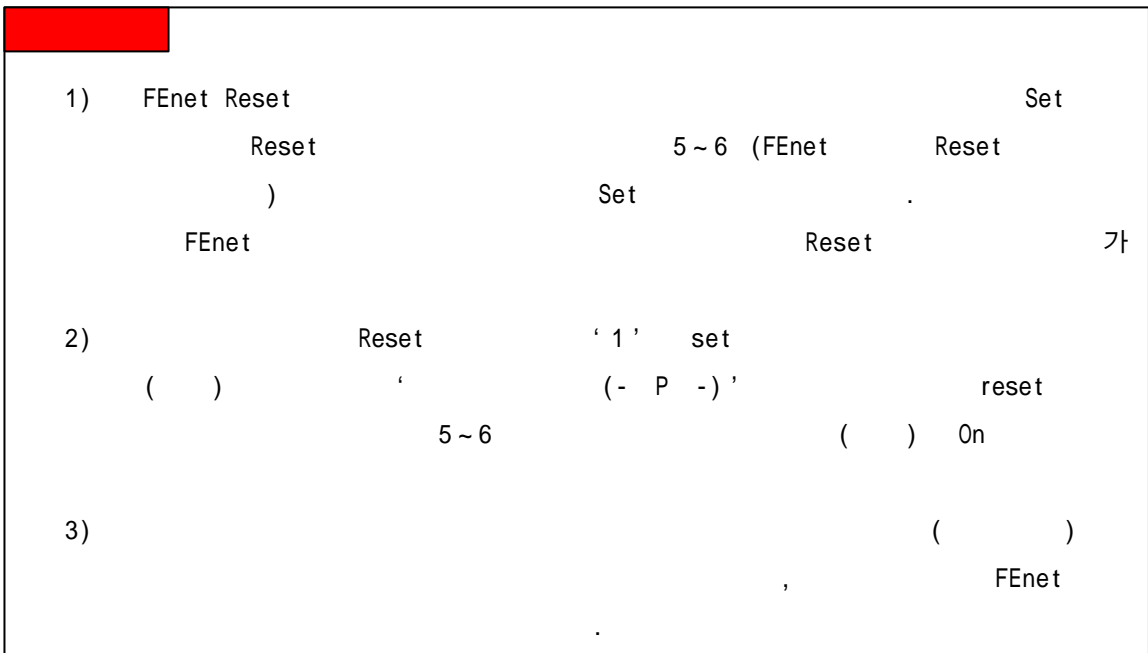
FEnet Reset FEnet FEnet

2)

FEnet FEnet Set
 FEnet Reset 가 . '1' Set Reset
 '0'

- ? FENET_RESETx : x FEnet Reset
- (1) _FENET_RESET_Mx :
- (2) _FENET_RESET_Ex : (GM12AB)
- ? RCV_SERx_CHy : x y
- (1) _RCV_SERVx_M_CH
- (2) _RCV_SERVx_E_CH
- ? RCV_SERx_COUNT : x
- (1) _RCV_SERV_CNT_M :
- (2) _RCV_SERV_CNT_E :
- ? X(Slot) : 0 ~ 5(GM4C), Y() : 0 ~ 15

- (1) GM4-CPUC O/S : V2.1
- (2) GMWIN : V4.04
- (3) Fast Enet O/S : V1.1



4.8

CPU 가 가 가 가

1)

	GM4-CPUA/B	GM4-CPUC
	128K byte	1M byte
☞ : ☞	1.5K byte	13K byte
☞ : ☞ I/O ☞ ☞	3.5K byte	10K byte
☞ : ☞ ☞ / ☞ ☞ ☞ ☞	123K byte	977K byte

2)

	GM4-CPUA	GM4-CPUB	GM4-CPUC
	64K byte	64K byte	512K byte
☞ I/O : ☞	7.5K byte	6.5K byte	50K byte
	1.5K byte	1.5K byte	4,096 byte
(%IX)	512 byte	1K byte	4,096 byte
(%QX)	512 byte	1K byte	4,096 byte
(%M)	2 ~ 16K byte	2 ~ 16K byte	8 ~ 64K byte
()	52K byte -	50K byte -	428K byte -
	4K byte	4K byte	4K byte

3)

(1)

CPU

GMWIN

(2)

(3)

가
 %IX0.0.0 ~ %IX7.7.63(B TYPE:%IX15.7.63) , %IX0.0.0 ~ %IX3.7.63

(GM4-CPUC 가 %IX0.0.0 ~ %IX63.7.63 ,
 %IX0.0.0 ~ %IX6.7.63 .)

(4)

가
 %QX0.0.0 ~ %IX7.7.63(B TYPE:%IX15.7.63) , GM4 %QX0.0.0
 ~ %QX3.7.63

(GM4-CPUC 가 %QX0.0.0 ~ %QX63.7.63 ,
 %QX0.0.0 ~ %QX6.7.63 .)

(5)

%MX0, %MBO, %MWO, %MDO
 가
 가

(6)

가
 가
 'PB ' FB
 PB 32K byte

4.9

1)

2)

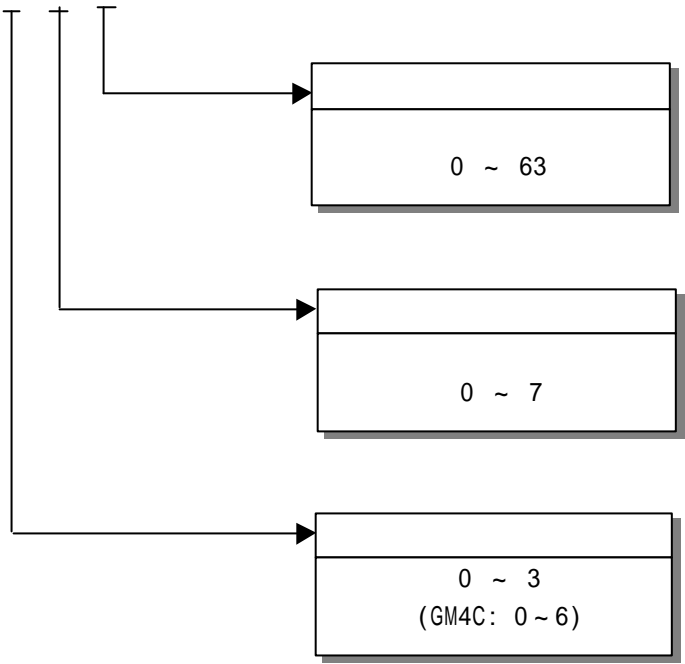
64

3)

64

4)

: %I X 0. 0. 0
: %Q X 0. 1. 15



5

5.1

	DC 3.0 V
	5
	, RTC
	, 3V
(mm)	? 14.5 X 26

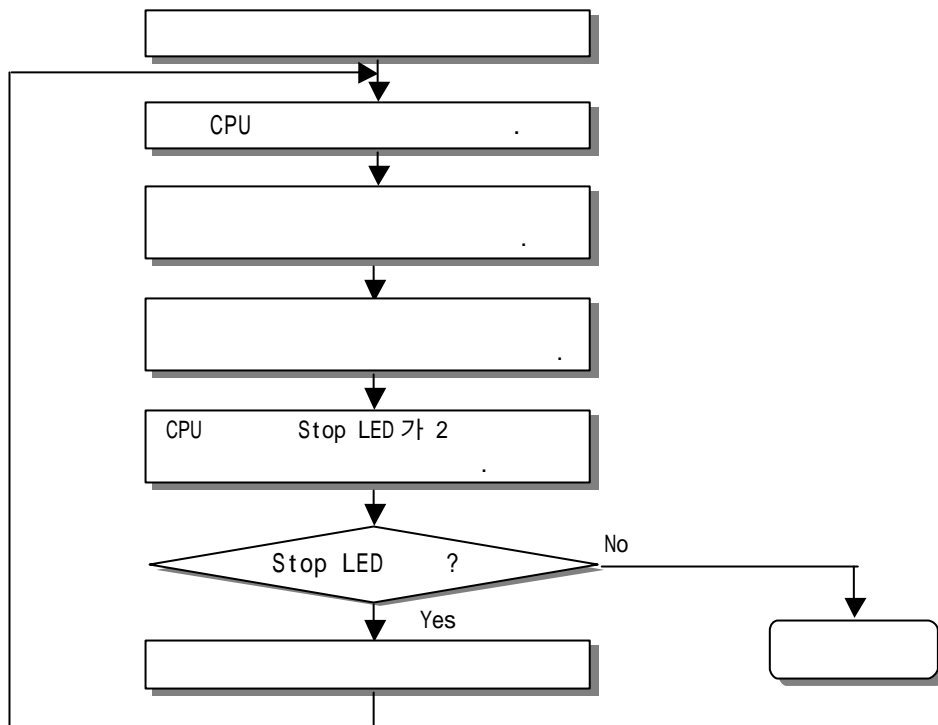
5.2

- 1) 가
- 2)
- 3)

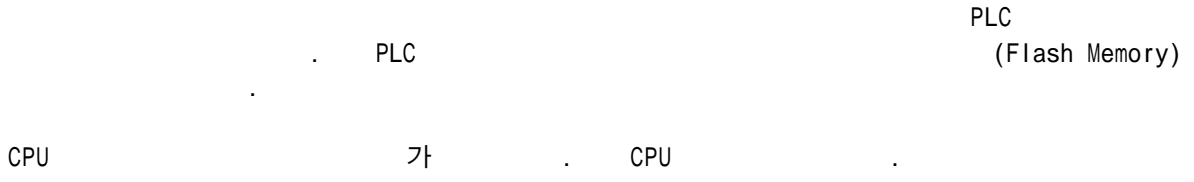
5.3

30

가

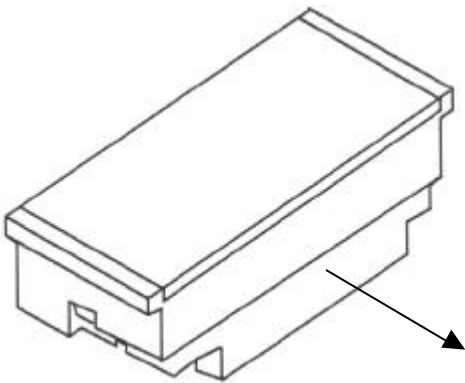


6



6.1 GM4-CPUA ()

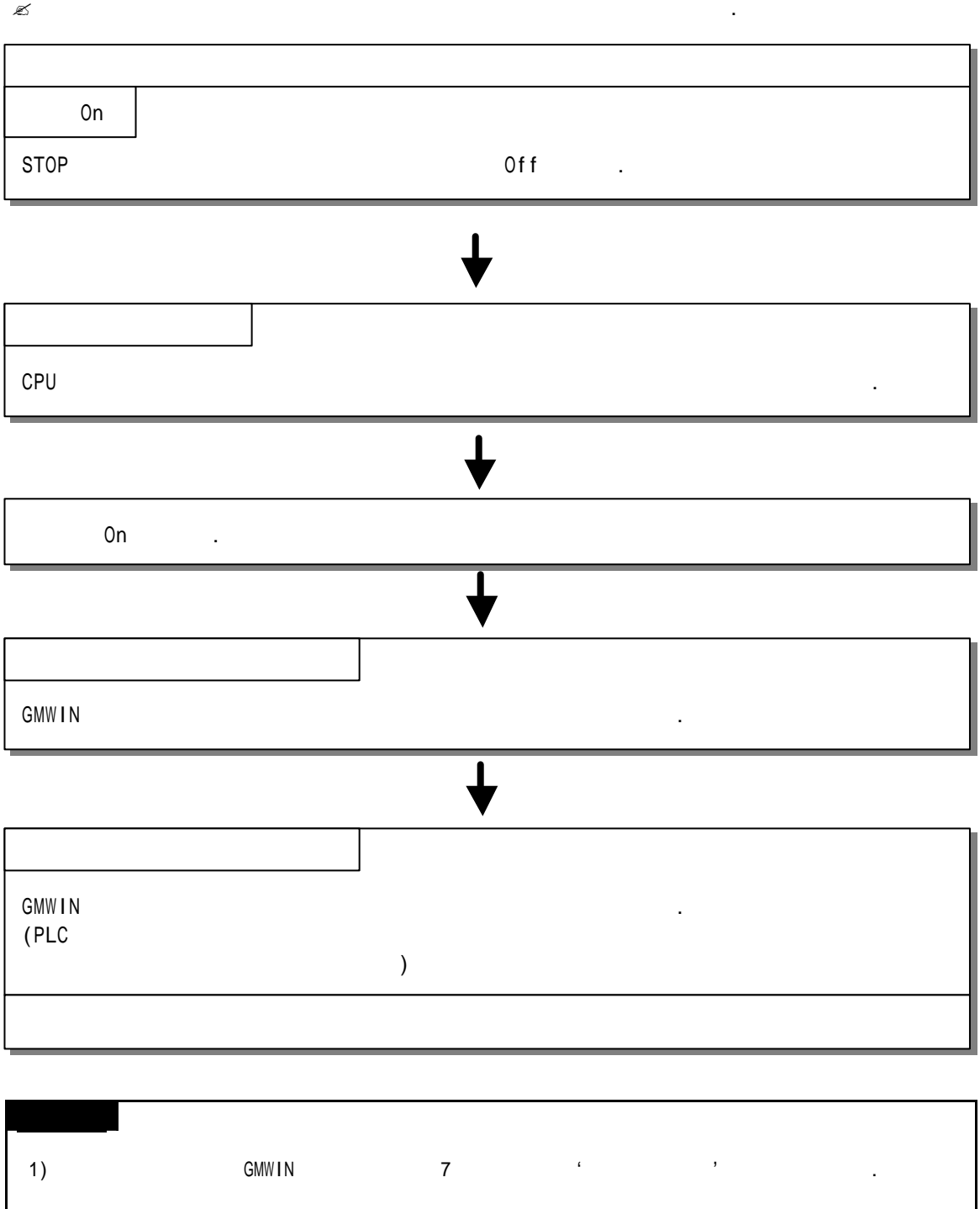
6.1.1



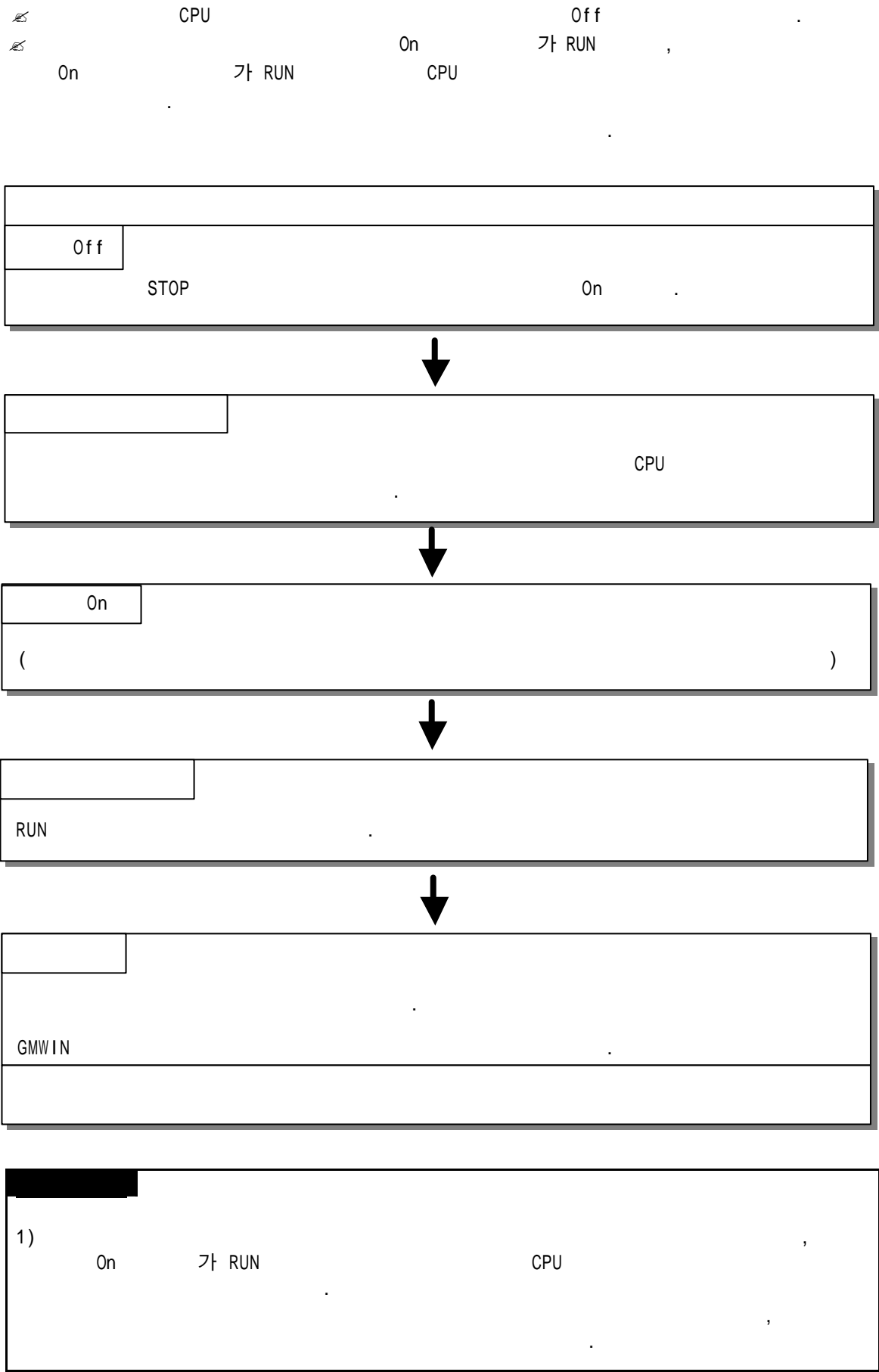
	G4M?M032
	128 Kbyte (32Kstep)
(Kg)	0.01

6.1.2

CPU Off CPU 가 STOP
가 .



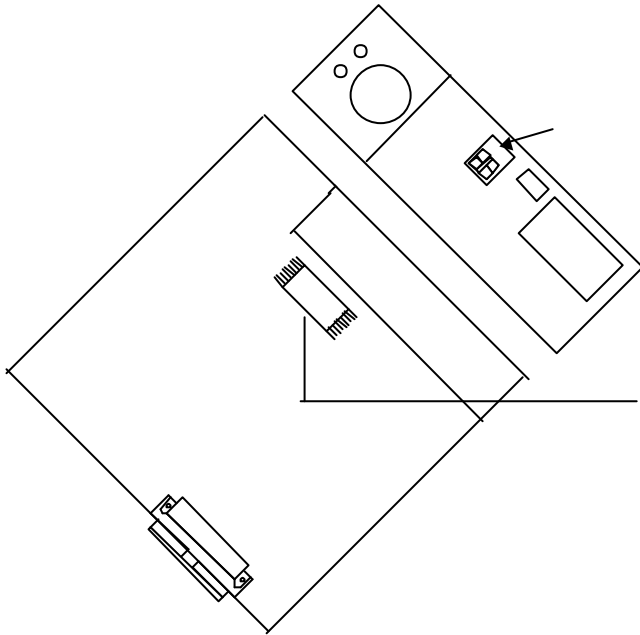
6.1.3



6.2 GM4-CPUB ()

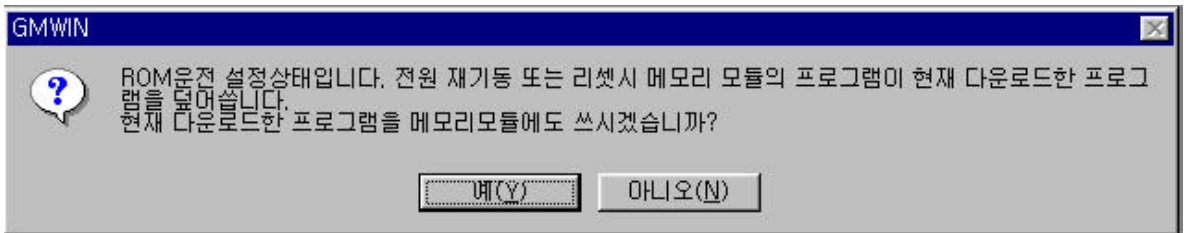
6.2.1

GM4-CPUB GM4-CPUA ROM
 , GM4-CPUB (Flash Memory)
 GM4-CPUB ROM ROM

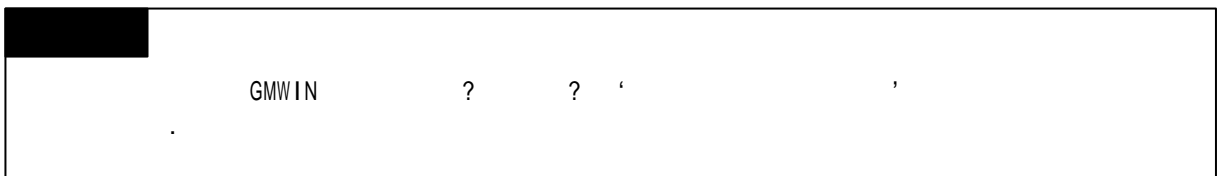


6.2.2

- 1) GM4-CPUB STOP
- 2) GM4-CPUB 가
- (1) GM4-CPUB GMWIN 가

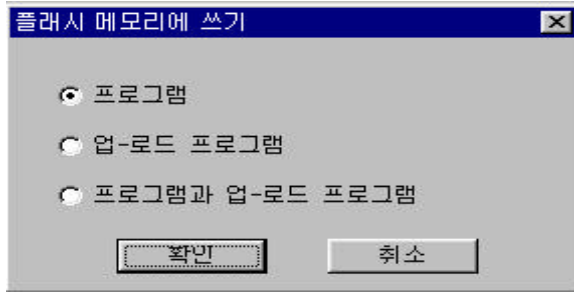


(Y)



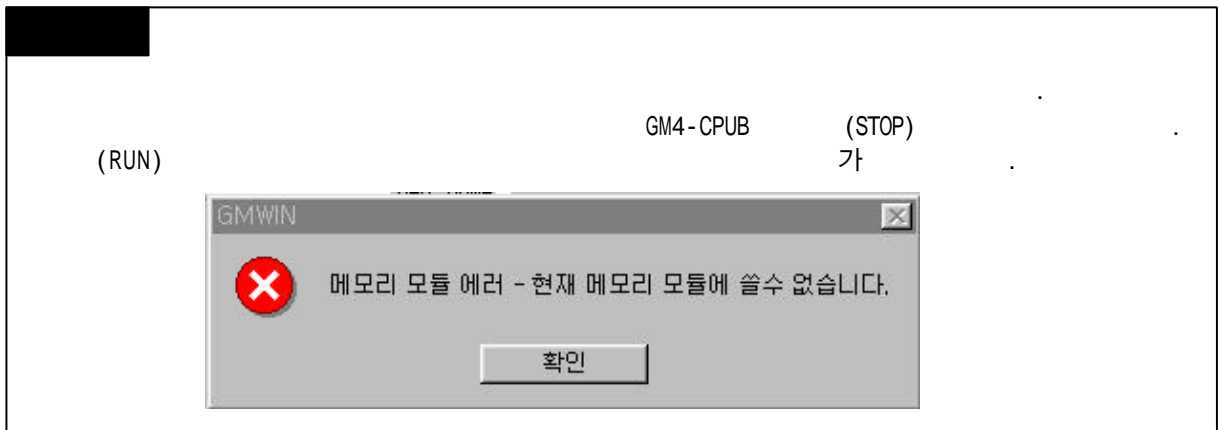
(2)

GMWIN (F) - (W) 가



(3) PLC

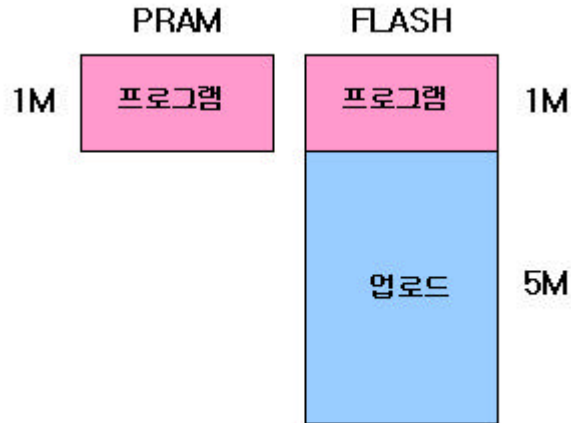
	On
	CPU



6.3 GM4-CPUC ()

GM4-CPUC GM4-CPUA ROM
, GM4-CPUC (Flash Memory)

ROM Flash Memory

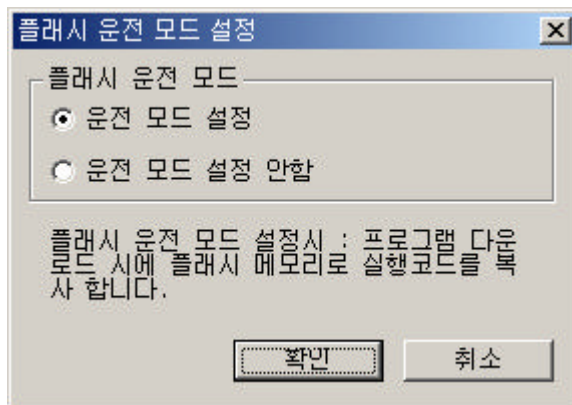


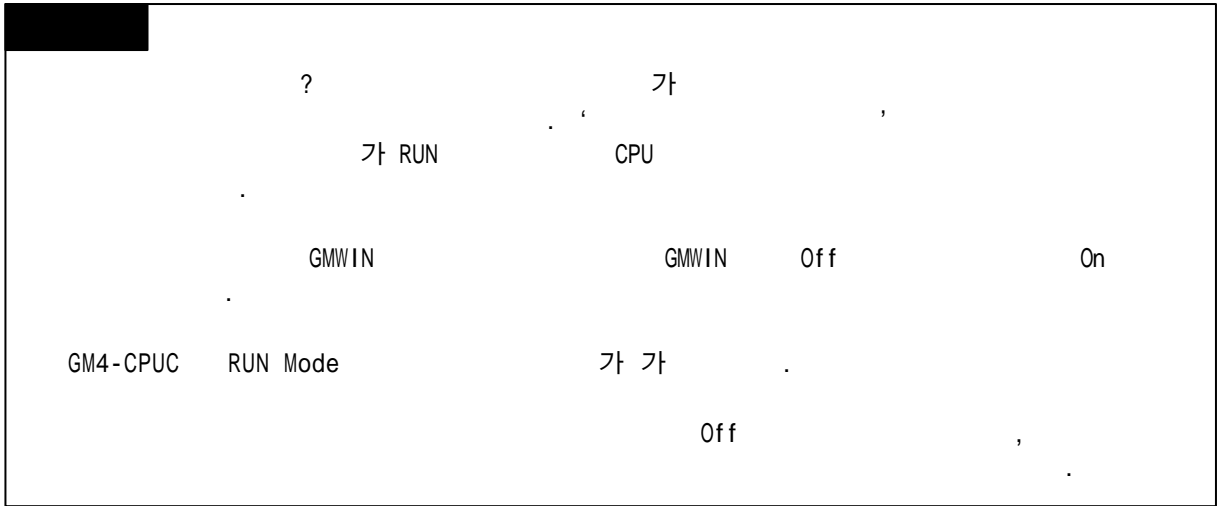
6.3.1

1)

GM4-CPUC GMWIN ROM

-> ->





2)

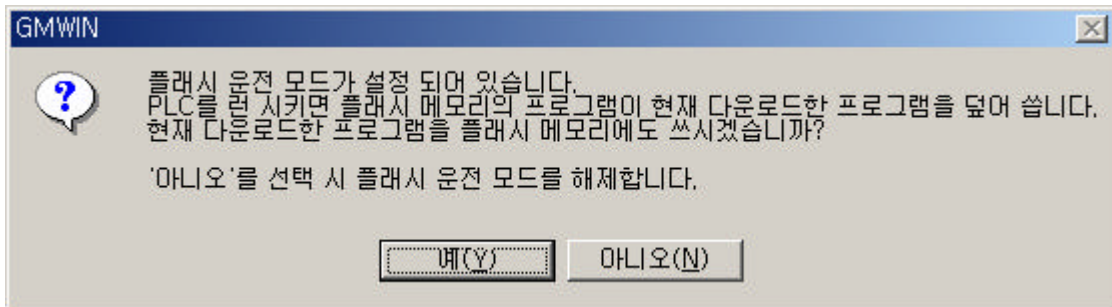
가 가

2.1)

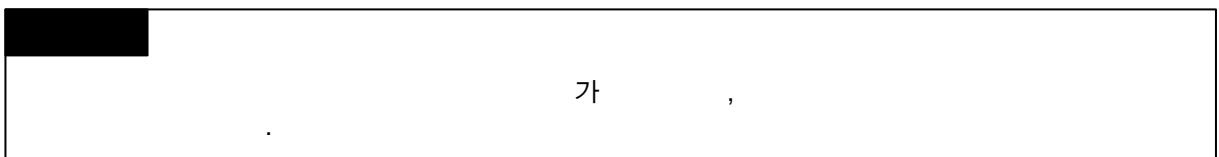
가
가

GM4-CPUC

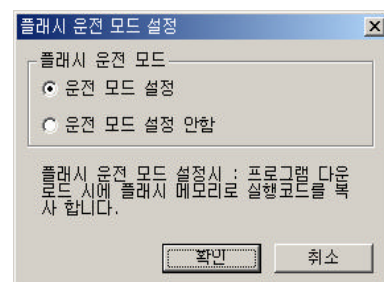
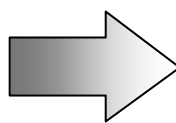
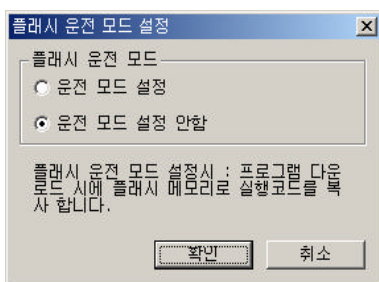
GMWIN

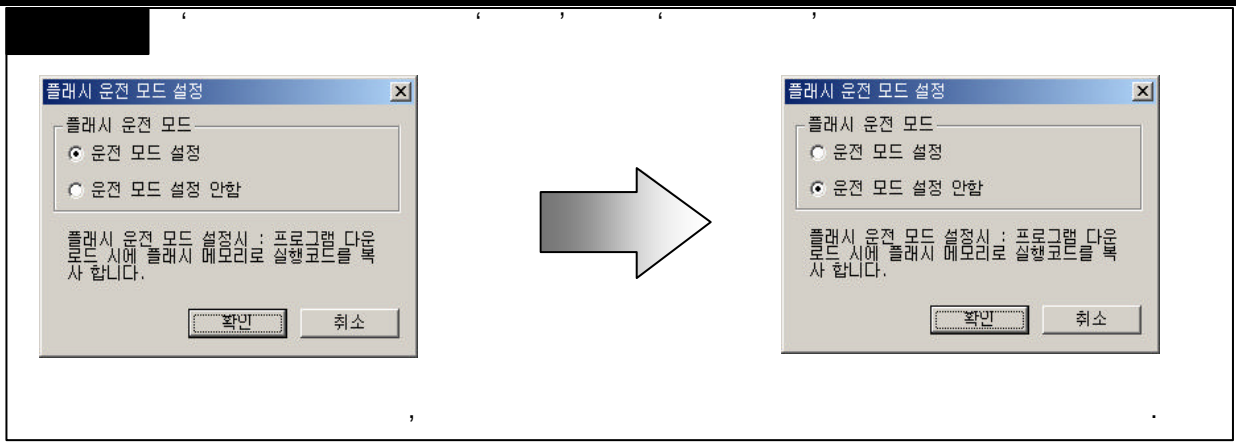


(Y)



2.2)

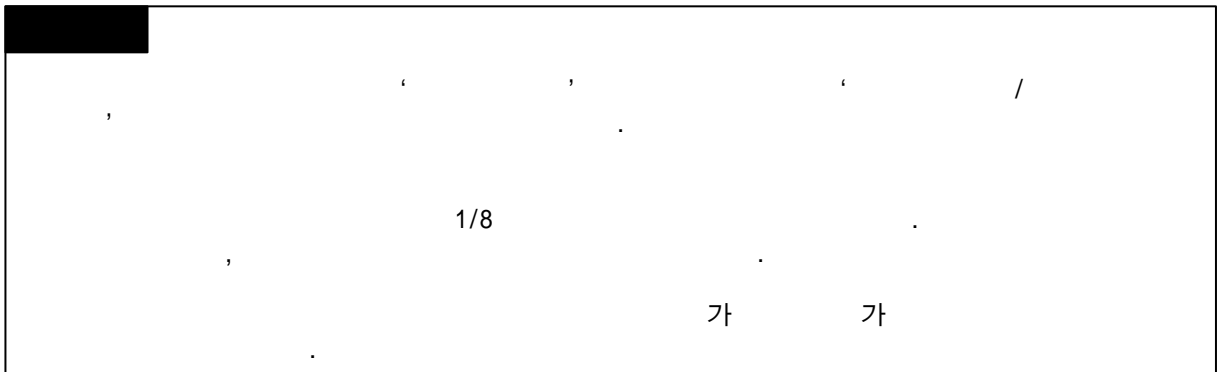
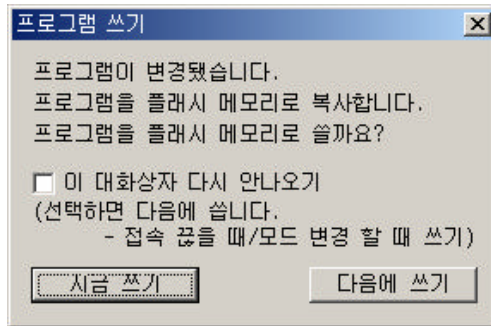




2.3)

GM4-CPUC

가



6.3.2

PLC

RUN

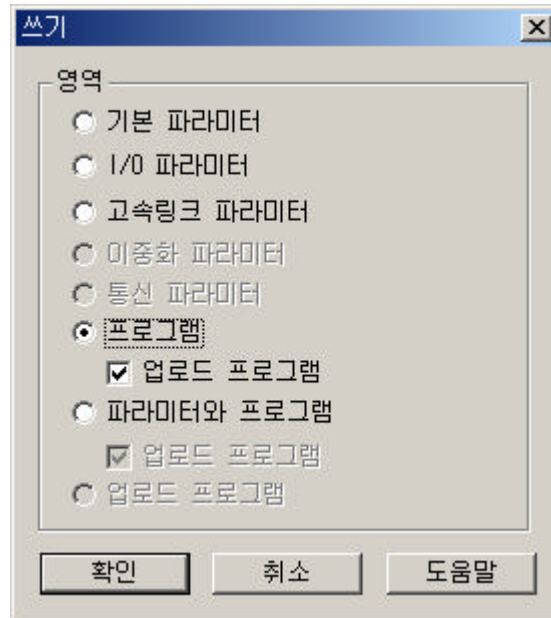
ON	(.)
OFF	CPU

6.3.3

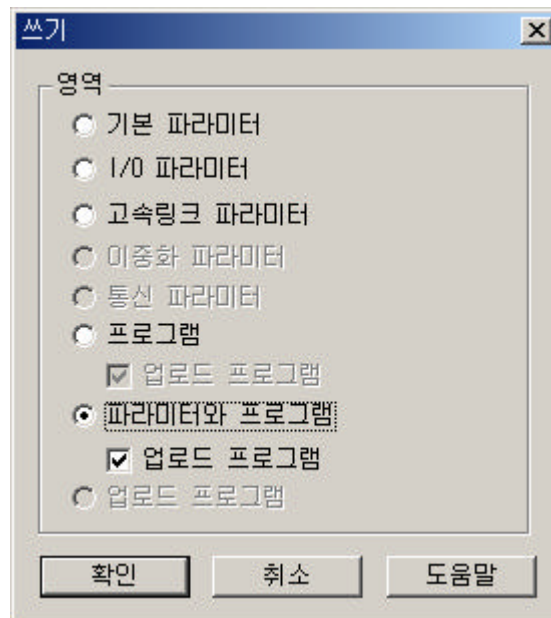
GM4C

가 가

1)



2)



3)

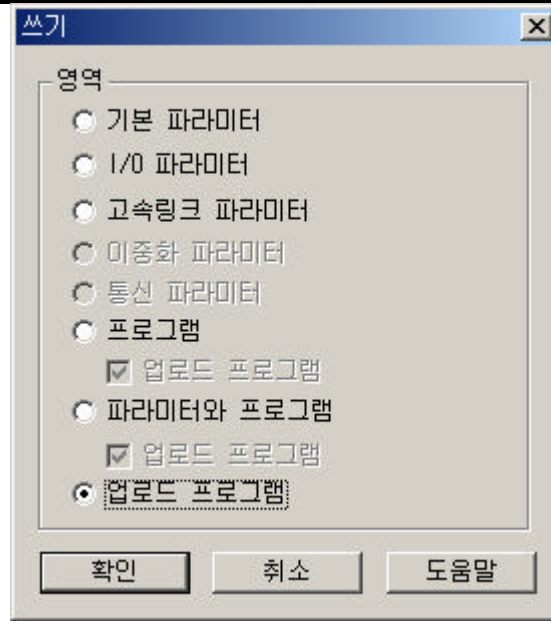
' -> '

. Stop

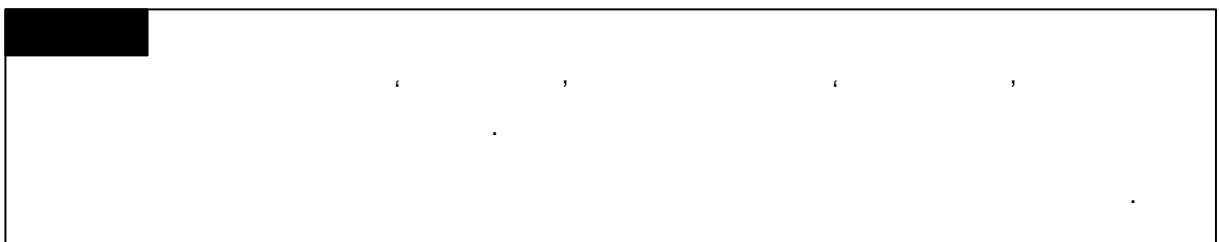
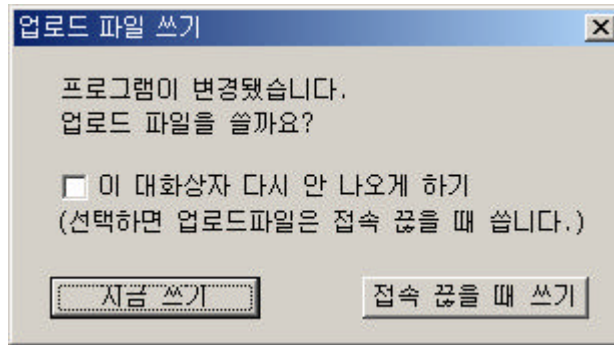
PLC

Stop

PLC



4)



7.1

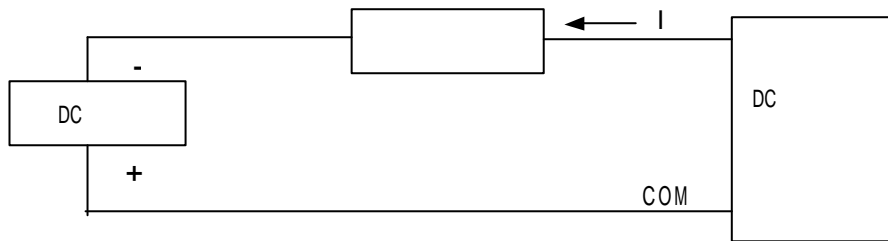
GLOFA?GM4

1)

DC

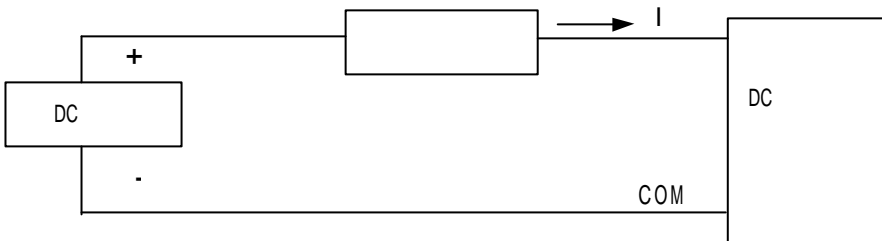
GM4

(1)



? 가 DC DC (-)
 ? On DC 가

(2)



? 가 DC DC (+)
 ? On DC 가 DC

2)

3)

CPU 1

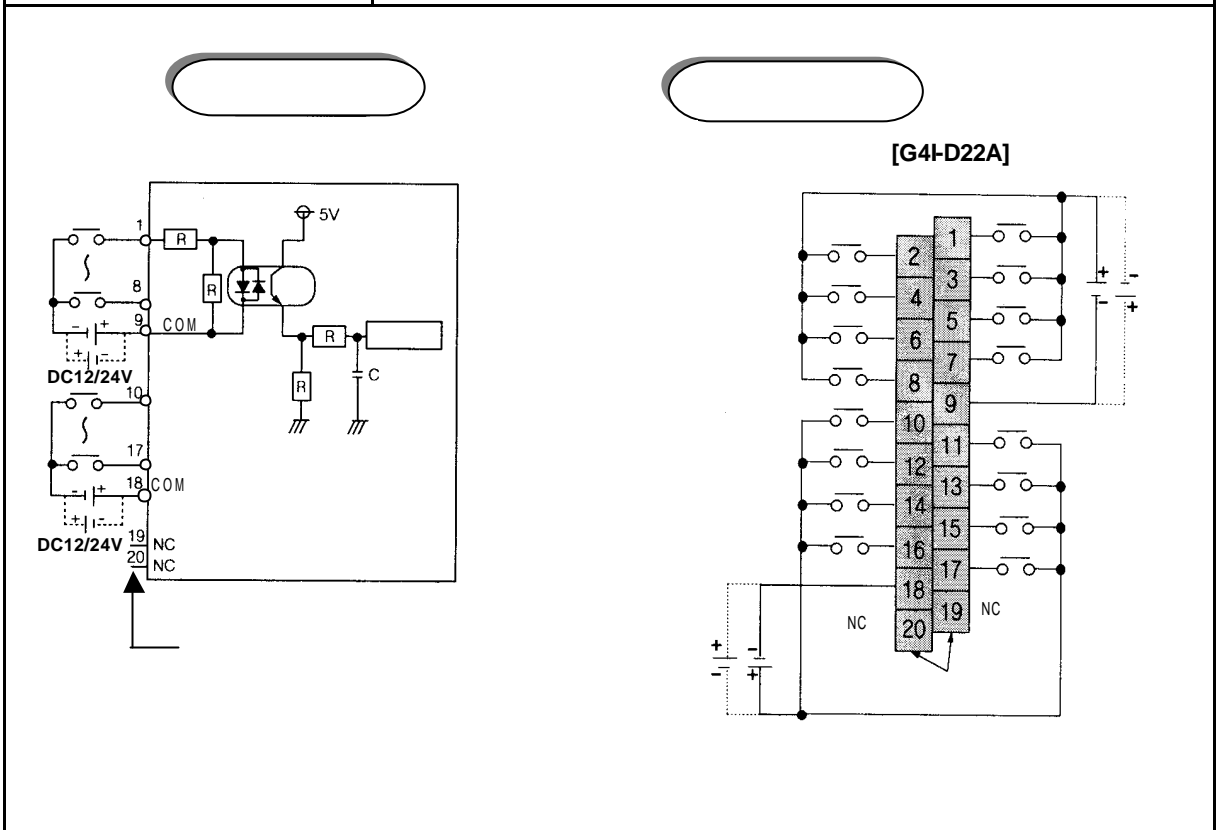
4)

가

7.2

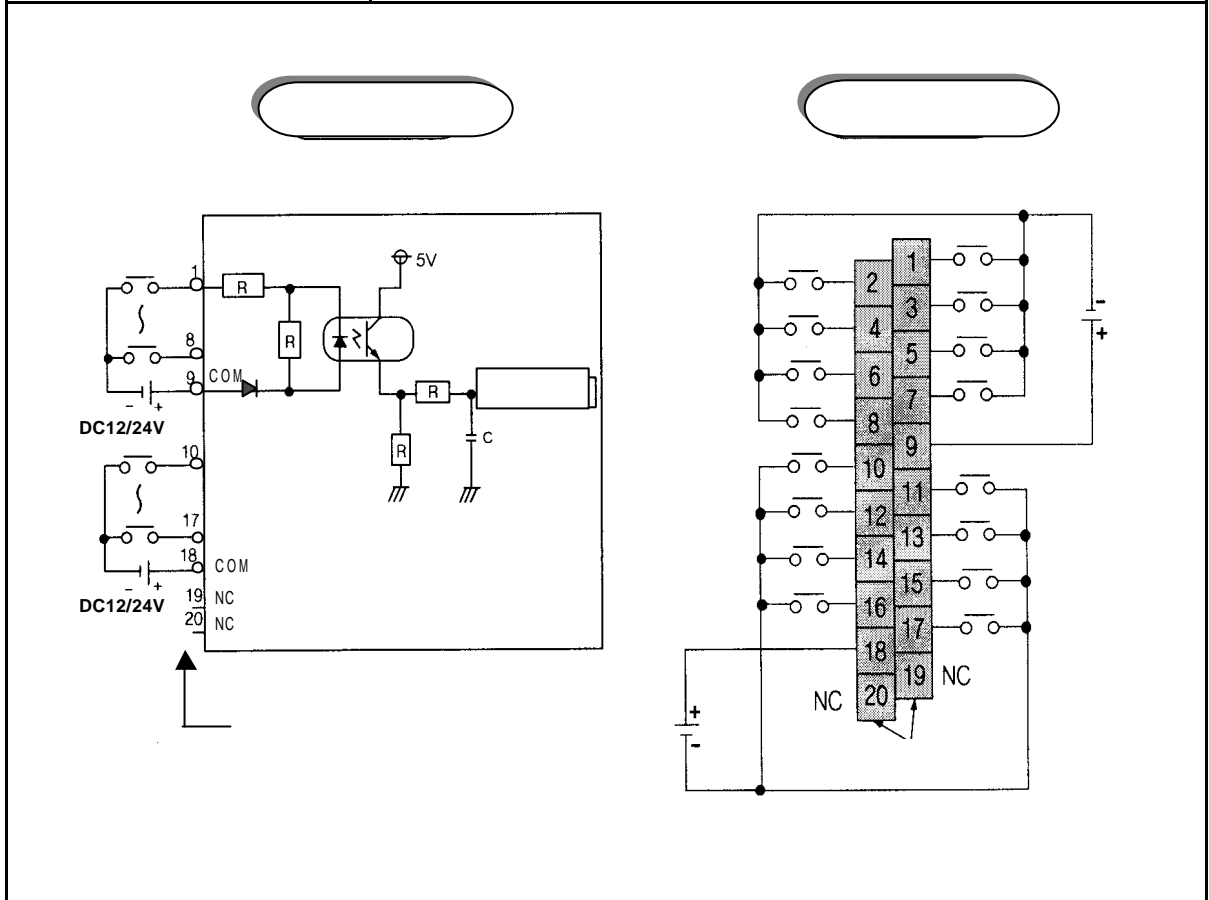
7.2.1 16 DC12/24V (/)

		GM4	
		G4I?D22A	
		16	
		DC12 / 24V	
		5 / 11mA	
		DC10.2 ~ 26.4V (5%)	
		100% On	
On / On		DC9.5V	/ 4.0mA
Off / Off		DC6V	/ 1.0mA
		2.2 k?	
	Off ? On	10 ms	
	On ? Off	10 ms	
		8 / COM	
		70 mA	
		On LED	
		20 (M3 X 6)	
		0.25 kg	



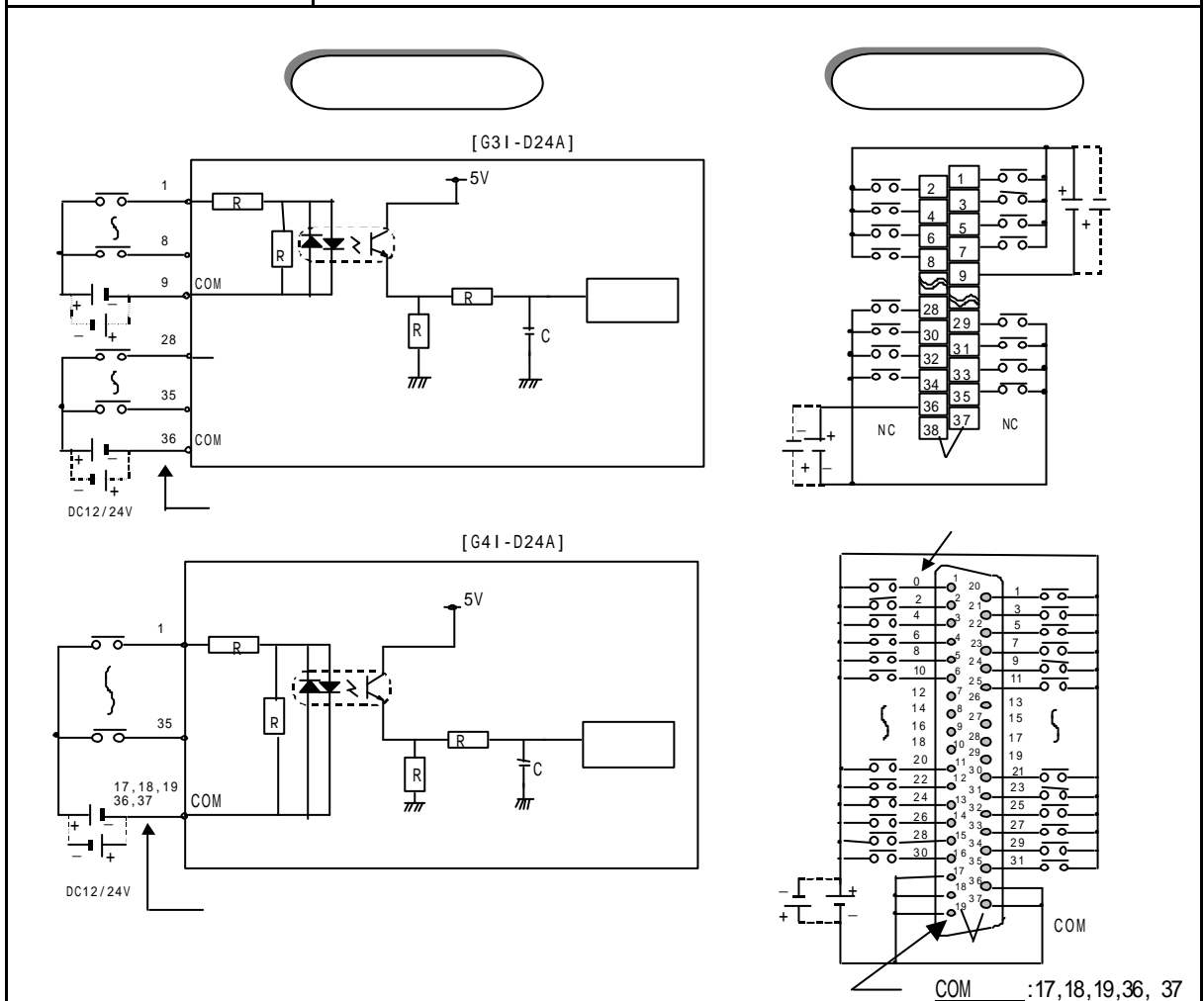
7.2.2 16 DC12/24V ()

		GM4	
		G4I-D22B	
		16	
		DC12V	DC24V
		5 mA	11mA
		DC10.2 ~ 26.4V (5%)	
		100% (8 / COM)	On
On / On		DC9.5V	/ 4.0 mA
Off / Off		DC6V	/ 1.0 mA
		2.2 k?	
	Off ? On	10ms	
	On ? Off	10ms	
		8 / COM	
		70mA	
		On LED	
		20 (M3 X 6)	
		0.25 kg	



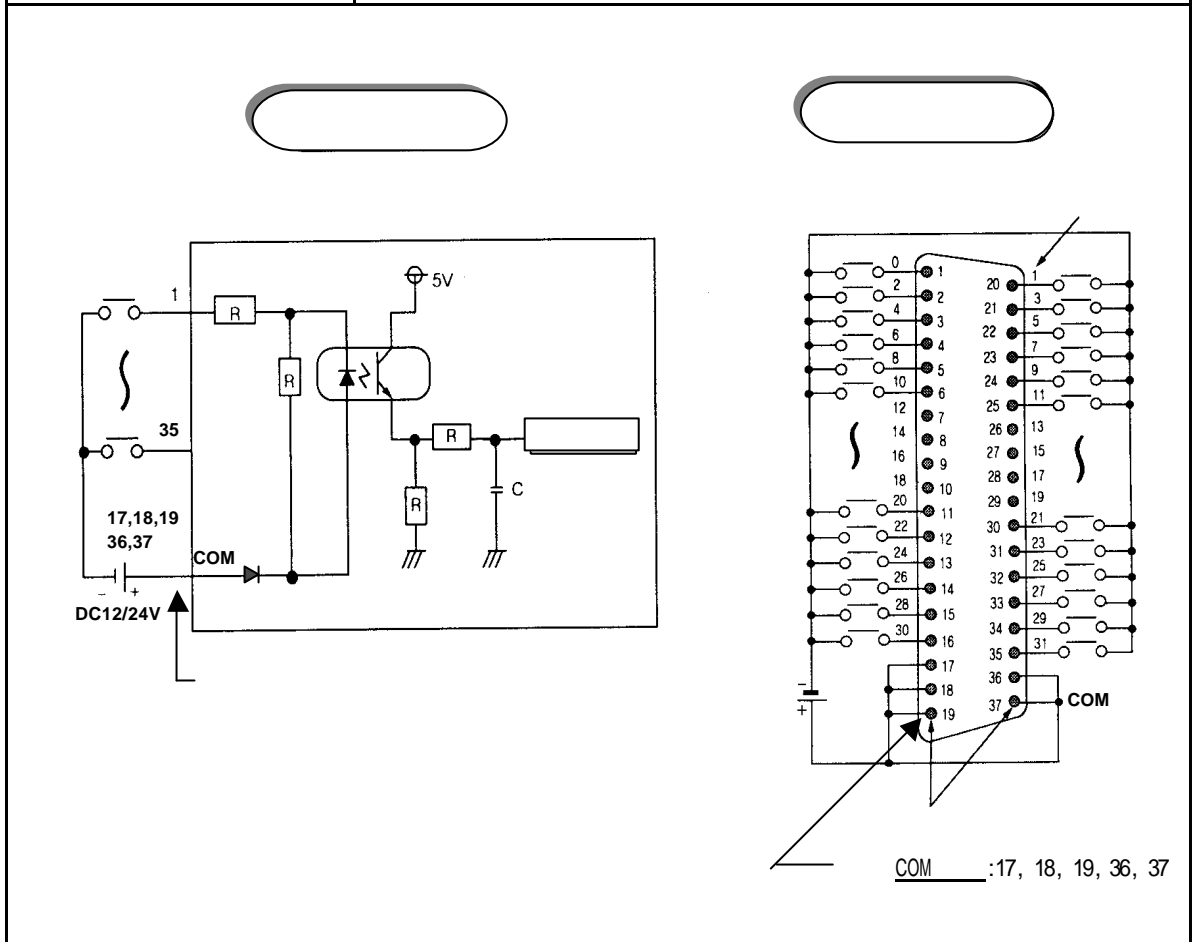
7.2.3 32 DC12/24V (/)

		GM4	
		G4I-D24A	
		32	
		DC12 / 24V	
		3 / 7 mA	
		DC10.2 ~ 26.4V (5 %)	
		60 % On (19 / COM)	
On / On		DC9.5V / 3mA	
Off / Off		DC6V / 1.5 mA	
		3.3 k?	
	Off ? On	10 ms	
	On ? Off	10 ms	
		32 / COM	
		75 mA	
		16	
		37 D-Sub	
		0.19 kg	



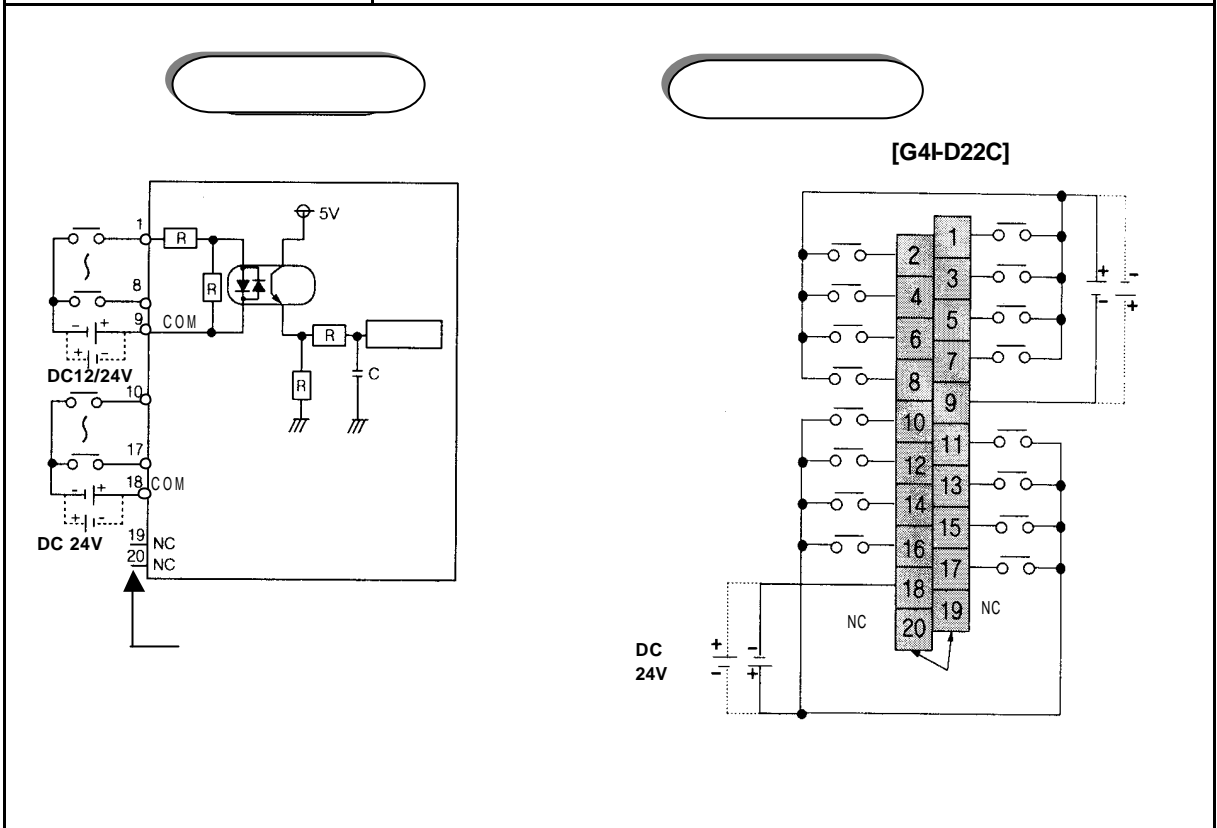
7.2.4 32 DC12/24V ()

		GM4	
		G4I-D24B	
		32	
		DC12V	DC24V
		3 mA	7 mA
		DC10.2 ~ 26.4V (5%)	
		60% (19 / 1COM) On	
On / On		DC9.5V / 3 mA	
Off / Off		DC6V / 1.5 mA	
		3.3 k?	
	Off ? On	10ms	
	On ? Off	10ms	
		32 / 1COM	
		75mA	
		On LED (16)	
		37 D-Sub	
		0.19 kg	



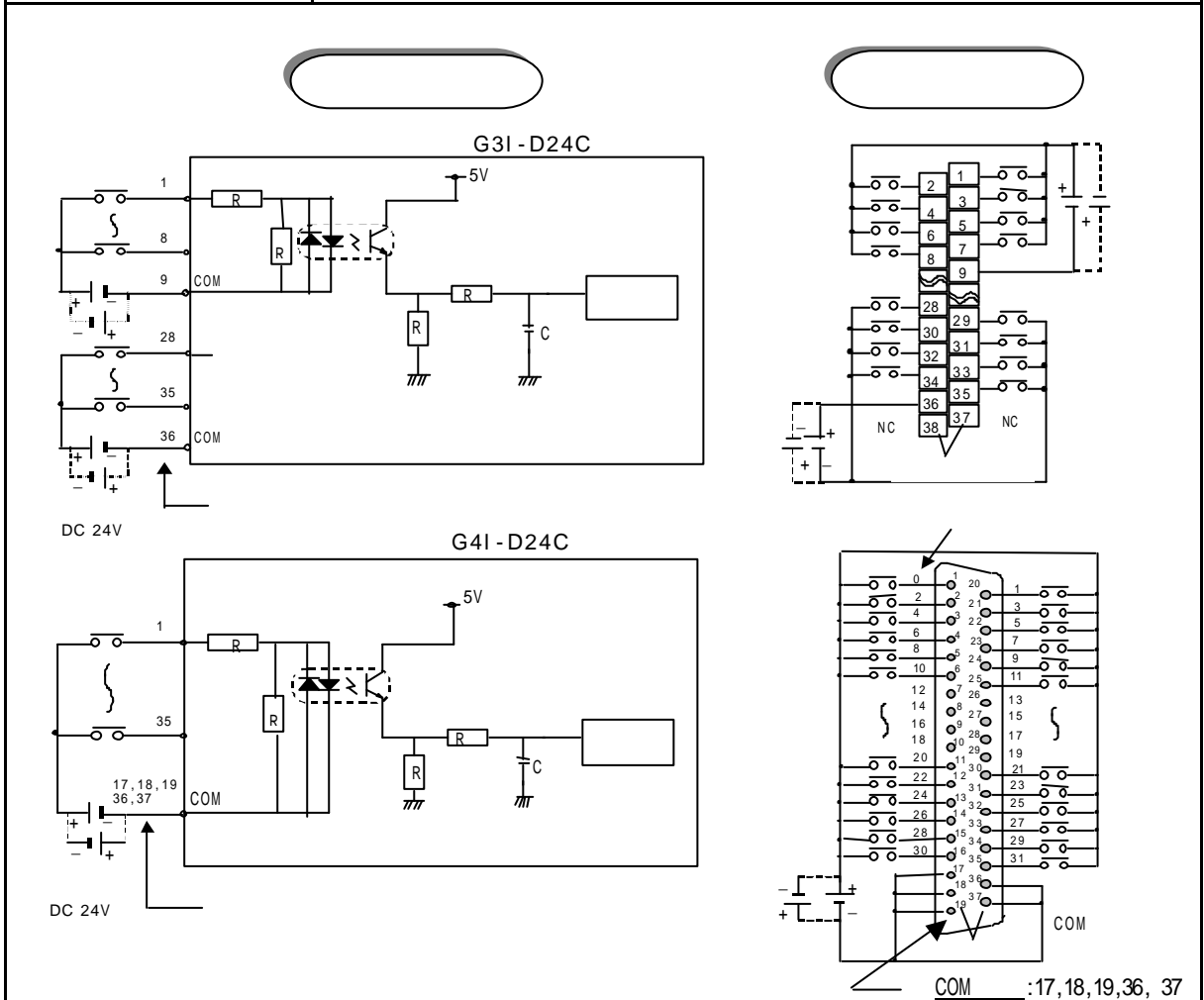
7.2.5 16 DC24V (/)

		GM4
		G41 ?D22C
		16
		DC 24V
		7mA
		DC20.4 ~ 28.8V (5%)
		100% On
On / On		DC17V / 5.2mA
Off / Off		DC8V / 2.4mA
		3.3 k?
	Off ? On	10 ms
	On ? Off	10 ms
		8 / COM
		70 mA
		On LED
		20 (M3 X 6)
		0.25 kg



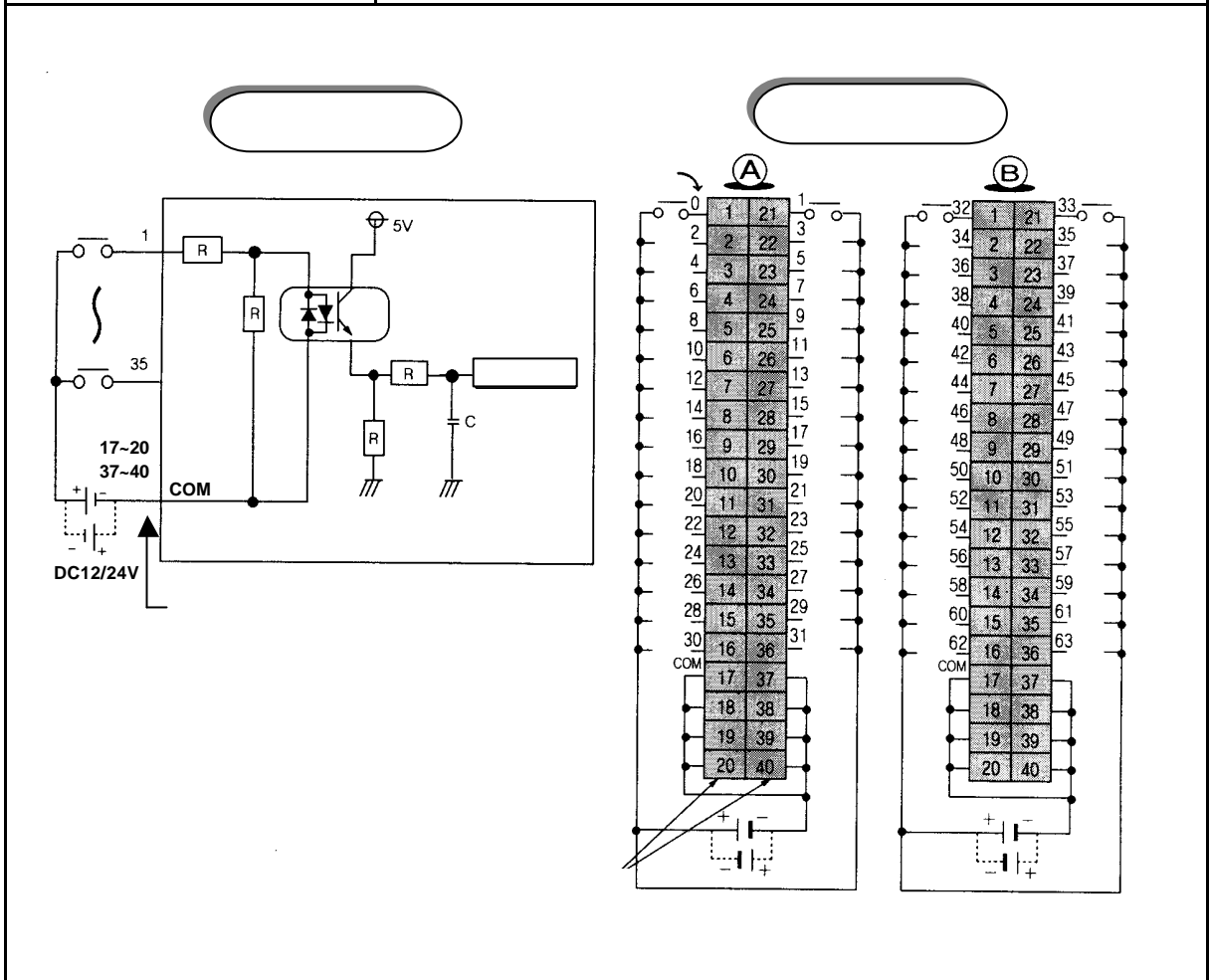
7.2.6 32 DC24V (/)

		GM4	
		G4I-D24C	
		32	
		DC 24V	
		7 mA	
		DC20.4 ~ 28.8V (5 %)	
		60 % On (19 / COM)	
On / On		DC17V	/ 5.2mA
Off / Off		DC8V	/ 2.4 mA
		3.3 k?	
	Off ? On	10 ms	
	On ? Off	10 ms	
		32 / COM	
		75 mA	
		16	
		37 D-Sub	
		0.19 kg	



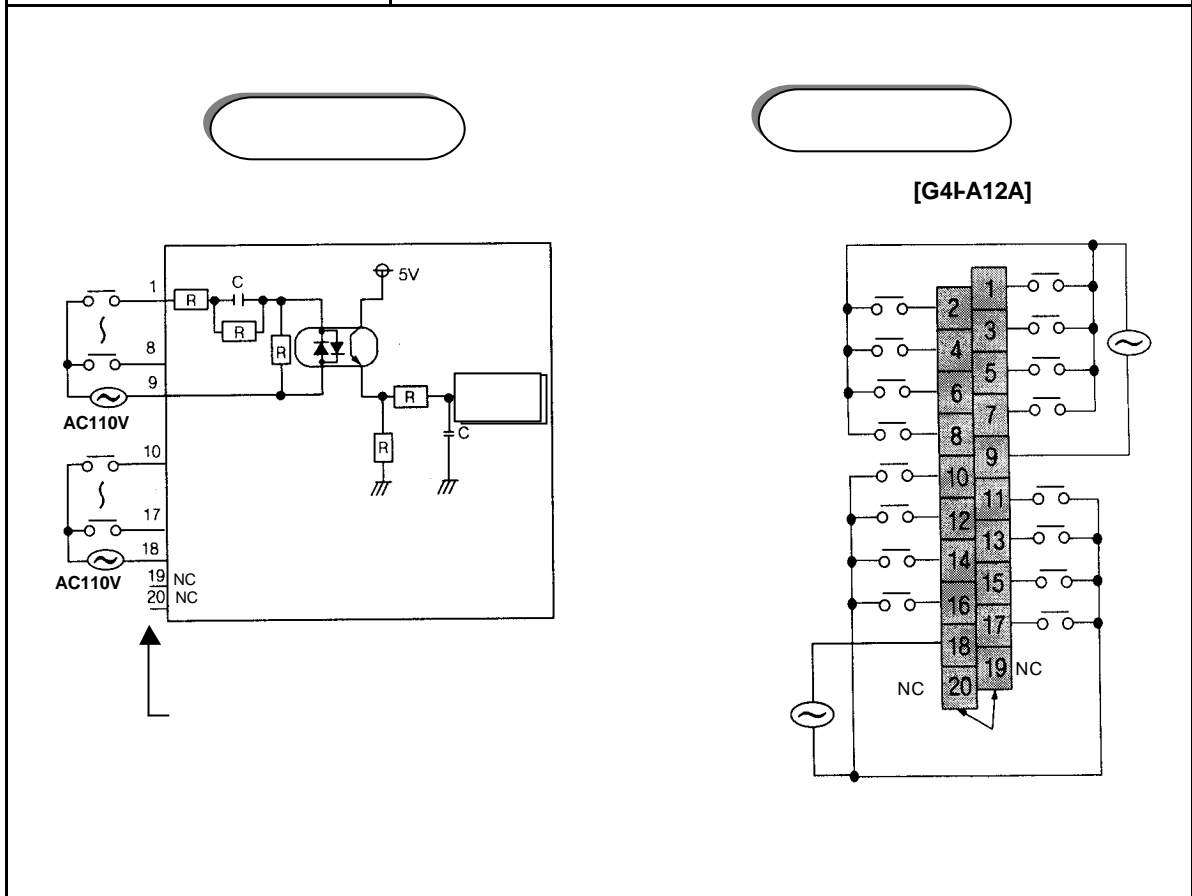
7.2.7 64 DC12/24V (/)

		GM4	
		G4I-D28A	
		64	
		DC12/24V	
		3 mA/7 mA	
		DC10.2 ~ 26.4V (5%)	
		60% (20 / 1COM) 0n	
On / On		DC9.5V / 4.0 mA	
Off / Off		DC6V / 1 mA	
		3.3 k?	
	Off ? On	10ms	
	On ? Off	10ms	
		32 / 1COM (: 17, 18, 19, 20, 37, 38, 39, 40)	
		120mA	
		16	
		40 2	
		0.46 kg	



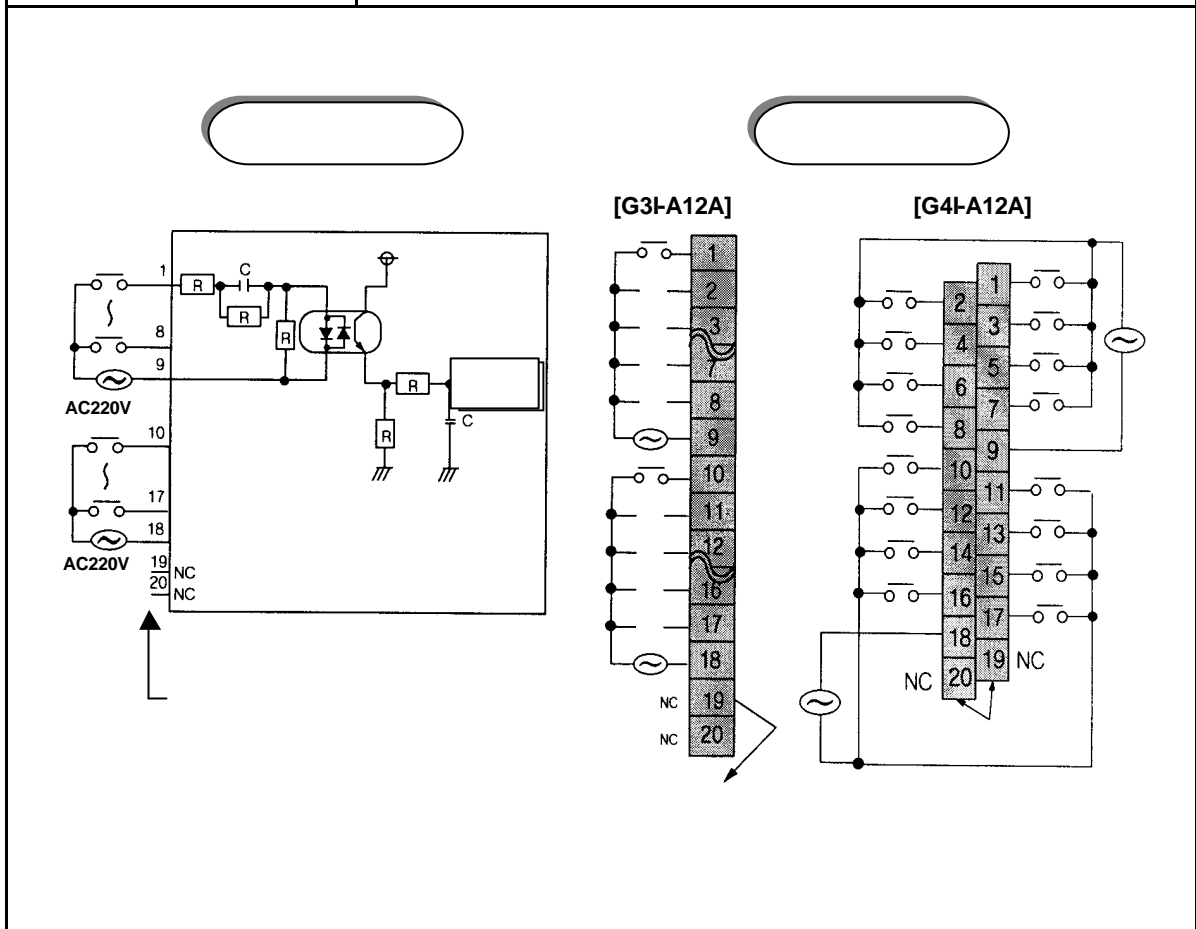
7.2.8 16 AC110V

		GM4
		G41?A12A
		16
		AC100V ~ 120V (50 / 60 Hz)
		11 mA (AC110V / 60Hz)
		AC85 ~ 132V (50 / 60? 3 Hz)
		100% On (8 / 1COM)
		300mA 0.3 ms (AC132V)
On / On		AC80V / 6 mA
Off / Off		AC30V / 3 mA
		10 k?
	Off ? On	15 ms
	On ? Off	25 ms
		8 / 1COM
		70 mA
		On LED
		20 (M3 X 6)
		0.29 kg



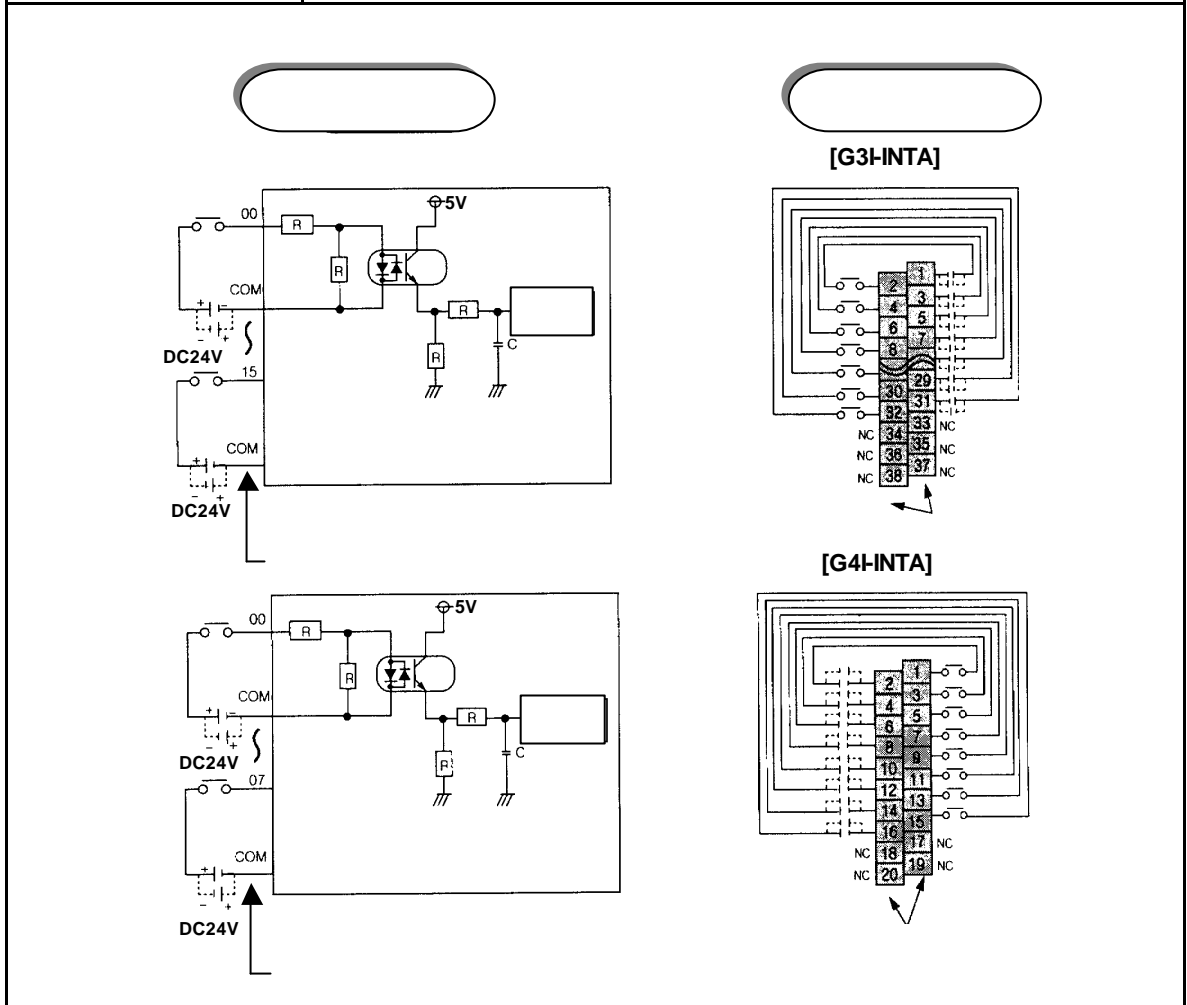
7.2.9 16 AC220V

		GM4	
		G41?A22A	
		16	
		AC200V ~ 240V (50 / 60 Hz)	
		11 mA (AC220V / 60Hz)	
		AC170 ~ 264V (50 / 60? 3 Hz)	
		100% On (8 / 1COM)	
		600mA 0.12 ms (AC264V)	
On	/ On	AC150V	/ 4.5 mA
Off	/ Off	AC50V	/ 3.0 mA
		20 k?	
	Off ? On	15 ms	
	On ? Off	25 ms	
		8 / 1COM	
		70 mA	
		On LED	
		20 (M3 X 6)	
		0.3 kg	



7.2.10

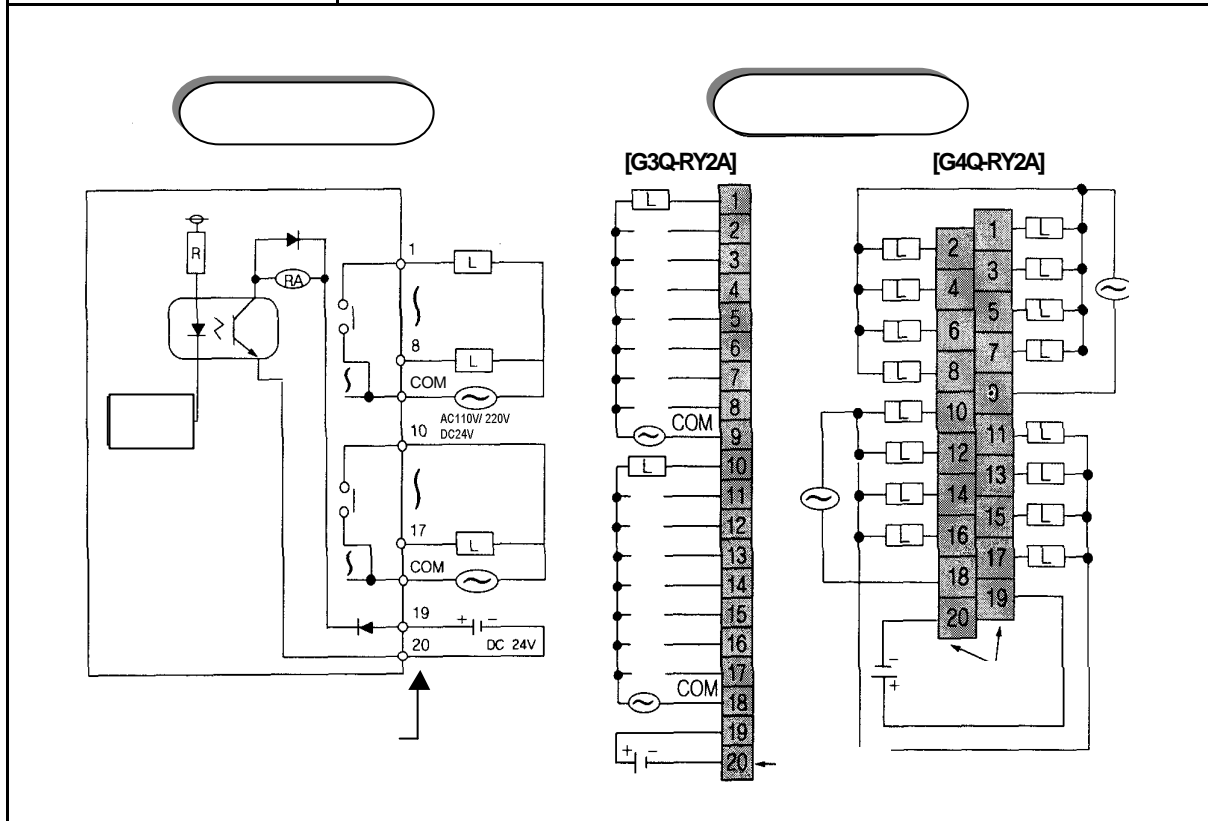
		GM4	
		G4F?INTA	
		8	
		DC24V	
		10 mA	
		2.4 k?	
		DC21.6 ~ 26.4V	
		100% On	
On / On		DC15V	/ 6.5 mA
Off / Off		DC5V	/ 2 mA
	Off ? On	0.5 ms	
	On ? Off	0.5 ms	
		1 / 1COM	
		()	
		65 mA	
		On LED	
		20 (M3 X 6)	
		0.16 kg	



7.3

7.3.1 16

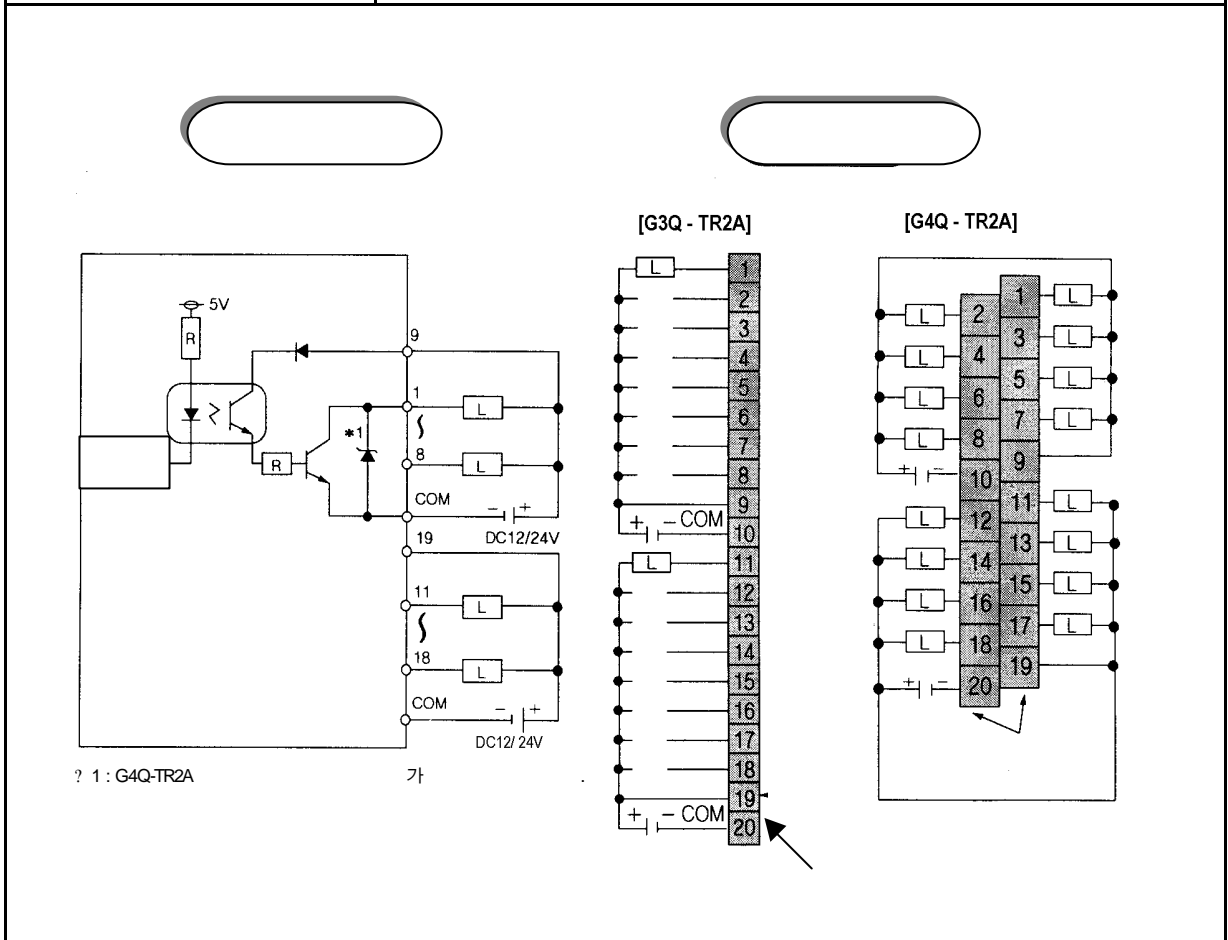
		GM4
		G4Q?RY2A
		16
/		DC24V 2A() / 1 , 4A / 1COM AC220V 2A(COS? = 1)
/		DC5V / 1mA
/		AC250V, DC125V
Off		0.1mA (AC220V, 60Hz)
		3,600 /
		2,000
		/ 10
		AC200V / 1.5A, AC240V / 1A (COS? = 0.7) 10
		AC200V / 1A, AC240V / 0.5A (COS? = 0.35) 10
		DC24V / 1A, DC100V / 0.1A (L / R = 7ms) 10
	Off ? On	10 ms
	On ? Off	12 ms
		8 / 1COM
		100 mA (On)
		DC24V ? 10% (4 Vp-p)
		100mA (DC24V On)
		On LED
		20 (M3 X 6)
		0.31 kg



7.3.2 16

()

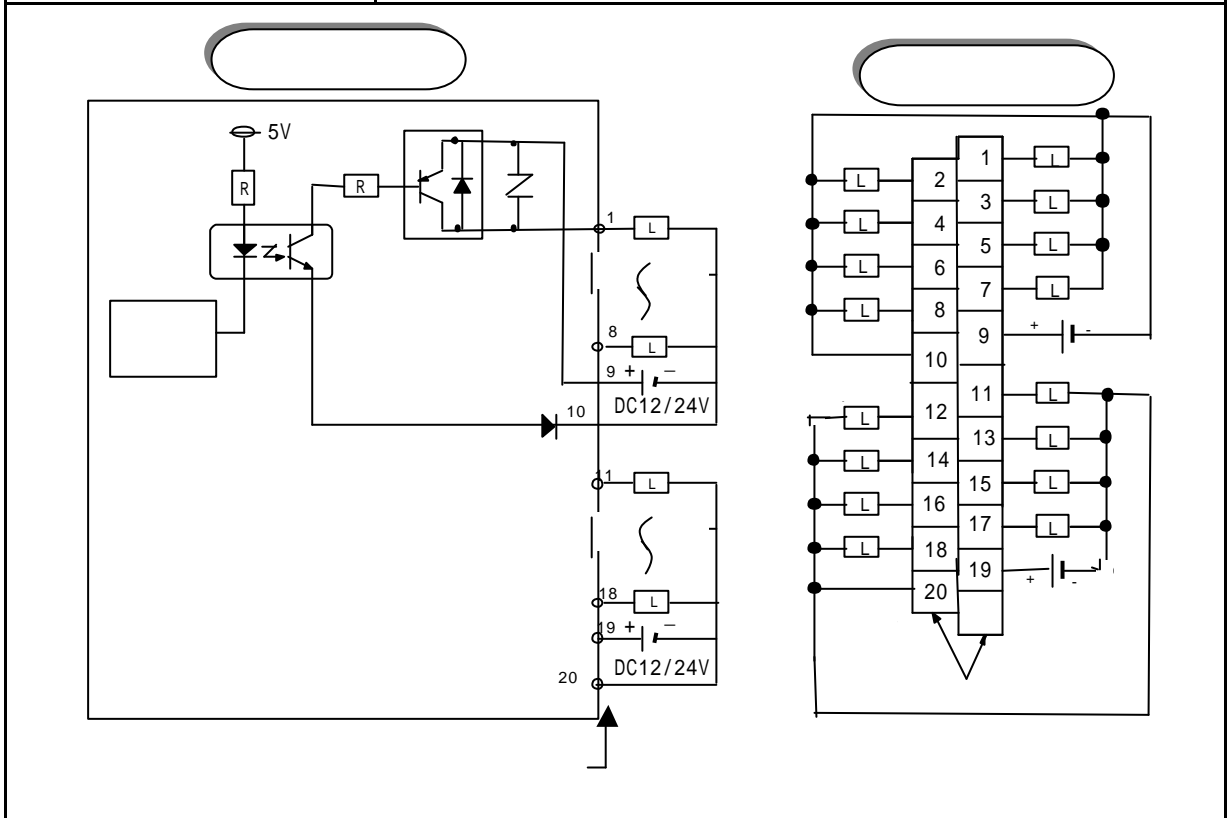
		GM4
		G4Q?TR2A
		16
		DC 12 / 24V
		DC 10.2 ~ 26.4V
		0.5A / 1 , 3A / 1COM
Off		0.1mA
		4A / 10 ms
On		DC 1.5V
	Off ? On	2 ms
	On ? Off	2 ms
		8 / 1COM
		110 mA (On)
		DC24V ? 10% (4 Vp-p)
		100mA (DC24V 1COM)
		On LED
		20 (M3 X 6)
		0.27 kg



7.3.3 16

()

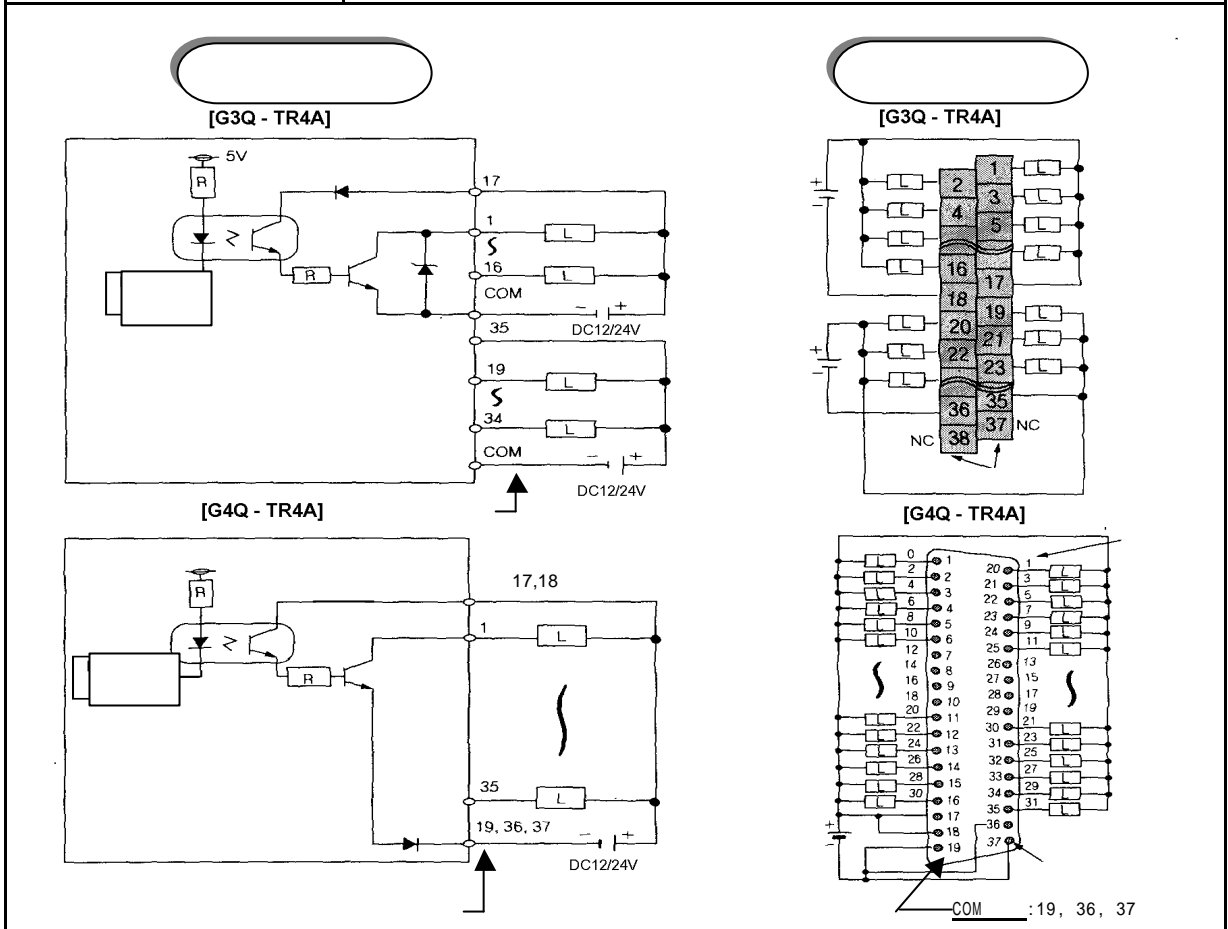
		GM4	
		G4Q?TR2B	
		16	
		DC 12 / 24V	
		DC 10.2 ~ 26.4V	
		0.5A / 1 , 3A / 1COM	
Off		0.1mA	
		4A / 10 ms	
On		DC 1.5V	
		Off ? On	2 ms
		On ? Off	2 ms
		8 / 1COM	
		110 mA (On)	
		DC24V ? 10% (4 Vp-p)	
		100mA (DC24V 1COM)	
		On LED	
		20 (M3 X 6)	
		0.27 kg	



7.3.4 32

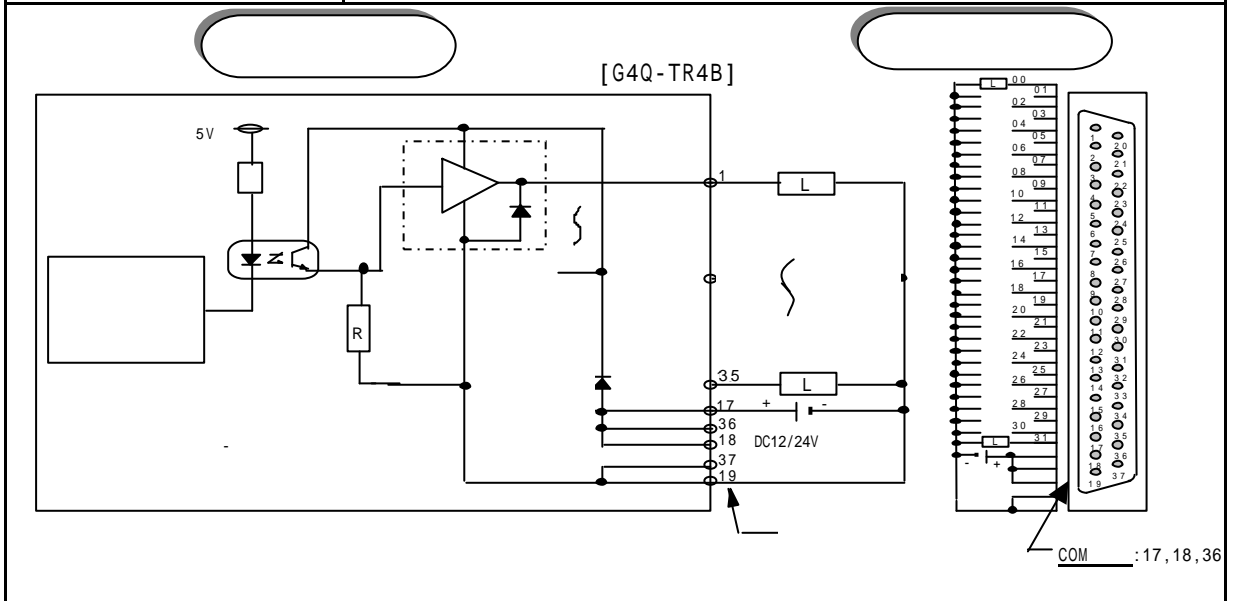
()

		GM4
		G4Q?TR4A
		32
		DC 12 / 24V
		DC 10.2 ~ 26.4V
		0.1A / 1 , 2A / 1COM
Off		0.1 mA
		4 A / 10 ms
On		DC 1.0 V
	Off ? On	2 ms
	On ? Off	2 ms
		32 / 1 COM
		160 mA (On)
	DC 24V ? 10 % (4Vp-p)	
		100 mA (DC 24V 1 COM)
		16
		37 D-Sub
		0.18 kg



7.3.5 32 ()

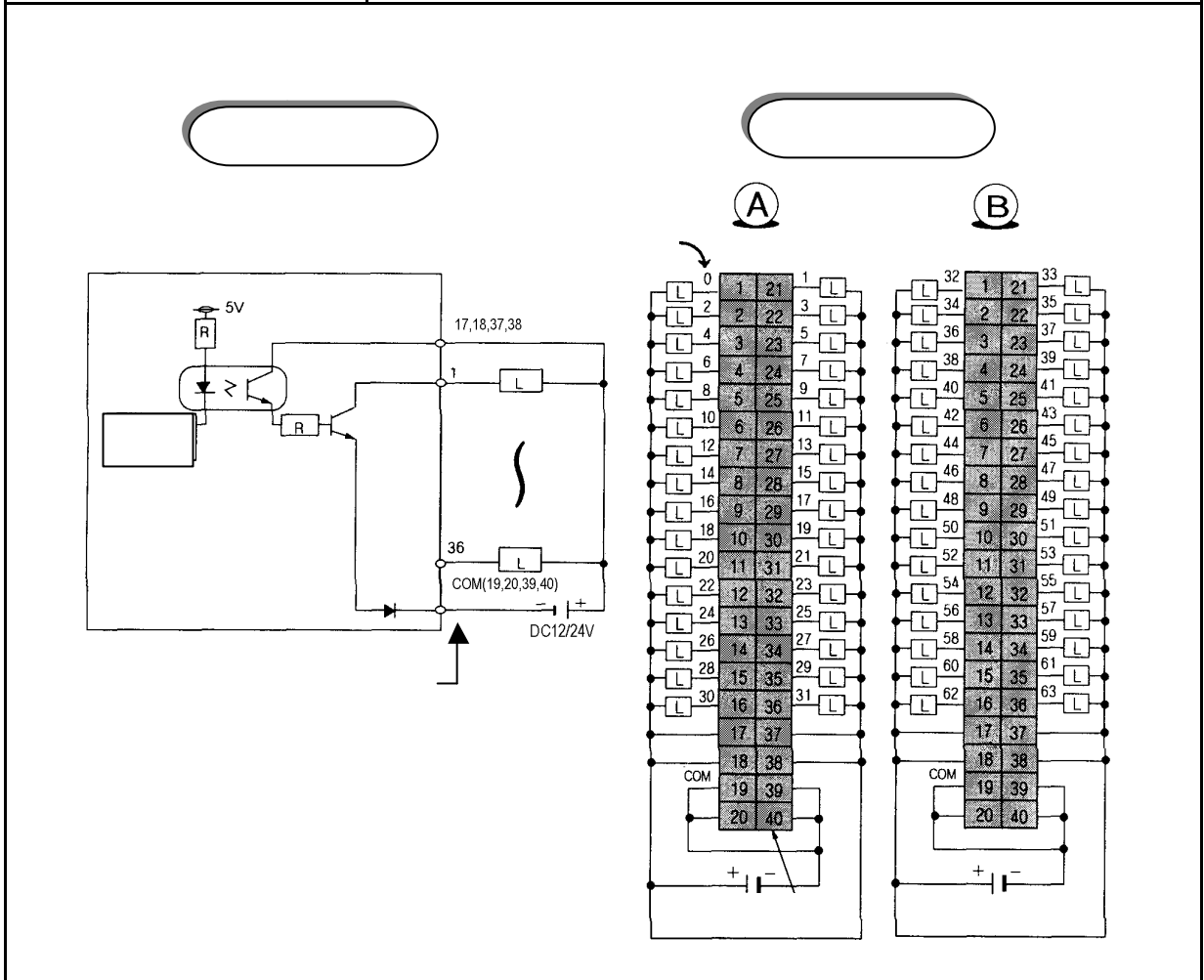
		GM4	
		G4Q?TR4B	
		32	
		DC 12 / 24V	
		DC 10.2 ~ 26.4V	
		0.1A / 1 , 2A / 1COM	
Off		0.1 mA	
		4 A / 10 ms	
On		DC 1.0 V	
		Off ? On	2 ms
		On ? Off	2 ms
		32 / 1 COM	
		160 mA (On)	
		DC 24V ? 10 % (4Vp-p)	
		100 mA (DC 24V 1 COM)	
		16	
		37 D-Sub	
		0.18 kg	



7.3.6 64

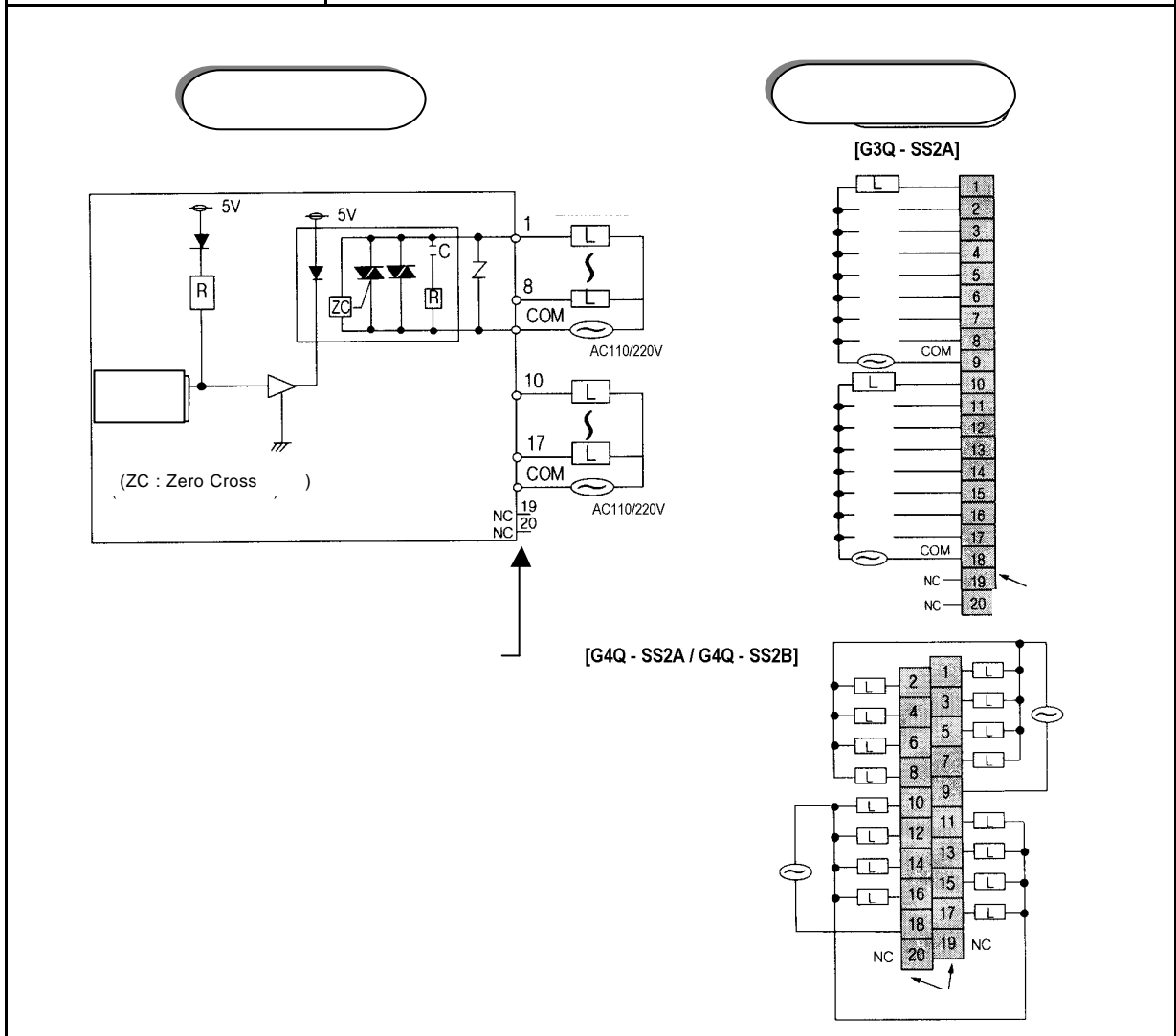
()

		GM4	
		G4Q?TR8A	
		64	
		DC 12 / 24V	
		DC 10.2 ~ 26.4V	
		0.1A / 1 , 2A / 1COM	
Off		0.1mA	
		0.4 A / 10 ms	
On		DC 1.0V	
	Off ? On	2 ms	
	On ? Off	2 ms	
		32 / 1COM	
		300 mA (On)	
		DC10.2 ~ 26.4V	
		100mA (DC24V 1COM)	
		16	
		40 2	
		0.42 kg	



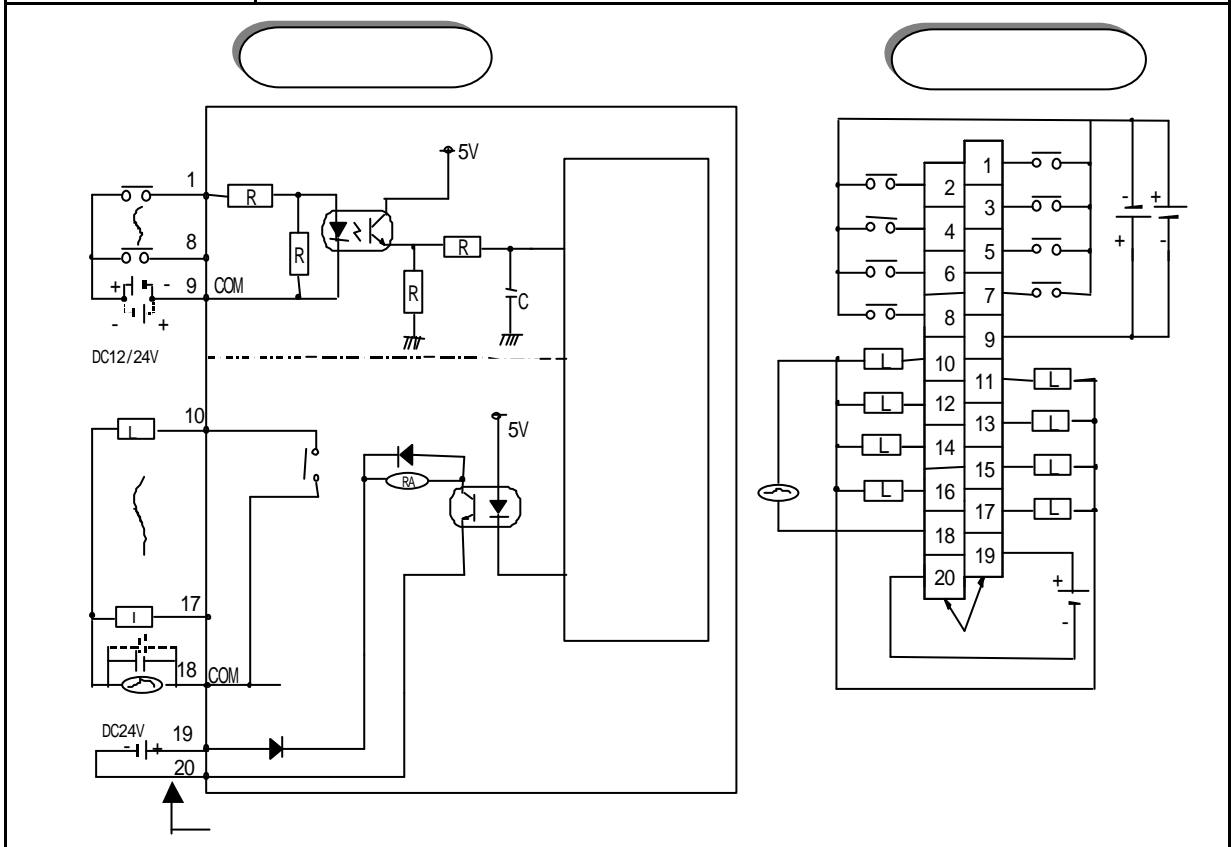
7.3.7 16

		GM4	
		G4Q?SS2A	G4Q?SS2B
		16	
		AC 100 ~ 240V (50 / 60 Hz)	
		AC 264V	
		1A/1 , 5A/1COM	0.6A/1 , 2.4A/1COM
		20 mA	10 mA
Off		2.5 mA (AC 220V 60 Hz)	
		25A, 10 ms	20A, 10 ms
On		AC 1.5V (1A)	AC 1.5V (0.6A)
		(387 ~ 473V), C.R	
	Off ? On	0.5 Cycle + 1 ms	
	On ? Off	0.5 Cycle + 1 ms	
		8 / 1 COM	
		330 mA (On)	
		On LED	
		20 (M3 X 6)	
		0.35 kg	



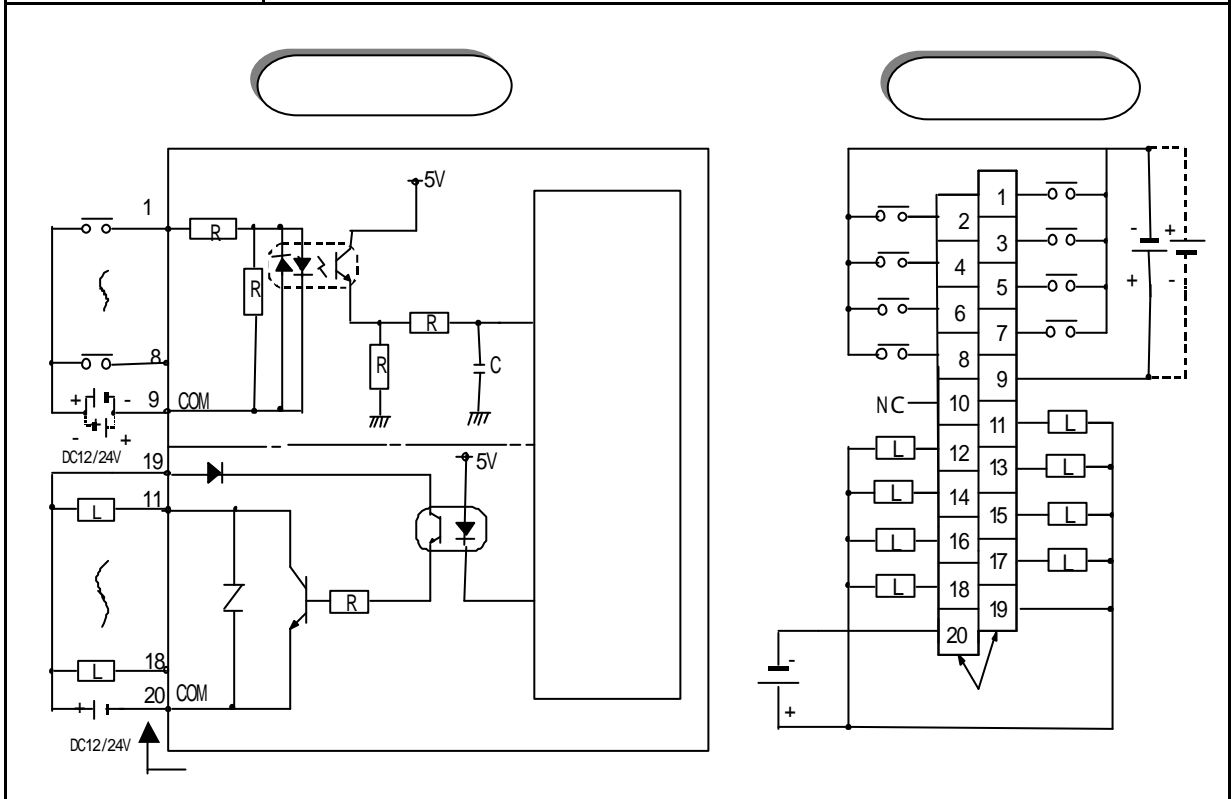
7.3.8 8 DC12/24V / 8

GM4			
G4H-DR2A			
	8		8
	DC12 / 24V	/	DC24V 2A()/1 , 4A / 1COM AC220V 2A(COS? = 1)
	5 / 11 mA	/	DC5V / 1 mA
	DC10.2 ~ 26.4V (5%)	/	AC250V, DC125V
	100% On	Off	0.1 mA (AC220V, 60Hz)
On /On	DC9.5V / 4.0 mA		3,600 /
Off /Off	DC6V / 1.0 mA		
	2.2 k?		G4Q-RY2A
Off? On	10 ms		
On ? Off	10 ms	Off ? On	10 ms
	8 / 1COM	On ? Off	12 ms
	On LED		8 / 1COM
-	-		DC24V ± 10% (4 Vp-p)
-	-		45 mA
-	-		On LED
	20 (M3 × 6)		
	100 mA		
	0.26 kg		



7.3.9 8 DC12/24V / 8

GM4			
G4H-DT2A			
	8		8
	DC12 / 24V		DC12 / 24V
	5 / 11 mA		DC10.2 ~ 26.4V
	DC10.2 ~ 26.4V (5%)		0.5 A / 1 3 A / 1COM
	100% On	Off	0.1 mA
On / On	DC9.5V / 4.0 mA		4 A / 10 ms
Off / Off	DC6V / 1.0 mA	On	DC1.5V
	2.2 k Ω		
	Off? On	10 ms	Off ? On
	On ? Off	10 ms	On ? Off
	8 / COM		8 / 1COM
	On LED		DC24V \pm 10% (4 Vp-p)
-	-		50 mA
-	-		On LED
	20 (M3 \times 6)		
	100 mA		
	0.26 kg		



8.1

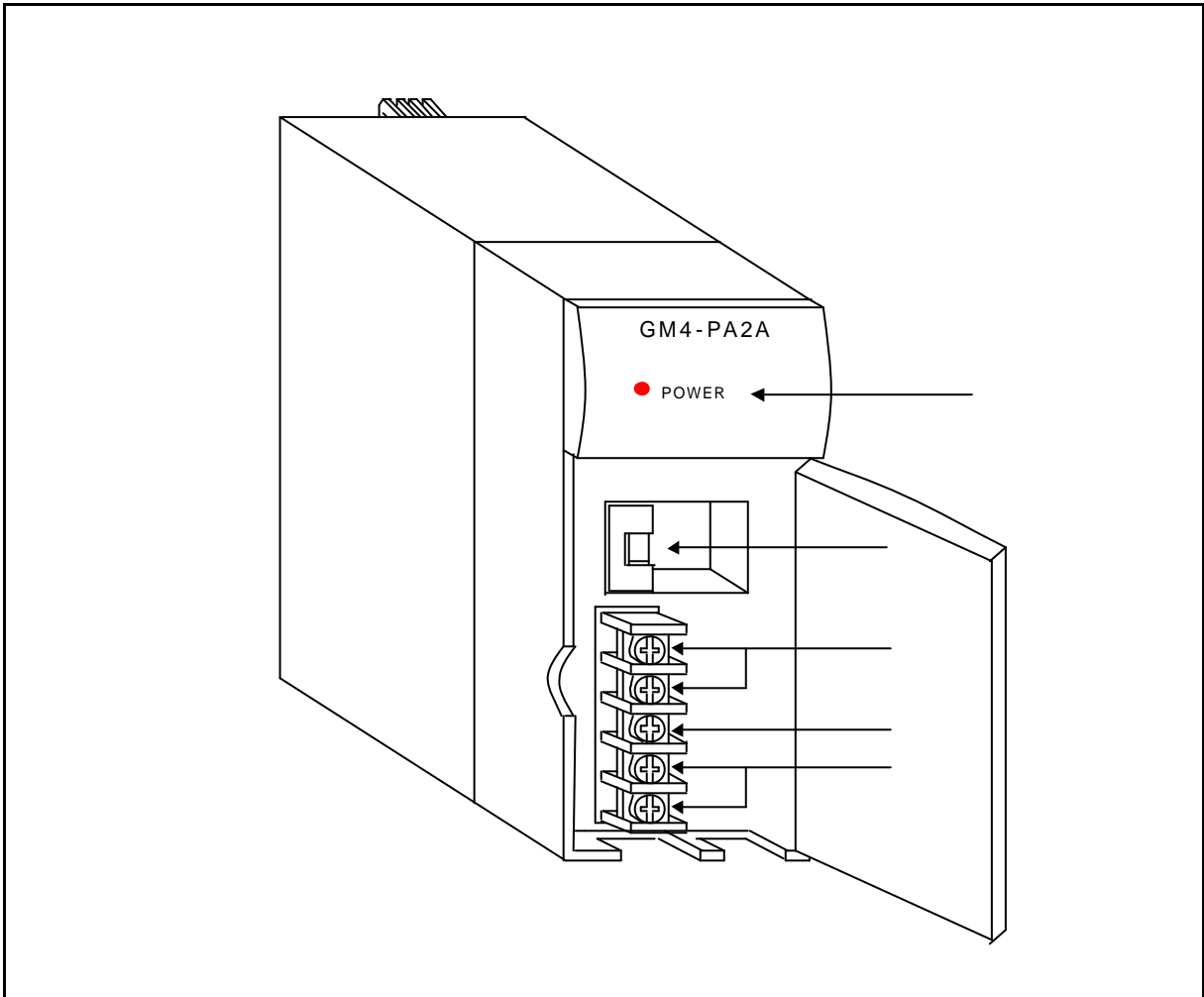
GM4			(: mA)		
CPU	GM4-CPUA	130	A/D	G4F-AD2A	400
	GM4-CPUB	130		G4F-AD3A	500
	GM4-CPUC	700		G4F-DA1A	450
DC12/24V	G4I-D22A	70	D/A	G4F-DA2V	400
	G4I-D22B	125		G4F-DA2I	680
	G4I-D22C	125		G4F-DA3V	700
	G4I-D24A	125		G4F-DA3I	60
	G4I-D24B	70		G4F-HSCA	300
	G4I-D24C	70		G4F-HO1A	400
	G4I-D28A	100		G4F-HD1A	450
AC110V	G4I-A12A	70		G4F-POPA	400
AC220V	G4I-A22A	70		G4F-POPB	400
	G4Q-RY2A	100		G4F-PP10	730
	G4Q-TR2A	120		G4F-PP20	760
	G4Q-TR2B	120		G4F-PP30	770
	G4Q-TR4A	160		G4F-PP1D	700
	G4Q-TR4B	160		G4F-PP2D	720
	G4Q-TR8A	250		G4F-PP3D	740
	G4Q-SS2A	330		G4F-TC2A	450
	G4Q-SS2B	330		G4F-RD2A	600
	G4H-DR2A	100	PID	G4F-PIDA	200
	G4H-DT2A	100		G4F-PIDB	600
	G4F-INTA	65		G4F-AT3A	200
	G4F-TMCA	360	Fnet I/F	G4L-FUEA	160

Fnet I/F	G4L-RBEA	150	Cnet I/F	G4L-CUEA	100
Rnet I/F	G4L-RUEA	160	Enet I/F	G4L-EUEA	270
Pnet I/F	G4L-PUEB	680	Dnet I/F	G4L-DUEA	270
Enet I/F	G4L-EUTB	600	Enet I/F	G4L-EUTB	600

8.2

	GM4? PA1A	GM4? PA2A	GM4? PA1B	GM4? PA2B	GM4? PA2C	GM4-PD3A
	AC110V (85 ~ 132V)	AC220V (170 ~ 264V)	AC110V (85 ~ 132V)	AC220V (170 ~ 264V)	AC220V (170 ~ 264V)	DC24V
	50 / 60 Hz (47 ~ 63 Hz)					-
	1.3A (AC110V)	0.8A (AC220V)	0.65A (AC110V)	0.35 A (AC220V)	0.6 A (AC220V)	1.2A (DC24V)
	40 A				60 A	100 A
	65% (/)					
	3 A / Slow / AC250V					5A / Slow / AC250V
	20 ms				10 ms	1 ms
(1)	DC5V					
	4 A		3 A		8 A	4 A
	4.4 A		3.2 A		8.8A	4.4 A
(2)	DC24V					-
	0.7 A		0.5 A		-	-
	0.8 A		0.6 A		-	-
	LED On					
	0.75 ~ 2 m ²					
	0.4 kg					

8.3



NO.		
1	LED	DC5V LED
2		AC
3		AC : AC 110V AC 220V DC : DC 24V
4	LG	
5	DC24V ,24G	DC 24V 가



9.1

9.1.1

	GM4?B04M GM4-B4MH	GM4?B06M GM4-B6MH	GM4?B08M GM4-B8MH	GM4?B12M
	4	6	8	12
(mm)	297 X 135 X 17	367 X 135 X 17	437 X 135 X 17	540 X 135 X 17
	? 4.5 (M4)			
FG	BHM X 6			
(kg)	0.85	1.1	0.73	0.85

1) GM4?B12M

9.1.2

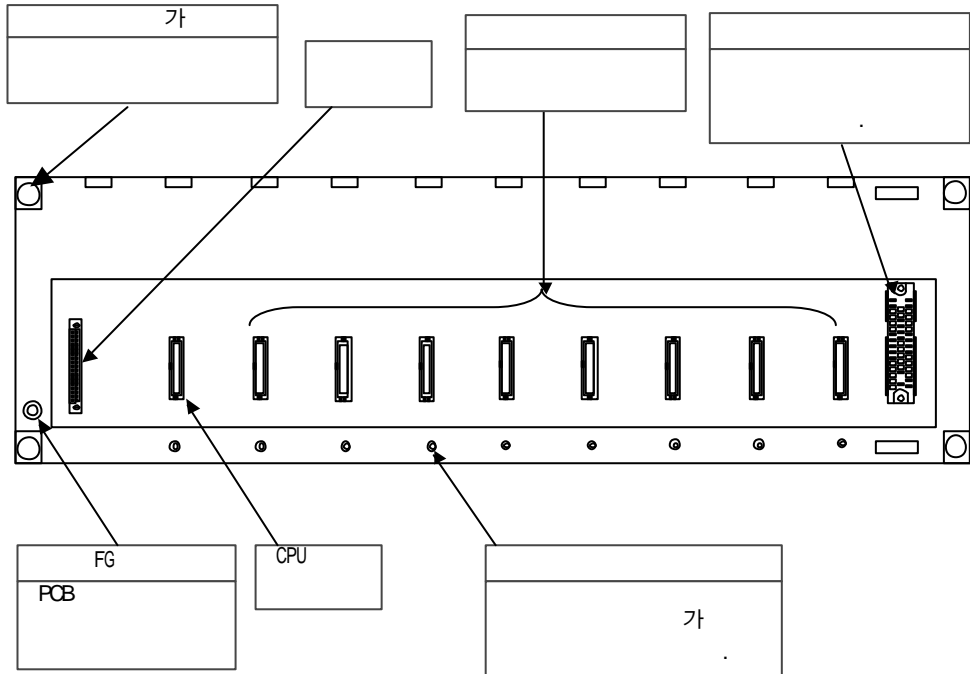
	GM4-B04E GM4-B4EH	GM4-B06E GM4-B6EH	GM4-B08E GM4-B8EH
	4	6	8
(mm)	297 X 135 X 17	367 X 135 X 17	437 X 135 X 17
	? 4.5 (M4)		
FG	BHM 3 X 6		
(kg)	0.9	1.15	1.4

9.1.3

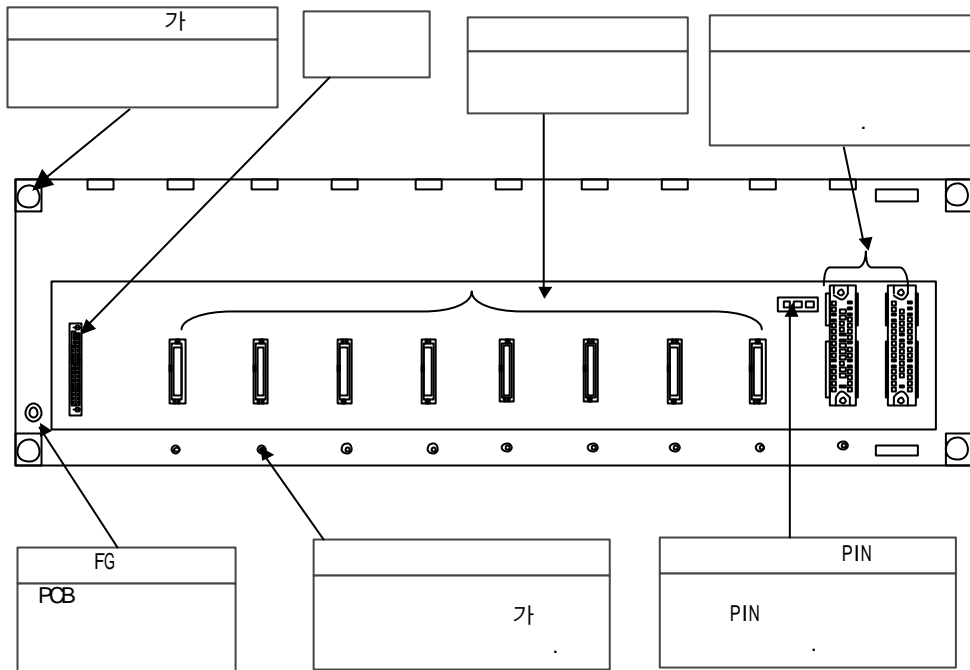
	G4C-E041	G4C-E061	G4C-E121	G4C-E301	G4C-E601	G4C-E102	G4C-E152
(m)	0.4	0.6	1.2	3.0	6.0	10.0	15.0
(kg)	0.21	0.32	0.52	1.09	1.86	2.98	4.39

9.2

9.2.1



9.2.2



10

10.1

10.1.1

가

1)

- (1) 가
- (2) 가
- (3)
- (4)
- (5) 가 0 ~ 55C
- (6) 가 5 ~ 95%
- (7) 가 가 가 가
- (8) 가

2)

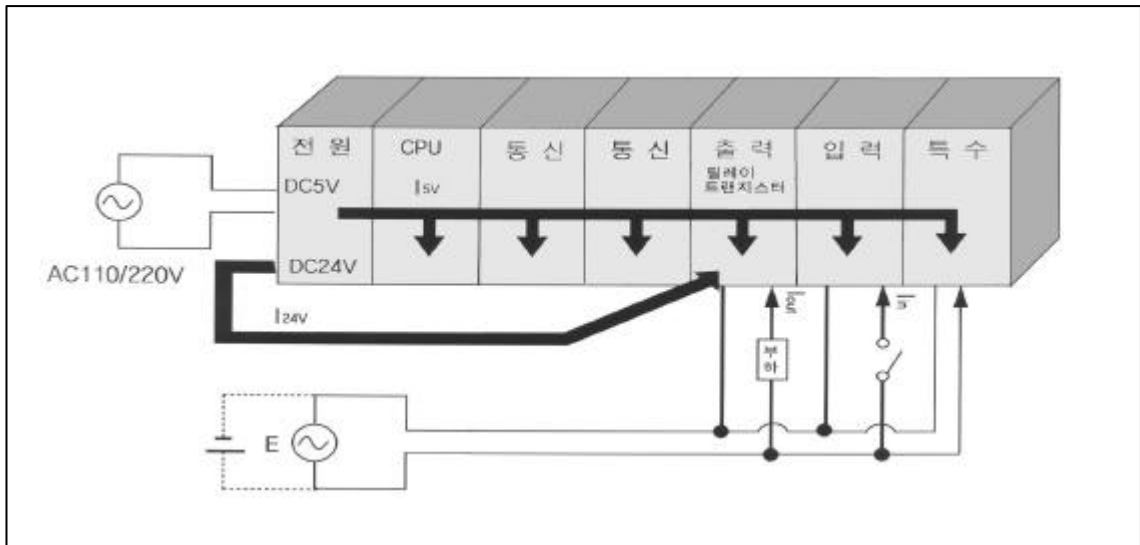
- (1) 가 PLC 가 가
- (2)
- (3) (Panel)
- (4) 50mm
- (5)

3)

- (1) PLC PLC
- , 가 PLC
- (2)

PLC

1) PLC



2)

(1)

70% , 30%

3/7

? $W_{pw} = 3/7 \{ (I_{5V} \times 5) + (I_{24V} \times 24) \}$ (W)

I_{5V} : DC5V ()

I_{24V} : DC24V (On)

DC24V DC24V

(2) DC5V

DC5V

? $W_{5V} = I_{5V} \times 5$ (W)

(3) DC24V

(On)

DC24V

? $W_{24V} = I_{24V} \times 24$ (W)

(4)

(On)

? $W_{out} = I_{out} \times V_{drop} \times \dots \times On$ (W)

I_{out} : () (A)

V_{drop} : (V)

(5)
$$W_{in} = I_{in} \times E \times \text{On} \quad (\text{W})$$

$$\left\{ \begin{array}{l} I_{in} : \quad (\quad) \text{ (A)} \\ E : \quad (\quad) \text{ (V)} \end{array} \right.$$

(6)
$$W_s = I_{5V} \times 5 + I_{24V} \times 24 + I_{100V} \times 100 \quad (\text{W})$$

PLC

$$W = W_{PW} + W_{5V} + W_{24V} + W_{out} + W_{in} + W_s \quad (\text{W})$$

$$T = W / UA \quad [^\circ\text{C}]$$

$$\left\{ \begin{array}{l} W : \text{PLC} \quad (\quad) \\ A : \quad [m^2] \\ U : \quad \text{?????????} \end{array} \right. \quad \begin{array}{l} \text{?????????} \end{array}$$

10.1.2

?
 ? PCB 가
 ?

1)

(1)

가

(2)

(0.3mm²)

AWG22

(3)

가

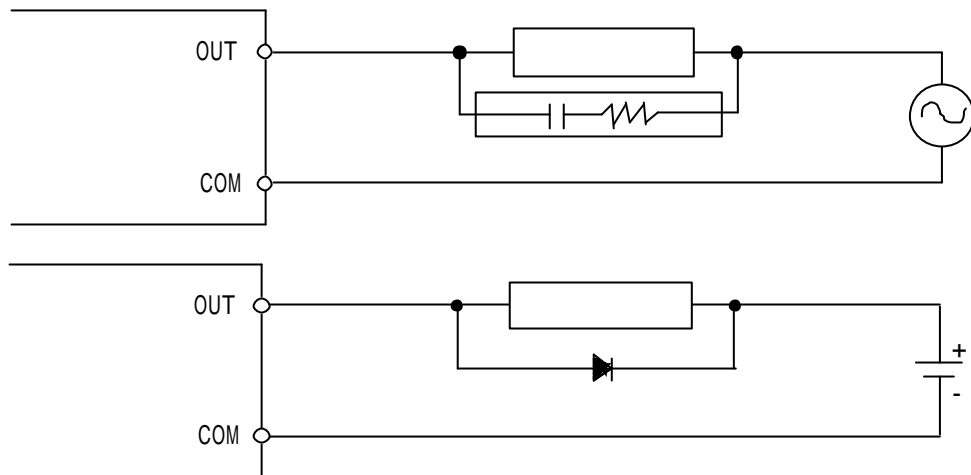
(4)

가

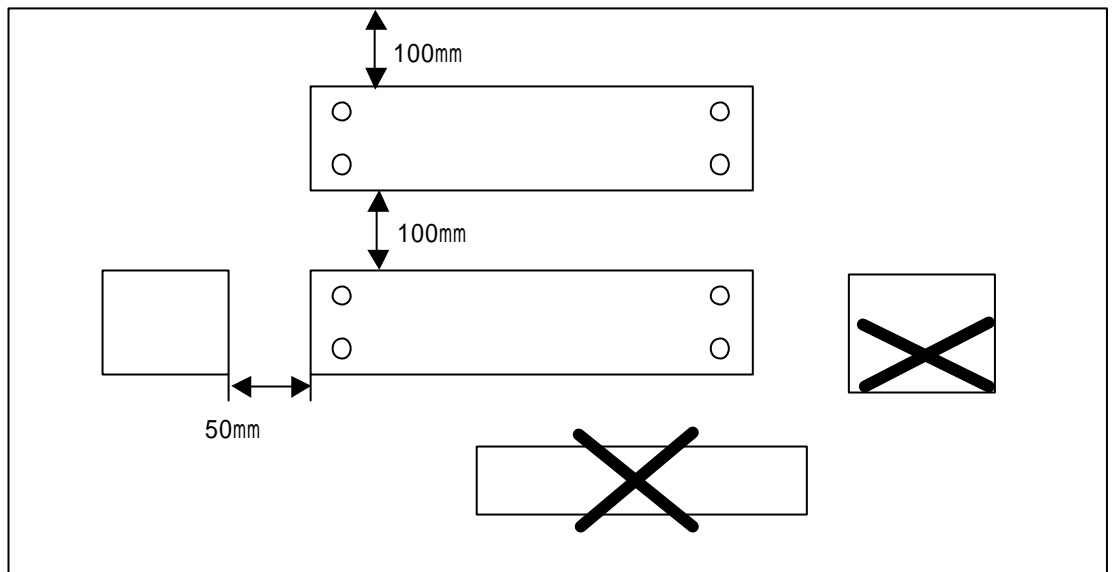
(5)

?

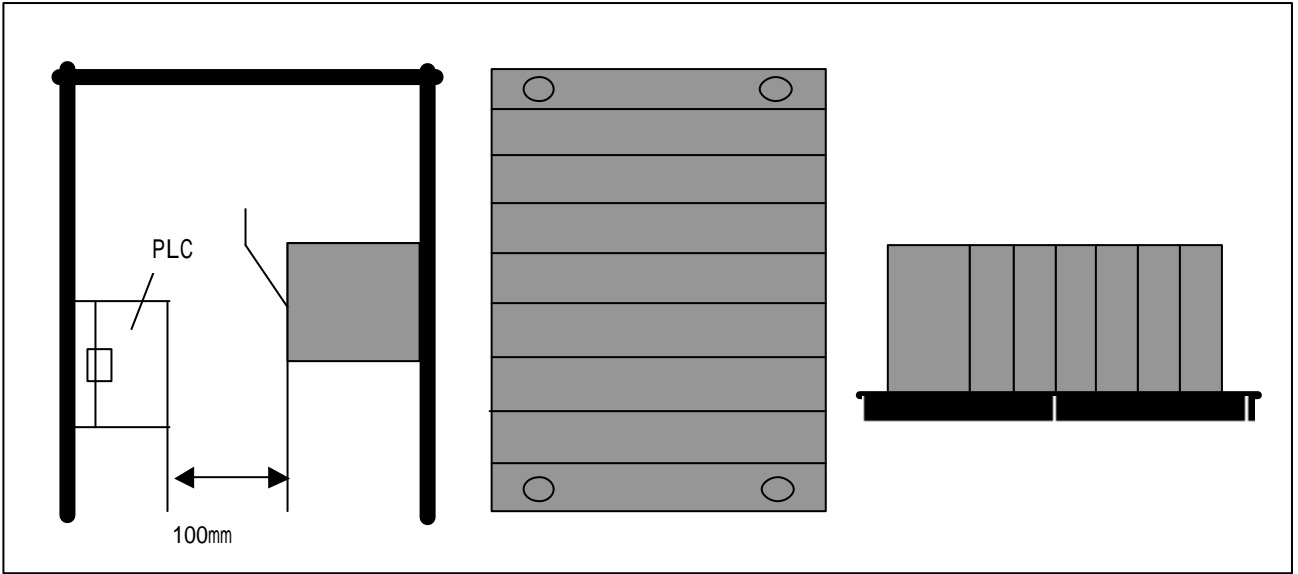
? (LED) 가
 ? (가 ,) (Surge Killer)
 ? 가 ,
 +



- (6) , 가 가 PLC
- (7) 가 , PCB
- 2) PLC
- (1)
- (2)
- (3) (Panel)
- (4) , PLC 가 10.1
 ? PLC PLC 50mm (Hook)
 ? PLC
- (5)) 100mm PLC 가 (50mm



[10.1] PLC



[10.2]

[10.3]

(가)

[10.4]

(가)

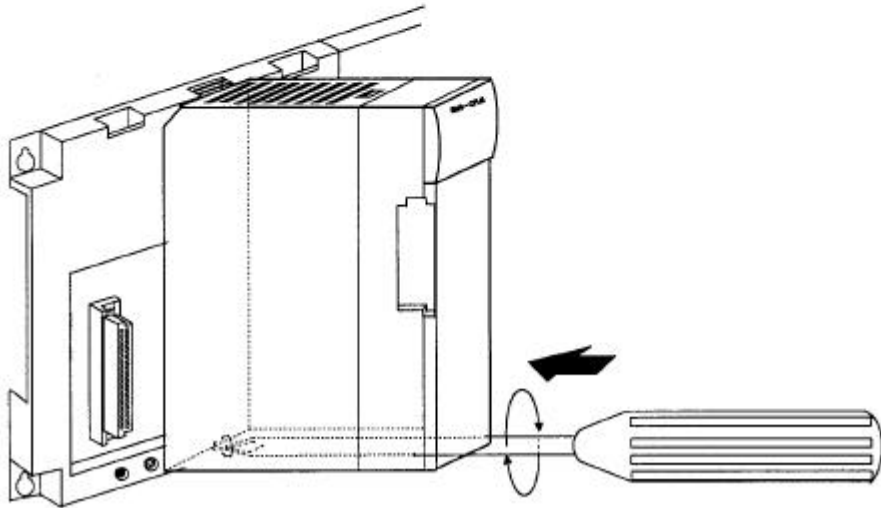
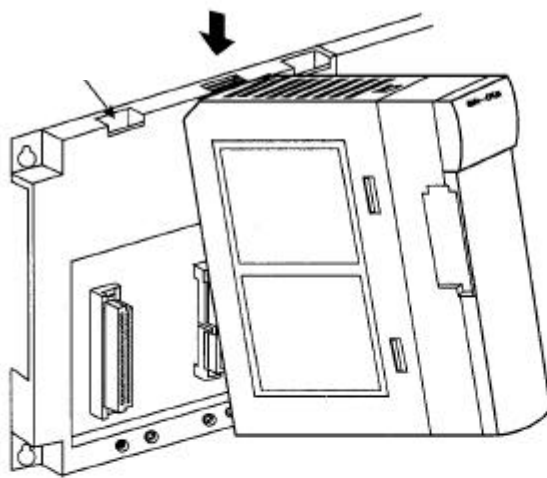
10.1.3

1)

?

?

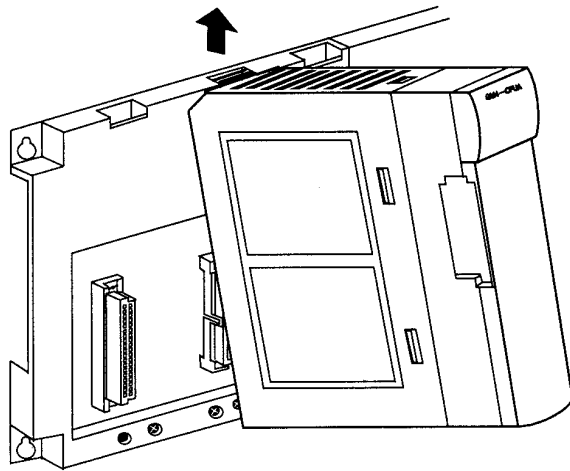
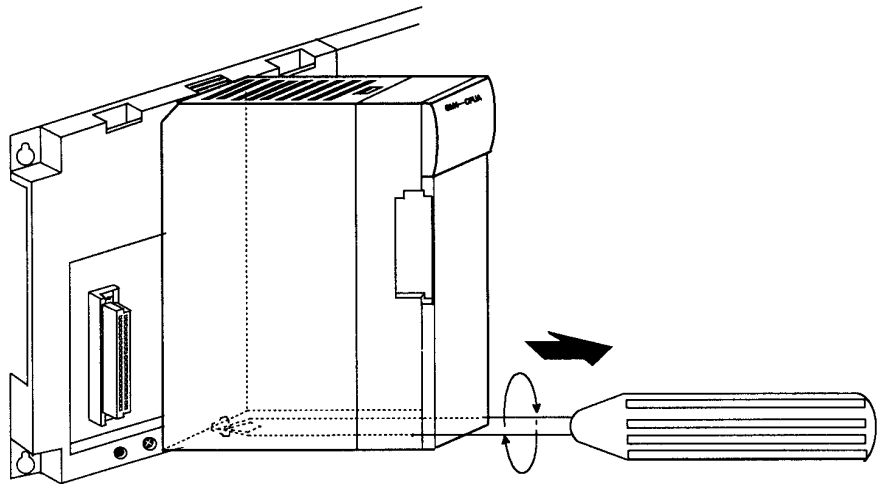
?



1)

2)

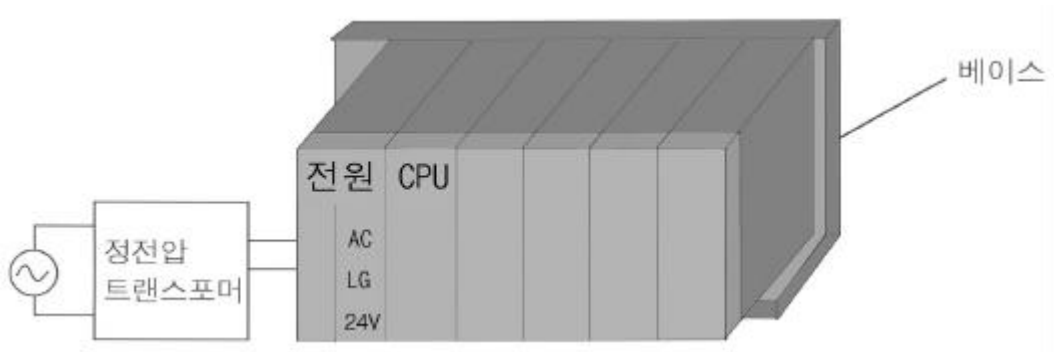
?
?



10.2

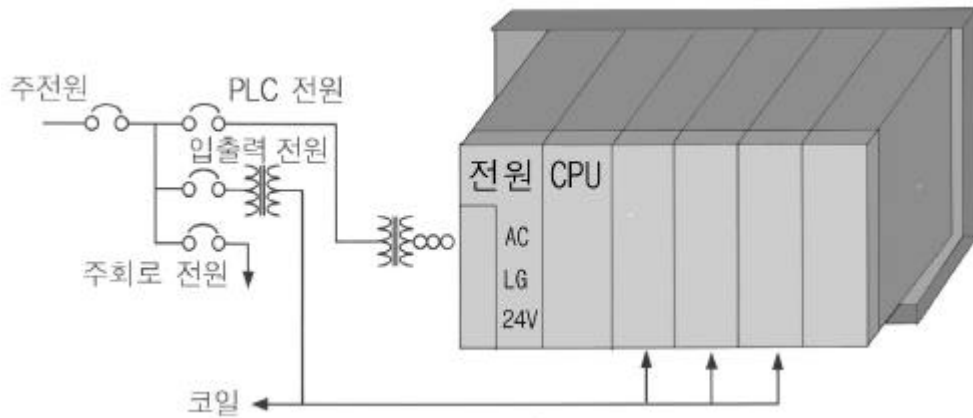
10.2.1

1)



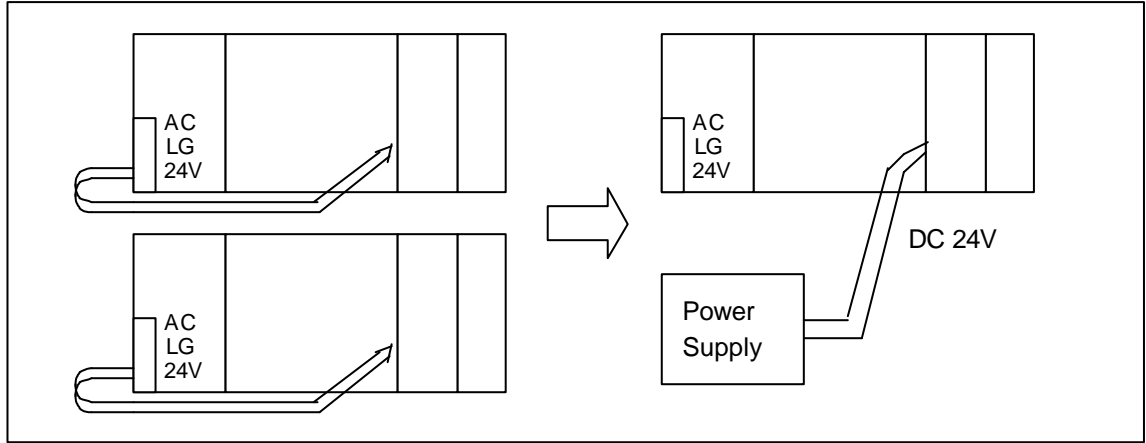
2) (가 가 .)

3) PLC



4) DC24V

DC24V
1 DC24V DC24V

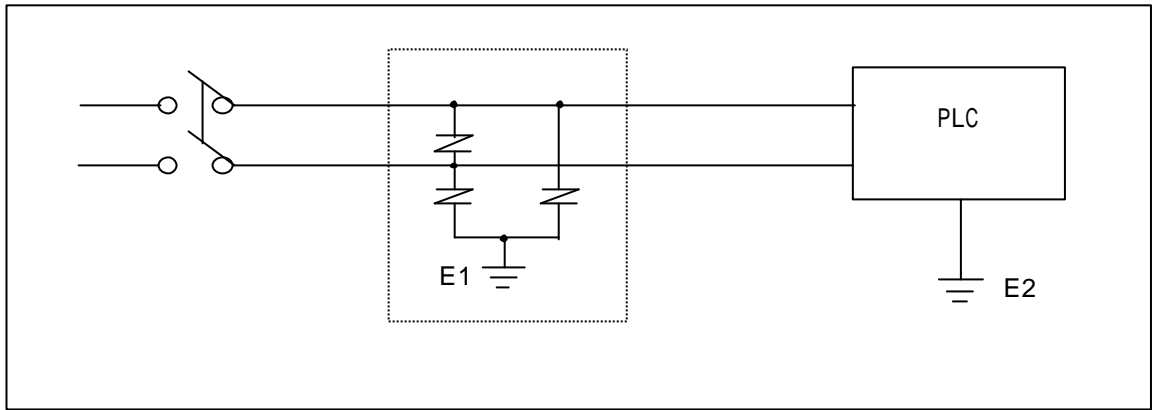


5) AC110V , AC220V , DC24V 가

6) AC110V , AC220V 가 (2mm²)

7) AC110V , DC24V (,) ,
가 100mm

8)



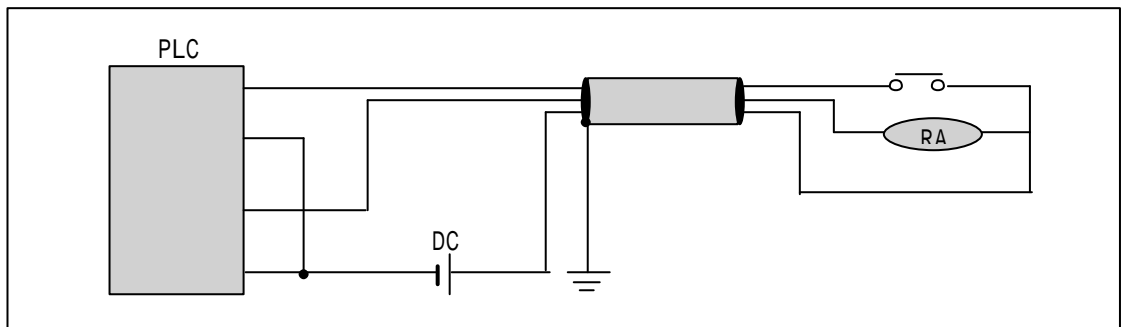
1) (E1) PLC (E2)
 2)

9) 가

10) 가

10.2.2

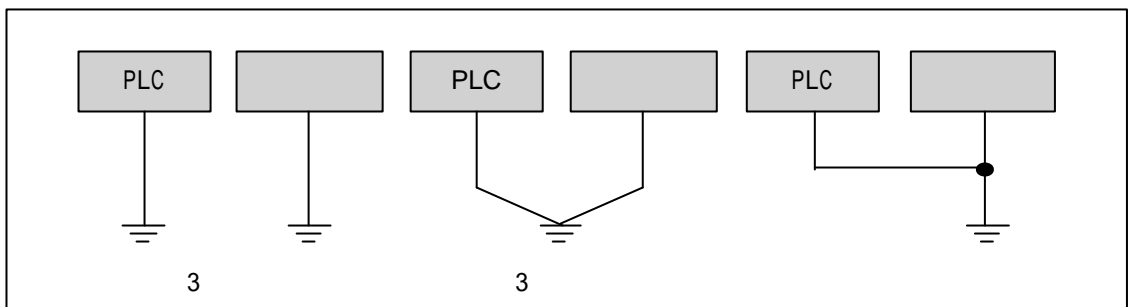
- 1) $0.3 \sim 2 \text{ mm}^2$, (0.3 mm^2)
- 2) .
- 3) ✂ 100mm .
- 4) , PLC



- 5) .
- 6) DC24V AC110V AC220V .
- 7) 200m
12 12.4 .

10.2.3

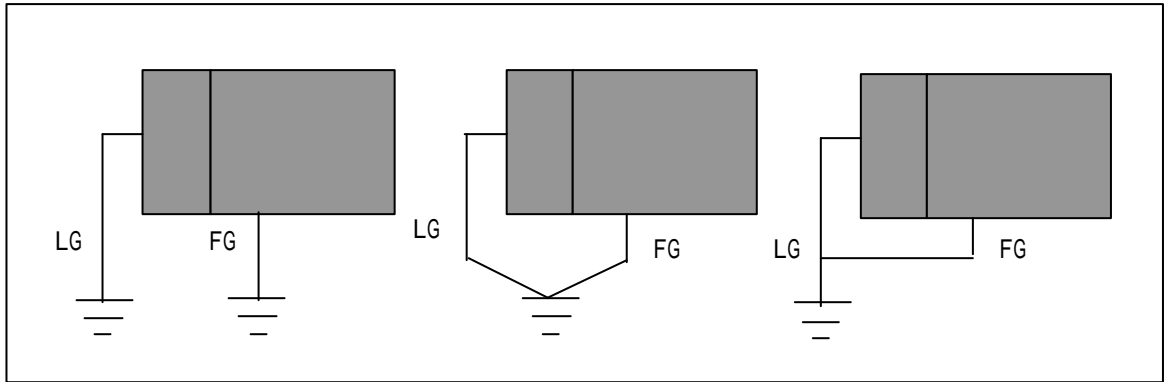
- 1) PLC , 가
- 2) 가 3 (100 ?) .
- 3))



가) : 가) :) :

4) 2 mm² 가 PLC

5) LG FG



가) : 가) :) :

6) FG

10.2.4

	(mm ²)	
	0.18 (AWG24)	1.5 (AWG16)
	0.18 (AWG24)	2.0 (AWG14)
	0.18 (AWG24)	1.5 (AWG16)
	0.18 (AWG24)	1.5 (AWG16)
	1.5 (AWG16)	2.5 (AWG12)
	1.5 (AWG16)	2.5 (AWG12)

11



PLC

11.1

6 1~2

		0 ~ + 55C	가
		5 ~ 95%RH	
		? 15% / 10%	

11.2

		? 가 가		
		? 가		
			가	
LED	LED		()	12
	Run LED	Run	()	12
	Stop LED	Run		12
	LED	,	On Off	12
	LED	,	On Off	12

11.3

6 1~2

		/ 가	0 ~ 55°C	()
			5 ~ 95%RH	
			가 가	
PLC	,	.		
	,			
			?AC110V : AC85 ~ 132V ?AC220V : AC170 ~ 264V ?DC24V : DC20 ~ 28V	
		,	? 가 ? 가	가
			?	가

12

12.1

가

가

1)

? 가 (,)
 ? 가
 ?
 ? (,)
 ? (Power LED, Run LED, Stop LED, LED)
 PLC

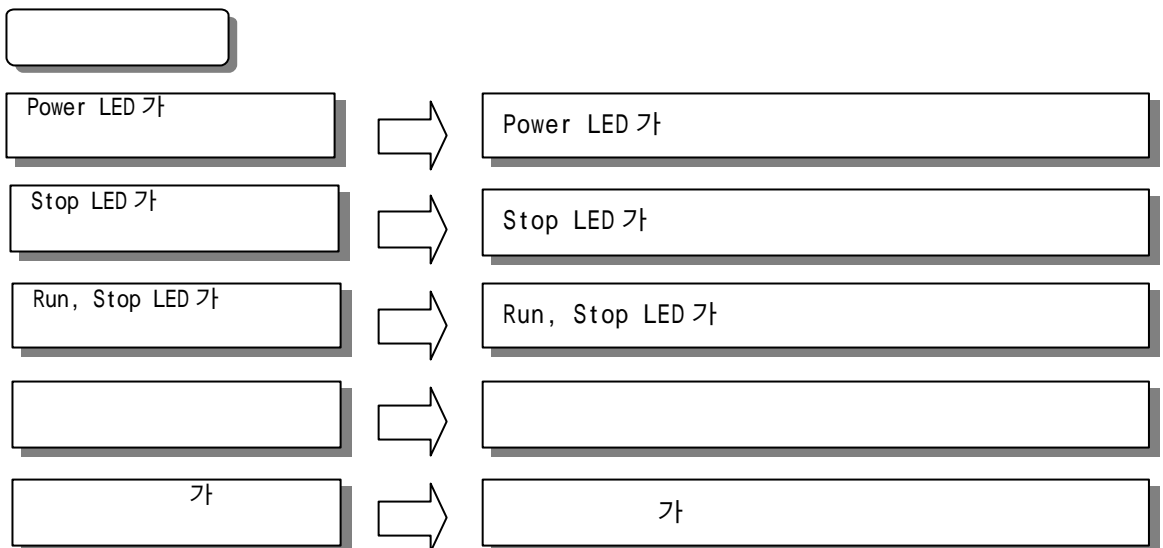
2)

? Stop 가 On / Off

3)

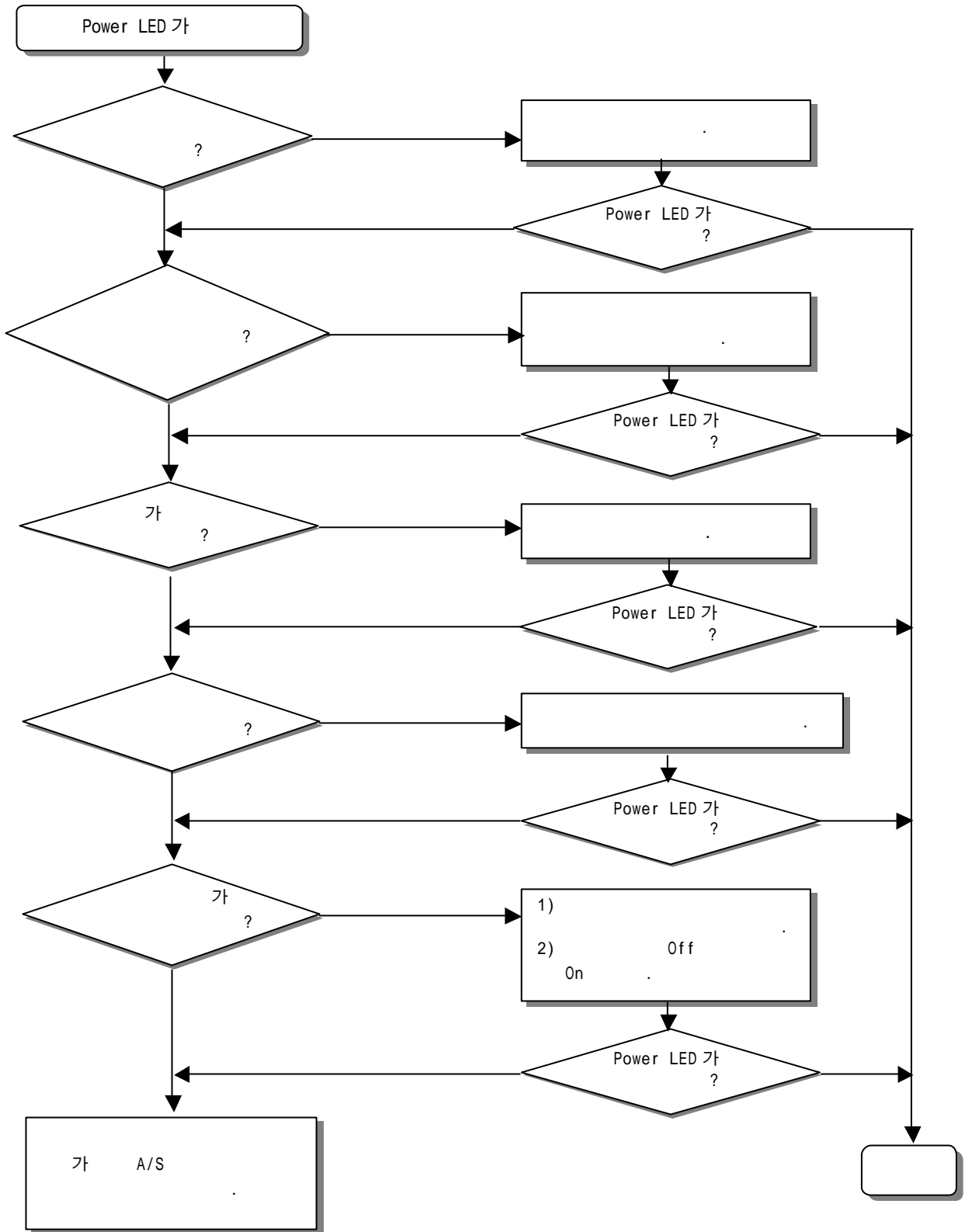
? PLC 가? 가?
 ? 가? 가?
 ? PLC 가?

12.2



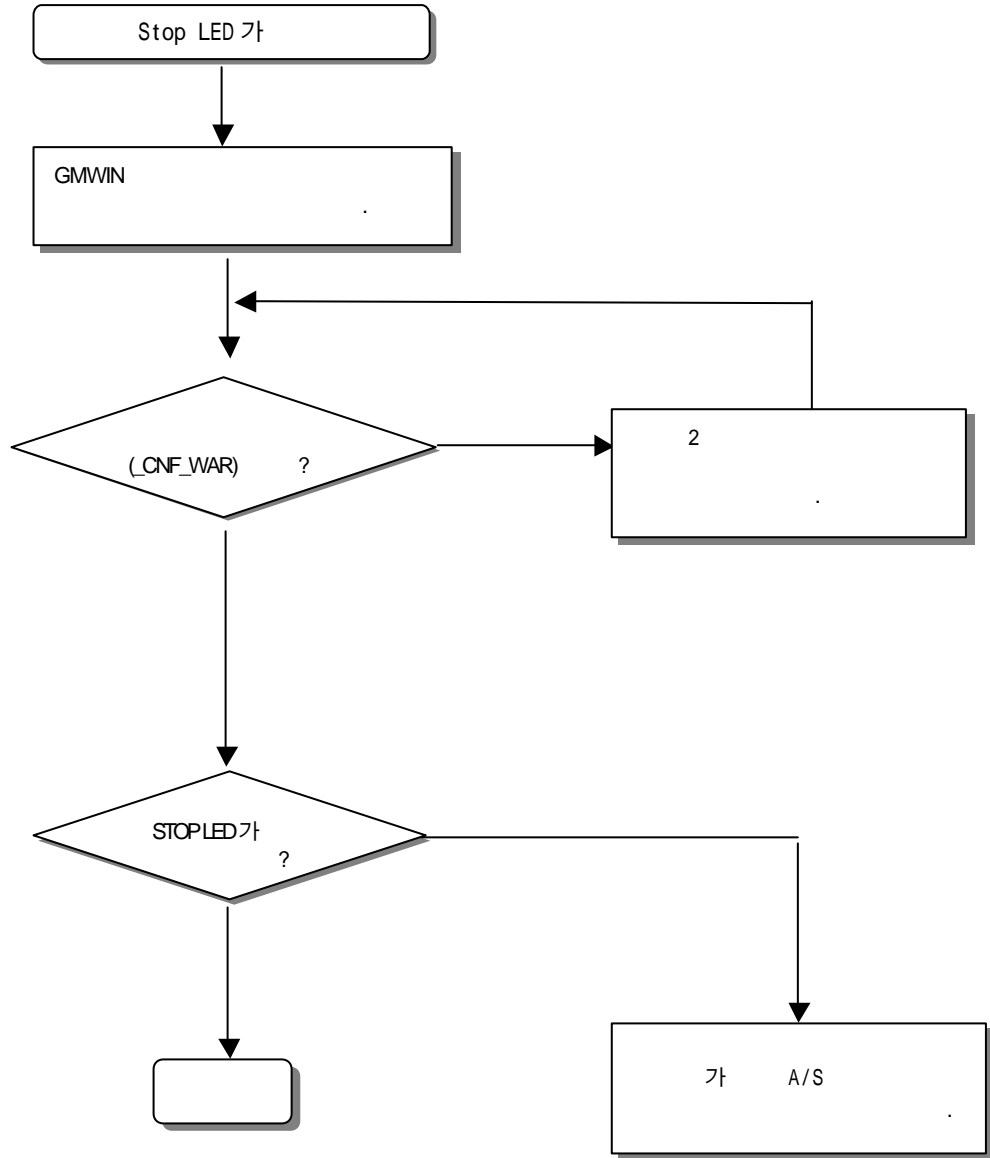
12.2.1 Power LED 가

Power LED 가



12.2.2 Stop LED 가

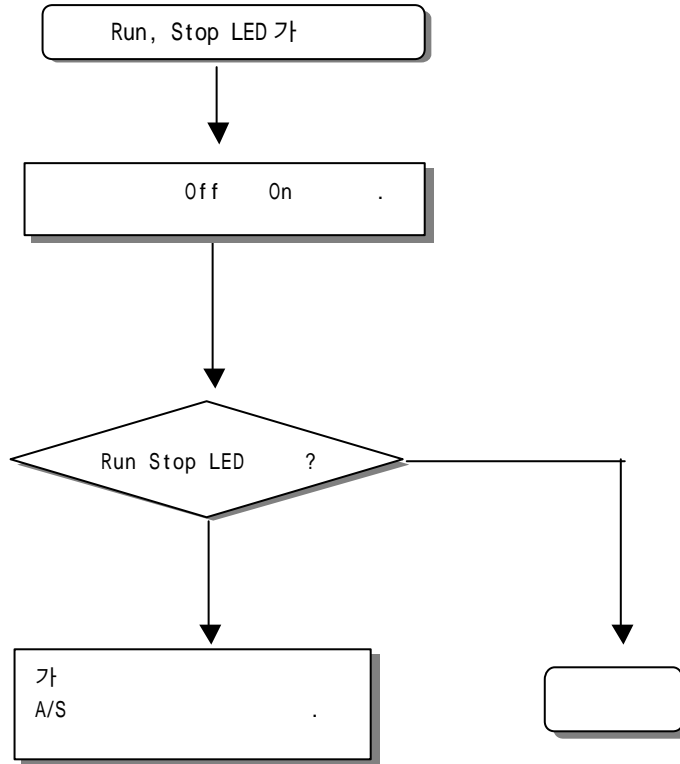
Stop LED 가



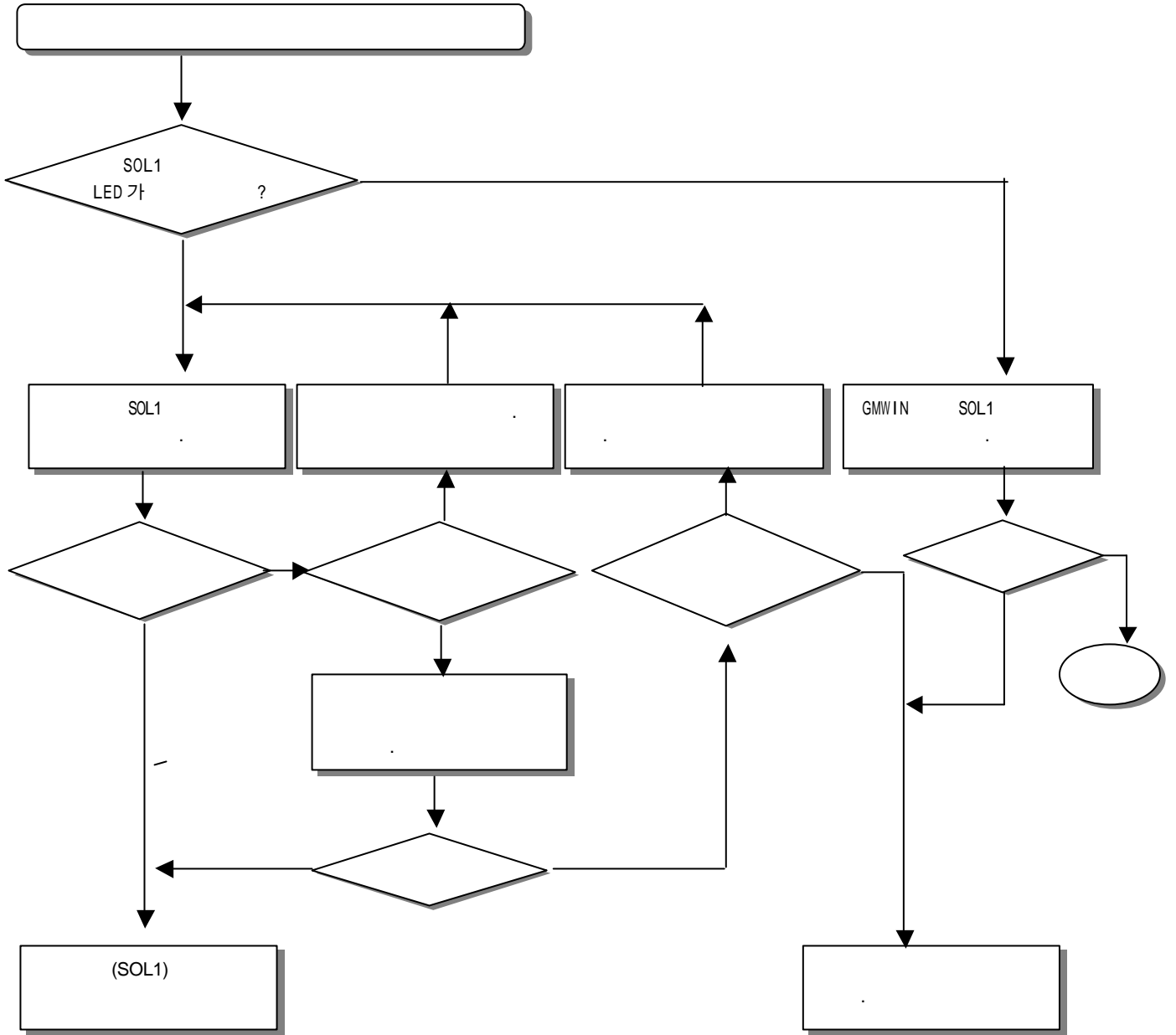
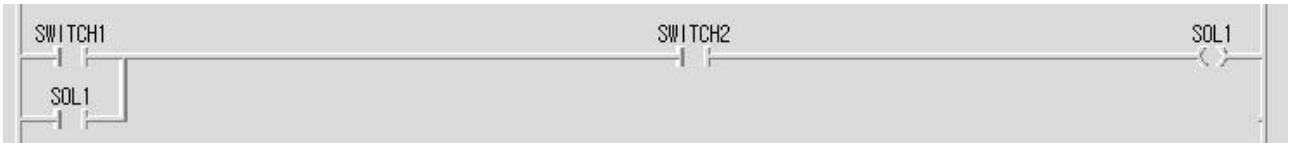
1) 가 PLC

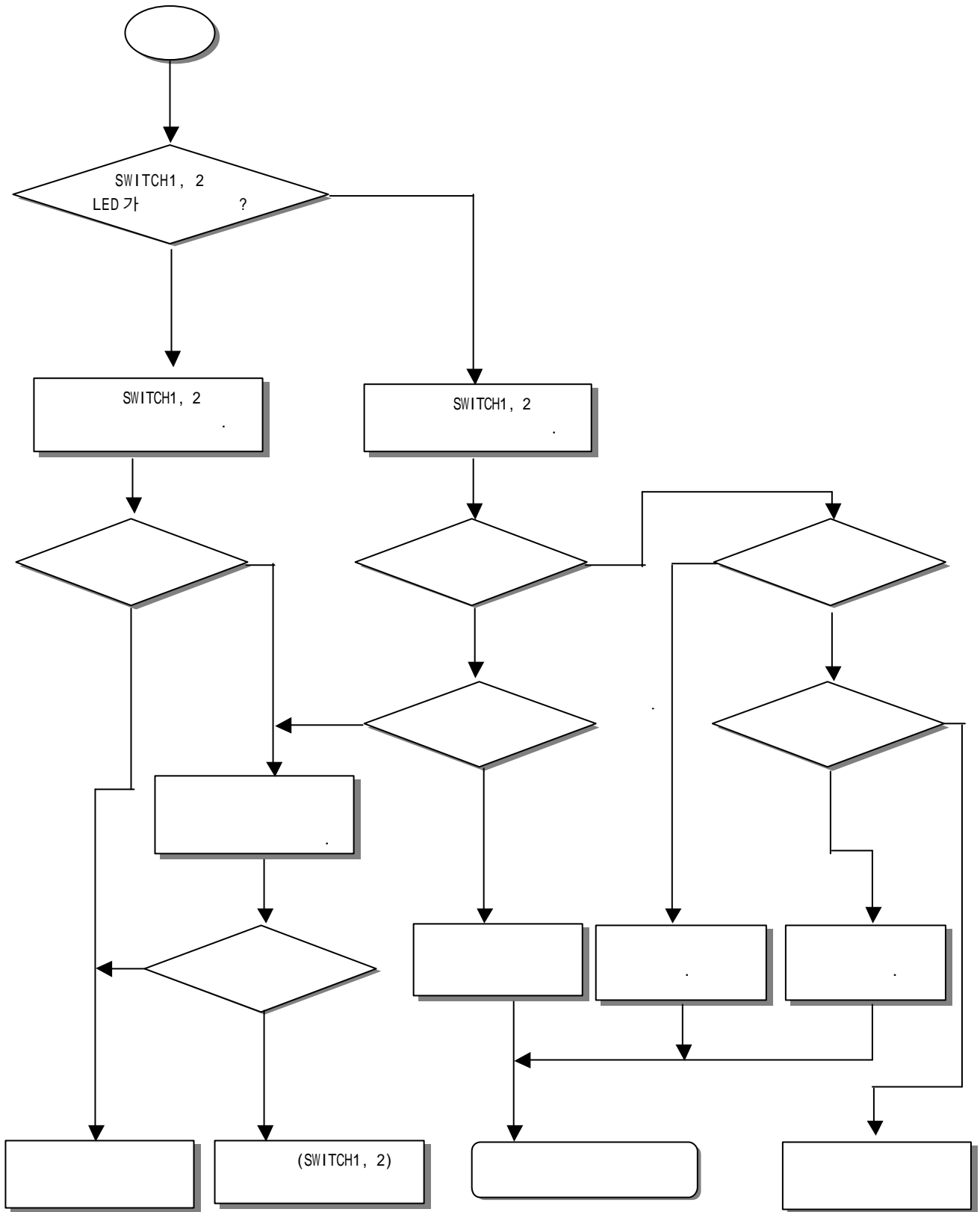
12.2.3 Run, Stop LED 가

Run, Stop LED 가



12.2.4



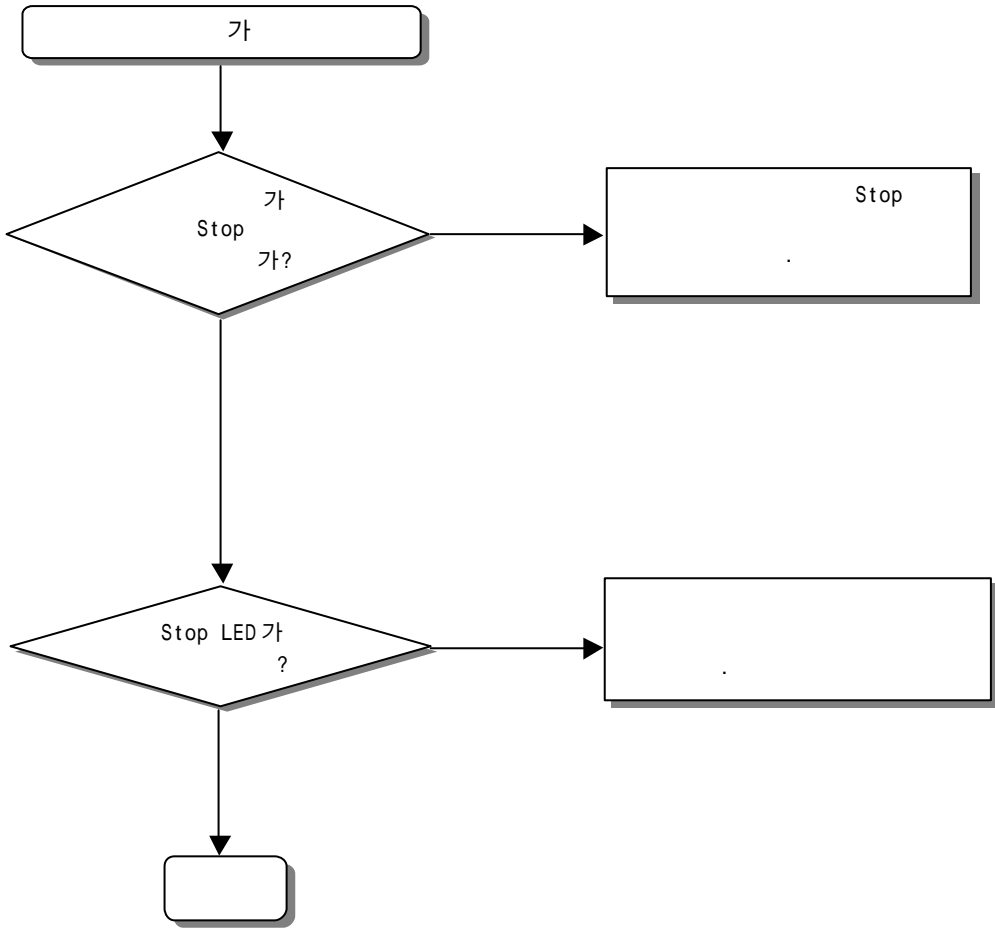


12.2.5

가

CPU

가

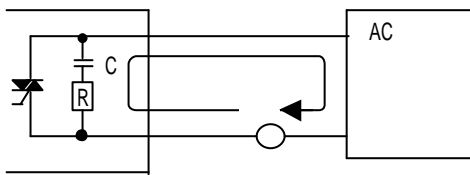
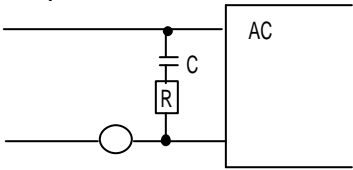
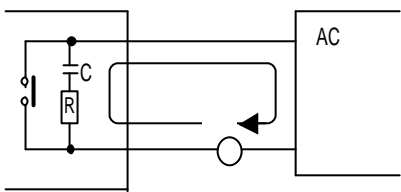
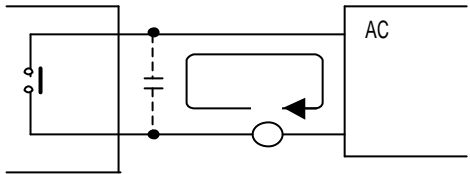
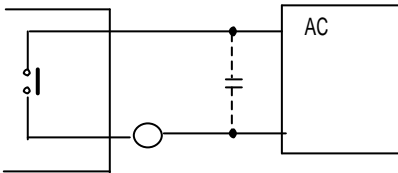
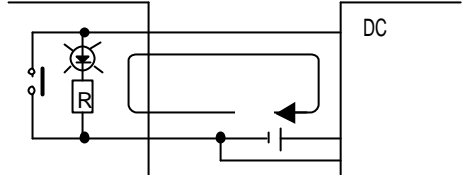
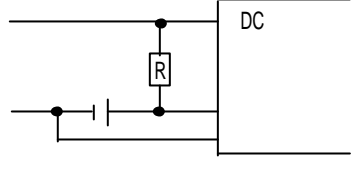
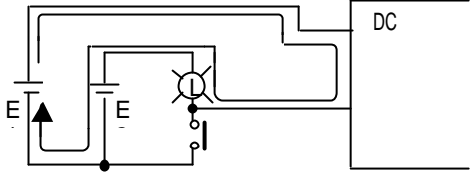
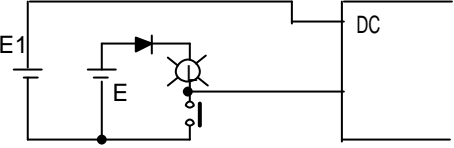


12.3

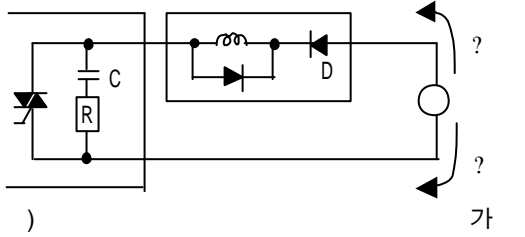
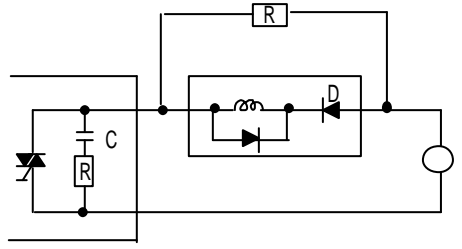
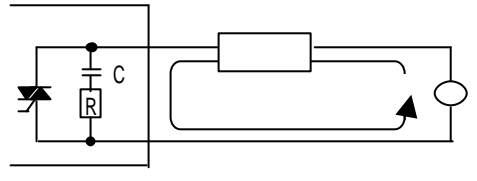
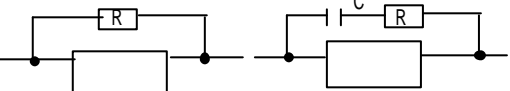
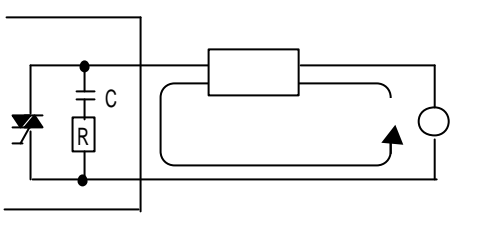
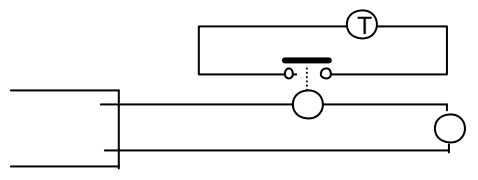
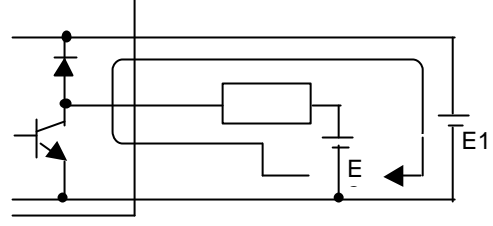
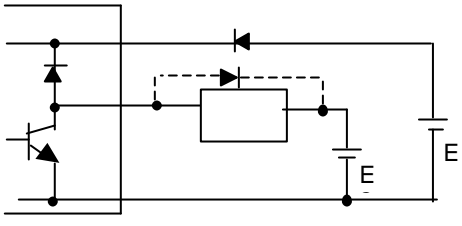
- GM4 , A/S
- FAX
- ☞ , .
1. :) _____
FAX) _____
 2. : ()
 3. ? CPU : ? OS (), ? ()
? GMWIN : ()
 4. :
 5. CPU :
? (), ? GMWIN ()
? ()
 6. CPU Stop LED ? Yes(), No()
 7. GMWIN :
 8. : ()
 9. 7 :
 10. :
 11. ☞ () : (), ()
()
 - ☞ () : :
 12. :
 13. :

12.4

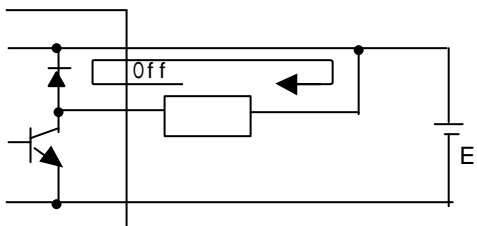
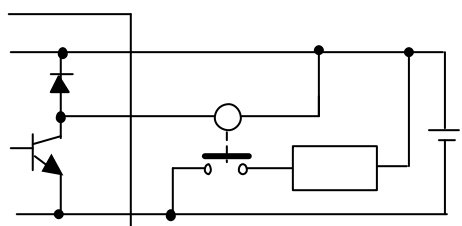
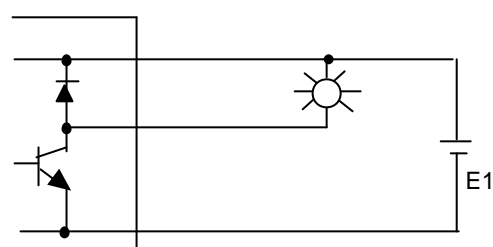
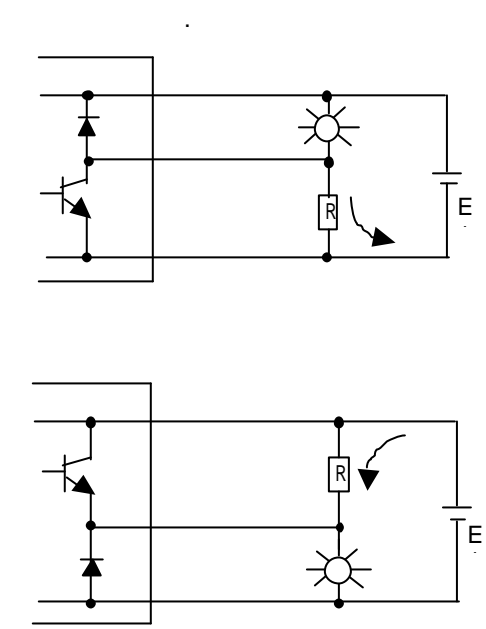
12.4.1

<p>가 Off</p>	<p>()</p> 	
<p>가 Off ()</p>	<p>(가)</p> 	<p>CR ? C : 0.1 ~ 0.47Uf R : 47 ~ 120? (1/2W)</p>
<p>가 Off</p>		
<p>가 Off</p>	<p>(LED)</p> 	<p>Off</p> 
<p>가 Off</p>	 <p>E1 > E2</p>	 <p>()</p>

12.4.2

<p>Off 가</p>	<p>가 (? C (D) 가 2?2 ? ? 가) (D) 가</p> 	<p>k? ~ k?</p> 
<p>가 Off</p>		<p>k? CR 가 가 R C R</p> 
<p>가 C?R</p>		<p>C?R C?R 가 가 T 가</p> 
<p>가 Off ()</p>	<p>2 E1 E E1 < E2 E1 Off (E2 On)</p> 	<p>() E E 가 가</p> 

()

<p>Off</p>	<p>Off [(L/R)]</p>  <p>Off</p> <p>가</p> <p>1</p>	<p>가</p> 
<p>가</p>	 <p>가</p> <p>10</p> <p>가</p>	<p>1/3 ~ 1/5</p> 

12.5

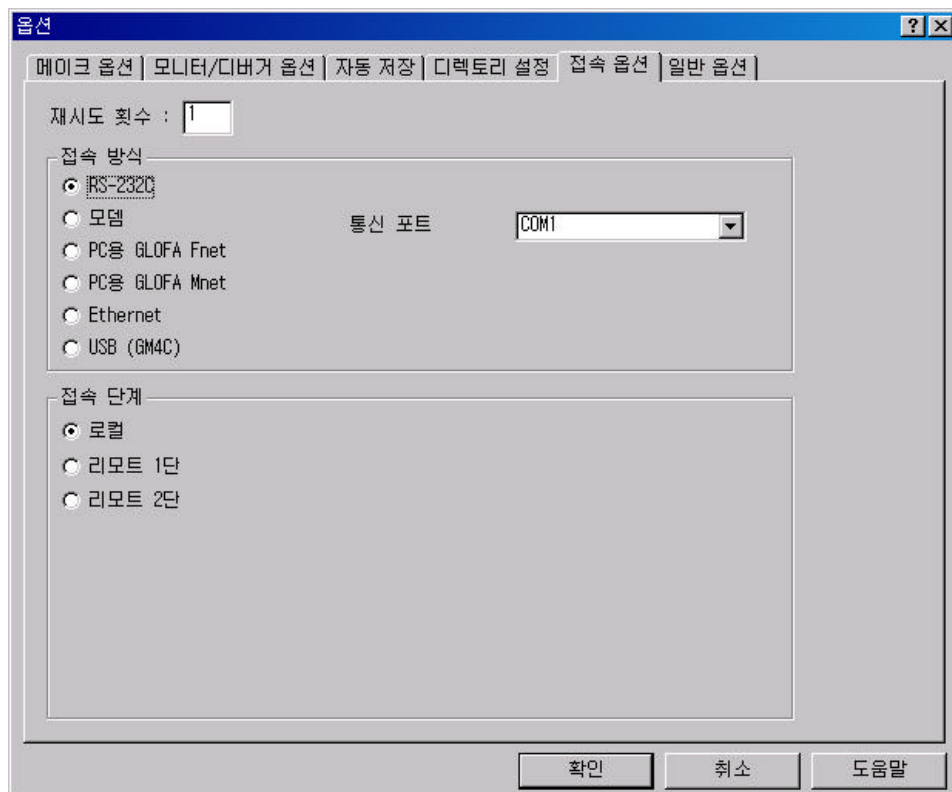
		()		LED	
-	CPU(OS ROM) 가	- A/S		-	
02	OS ROM	- A/S		-	
03	OS RAM	- A/S		-	
04	IC(RTC)	- A/S		Stop LED 2	
05		- A/S		Stop LED 1 Run LED 1	
06		- RUN A/S		Stop LED 2 Run LED 1	RUN
07		- A/S		Stop LED 3 Run LED 1	
08	IC	- A/S		Stop LED 4 Run LED 1	
-	OS 가	-		Stop LED 0.4	
22		- () CPU ()	STOP	Stop LED 0.4	RUN
23		- () CPU ()	STOP	Stop LED 0.4	RUN
30		- GMWLN (:) (_IO_TYER, _IO_TYER_N, _IO_TYERR[n])	STOP (RUN)	Stop LED 0.4	RUN
31	가	- GMWLN / 가 (:) (_IO_DEER, _IO_DEER_N, _IO_DEERR[n])	STOP (RUN)	Stop LED 0.4	
32		- GMWLN (:) (_FUSE_ER, _FUSE_ER_N, _FUSE_ERR[n])	STOP (RUN)	Stop LED 0.4	
33	가	- GMWLN 가 (:) / (_IO_RWER, _IO_RWER_N, _IO_RWERR[n])	STOP (RUN)	Stop LED 0.4	
34	가 /	- GMWLN 가 (: /) (_SP_IFER, SP_IFER_N, _SP_IFERR[n])	STOP (RUN)	Stop LED 0.5 On, 0.4 Off	
40		- ()	STOP	Stop LED 0.4	

		()		LED	
41		- ()	STOP	Stop LED 0.4	
42		- ()	STOP	Stop LED 0.4	
50		- (_ANNUN_ER, _ANC_ERR[n]) ()	STOP (RUN)	Stop LED 0.4	
55	가	- (A/S)	STOP (RUN)	Stop LED 0.4	
60	'E_STOP'	- 'E_STOP' ()	STOP	Stop LED 0.5	
500	가	-	RUN	-	
501		- GMWTR	RUN	Stop LED 2	'
502		-	RUN	Stop LED 4	'
-		-	RUN	Stop LED 0.2	'
-		-	RUN	Run LED 0.4	'

1

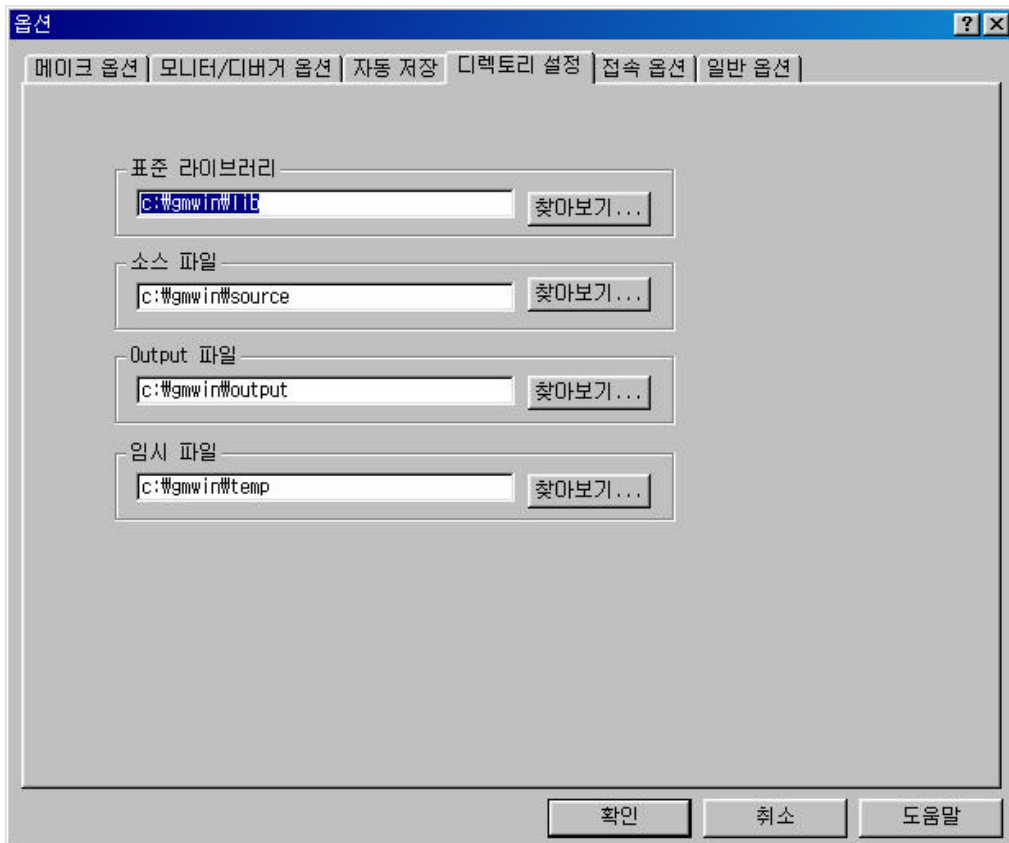
1)

- (1)
- | | | |
|-----|-------|------------|
| PLC | GMWIN | (COM1 ~ 4) |
| ? | - | - |
| ? | | RS-232C 가 |
| ? | GMWIN | |



(2)

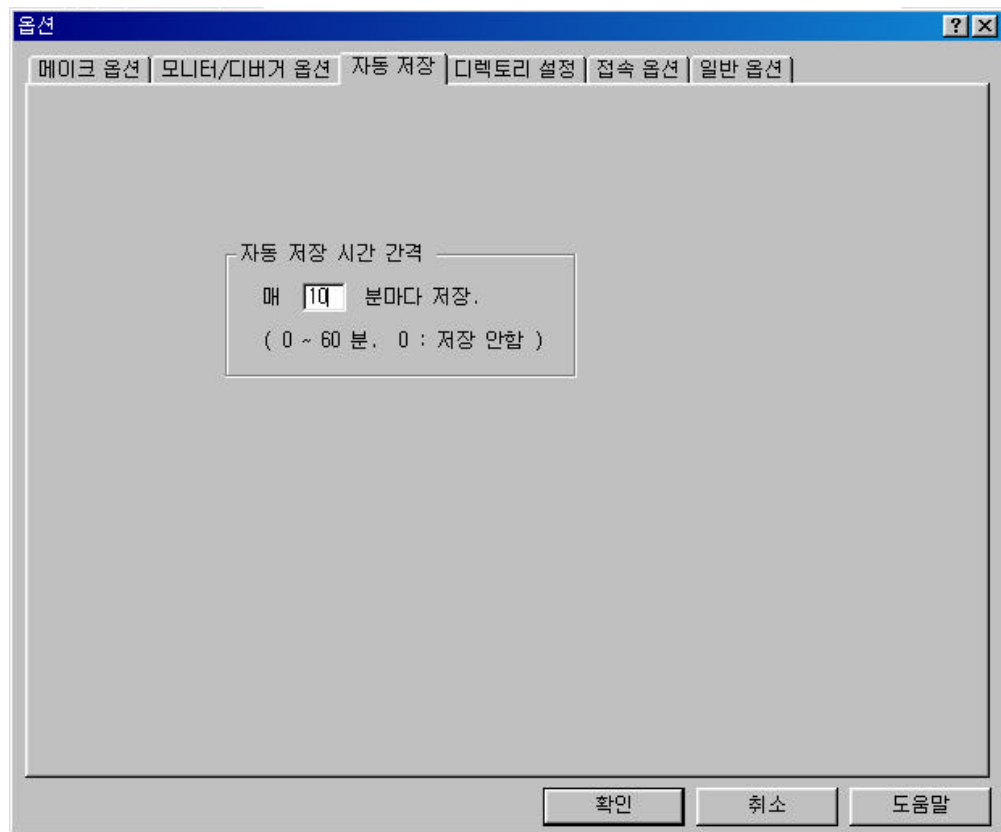
GMWIN 4
?
? : GMWIN 가
?
? Output :
? : GMWIN
GMWIN



(3)

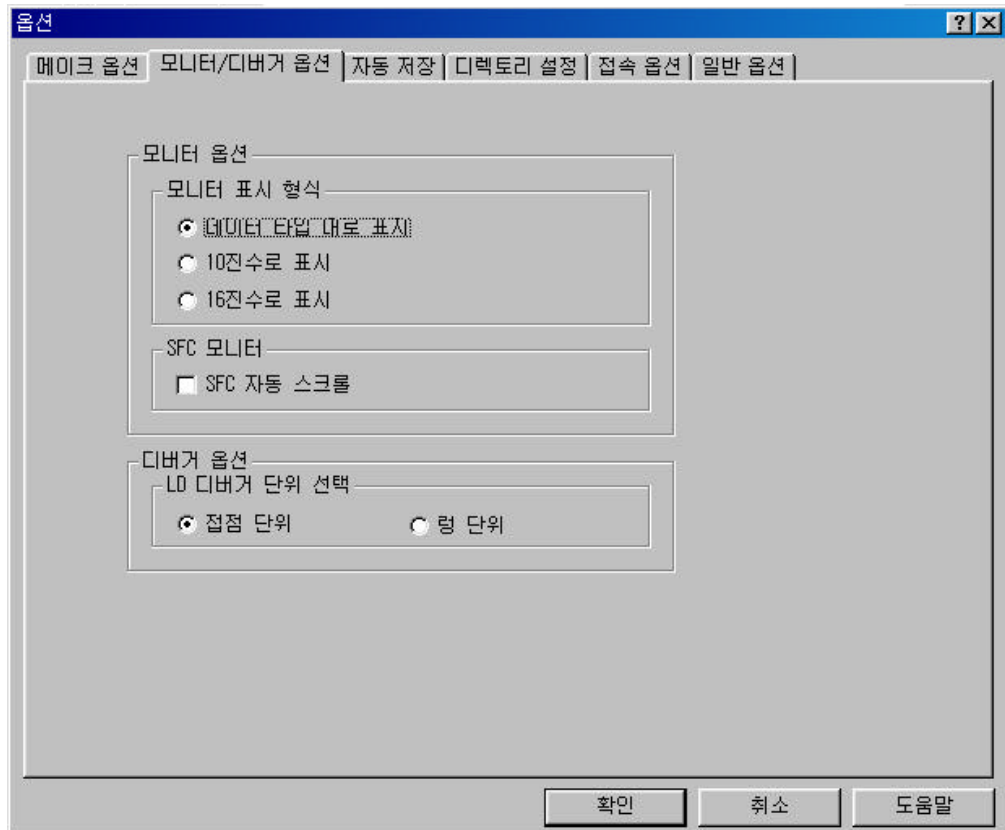
'ASV.'

?
?
- 0 ~ 60 0
?



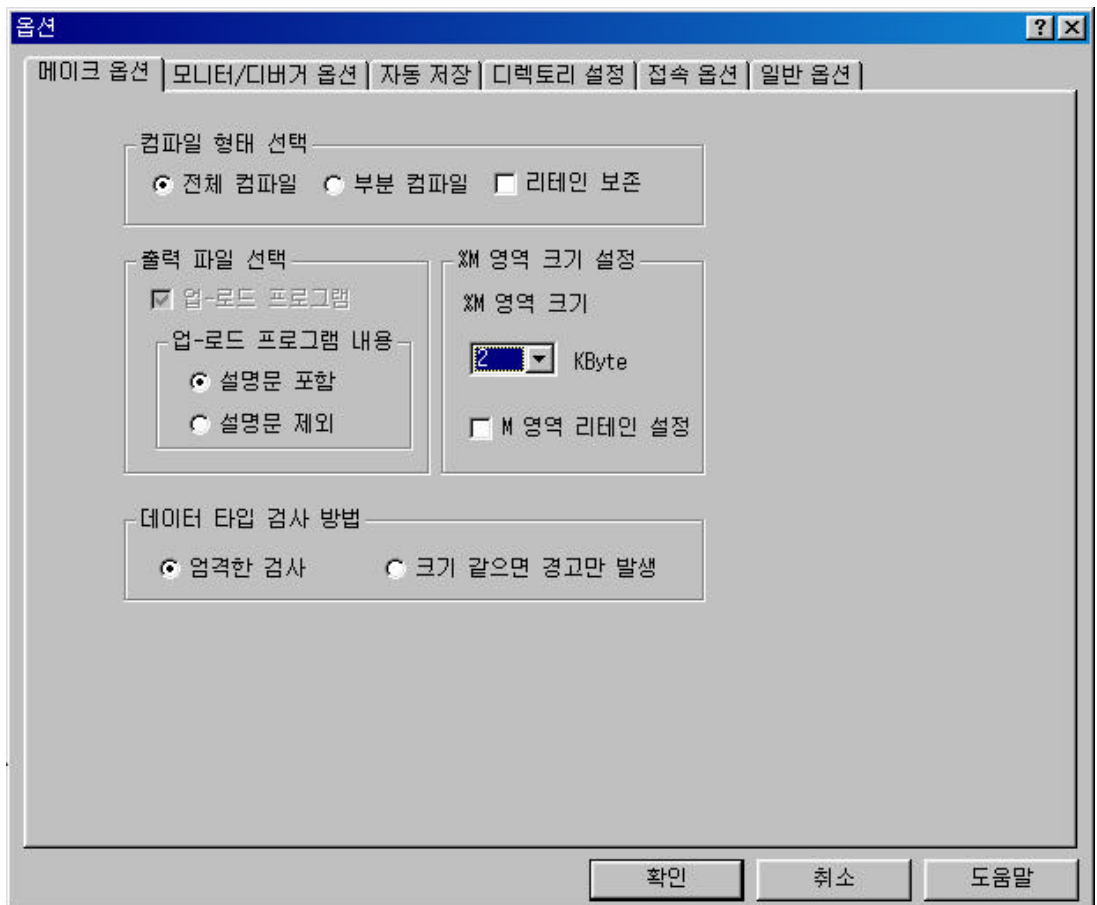
(4) /

? - - /
- :
- SFC :
- : LD



(5)

, %M
?
?
:
- :
- :
- :
- :
PLC 가
?
:
.(.)
? %M : %M
?

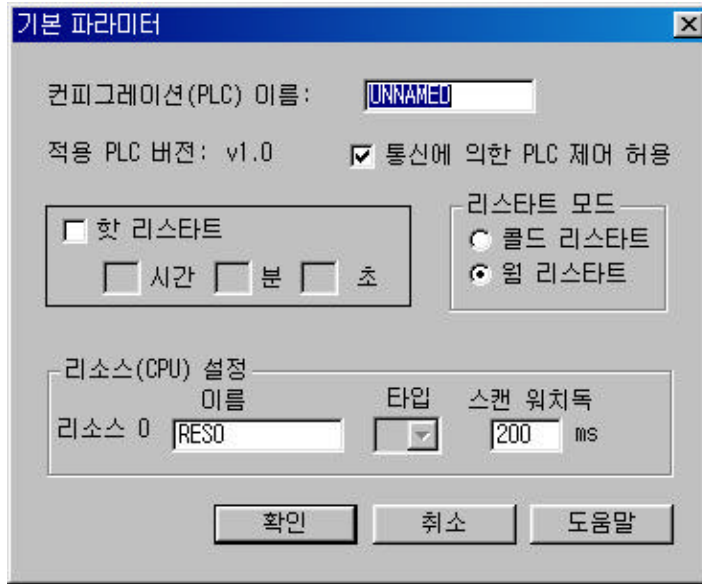


2)

PLC

PLC

(Scan Watchdog Time)



(1) (PLC)

? PLC

PLC

(2) PLC

? PLC

GMWIN

FAM,

/

가 가

(3)

? PLC

가

(4)

? PLC

1

23

59

59

가

(5) (CPU)

? PLC

CPU

? PLC

CPU

? GM4

CPU

0

(6)

?

1ms

65,536 ms

가

? GM4

CPU

0

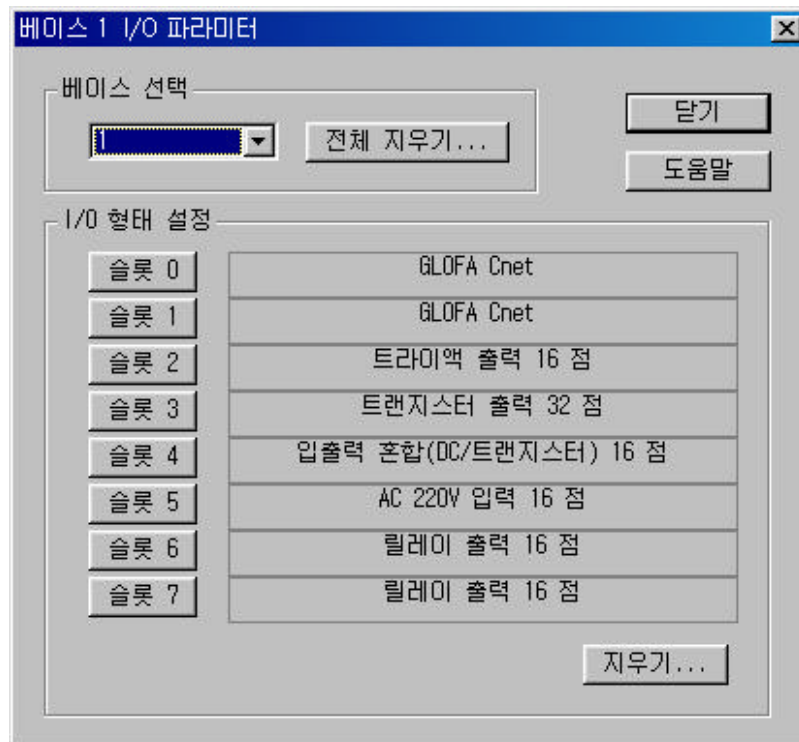
3)

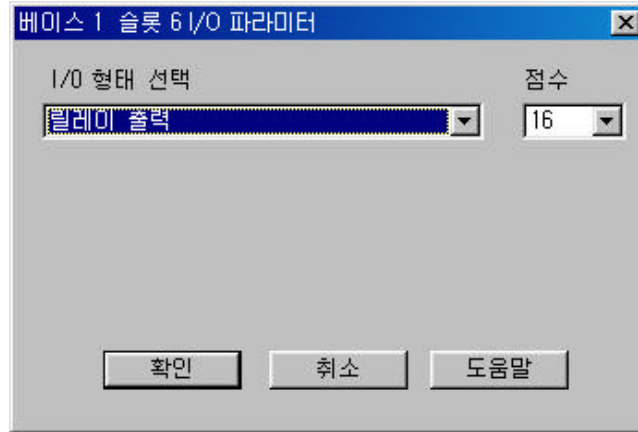
가

(DEF_MODULE)

가

PLC





< >

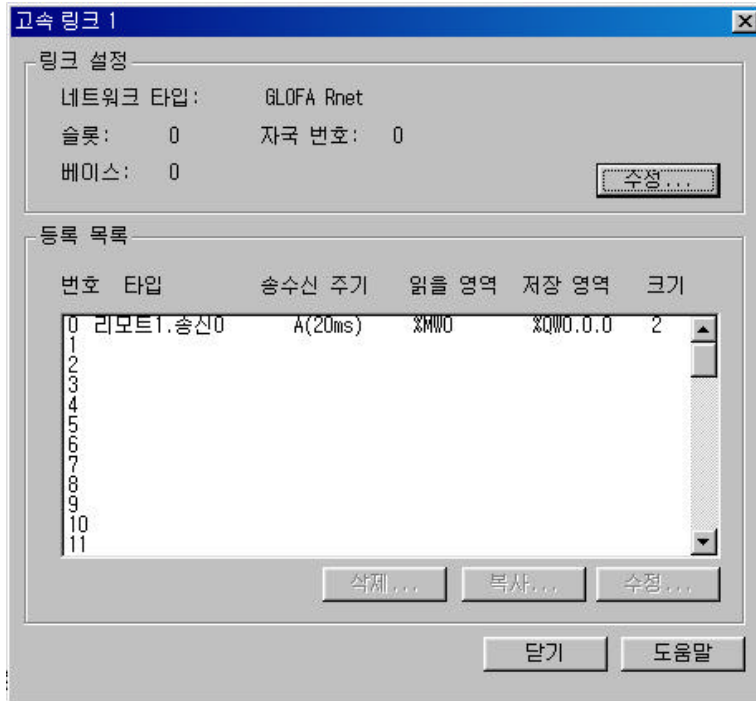
DC Input	DC	G4I-D22A(16), G4I-D24A(32), G4I-D22B(16) G4I-D24B(32)
AC 110V Input	AC 110V	G4I-A12A(16)
AC 220V Input	AC 220V	G4I-A22A(16)
Relay Output		G4Q-RY2A(16)
SSR Output		G4Q-SS2A(16) , G4Q-SS2B(16)
TR Output		G4Q-TR2A(16) , G4Q-TR4A(32)
DC Input/Relay Output		G4H-DR2A(16)
DC Input/TR Output		G4H-DT2A(16)
Interrupt Input		G4F-INTA(8)
Analog Timer		G4F-AT3A(8)
A/D	A/D	G4F-AD2A(4) , G4F-AD3A(8)
DAV, DAI	D/A	G4F-DA2V(4 ,) , G4F-DA2I(8 ,) G4F-DA2V(4 ,) , G4F-DA2I(8 ,) G4F-DA1A(2 , /)
T/C		G4F-TC2A(4)
RTD		G4F-RD2A(4)
PID	PID	G4F-PIDA/B(8)
HSC		G4F-HSCA(1)

< > ()

POSITION CONTROL (PULSE)	()	G4F-POPB(2), G4F-POPA(1), G4F-PP10/20/30(1 /2 /3) G4F-PP1D/2D/3D(1 /2 /3)
		G4F-TMCA
GLOFA Fnet	Fnet I/F	G4L-FUEA
GLOFA Enet	Enet I/F	G4L-EUEA
DEF_I		G4I-D22A(16), G4I-D24A(32), G4I-D22B(16), G4I-D24B(32), G4I-D22C(16), G4I-D24C(32), G4I-D28A(64), G4I-A12A(16), G4I-A22A(16)
DEF_O		G4Q-RY2A(16), G4Q-SS2A(16), G4Q-SS2B(16), G4Q-TR2A(16), G4Q-TR2B(16), G4Q-TR4A(32) G4Q-TR4B(32), G4Q-TR8A(64)
DEF_IO		G4H-DR2A(16), G4H-DT2A(16)
DEF_SP	/	? ?
DEF_MODULE		? ? ? ? ?
DEF_EMPTY		?

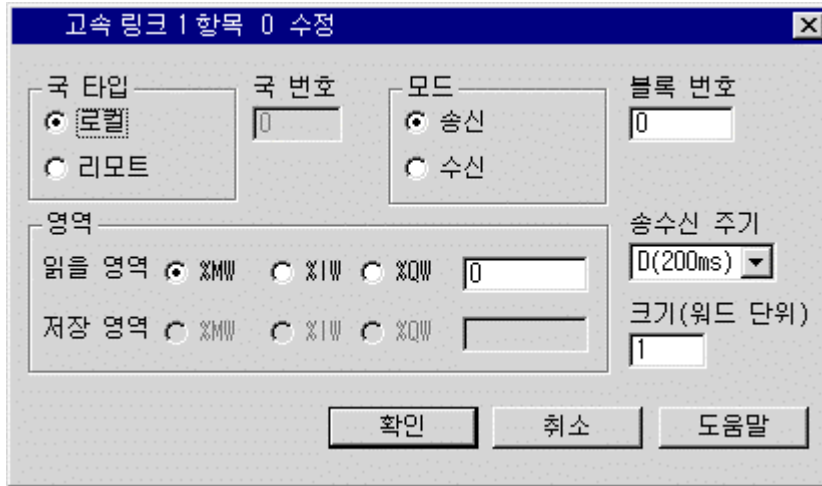
4)

()



- (1) :
- (2) :
- (3) :

(4) :



- (1) :
- (2) :
- (3) : /
- (4) :
- (5) :
- (6) : I, Q, M (Word)
- (7) :

1)

	TYPE	가		
_LER	BOOL	가		(PB) , 가
_ERR	BOOL	가		(FN) (FB) ,
_T20MS *	BOOL	-	20ms	On/Off
_T100MS *	BOOL	-	100ms	
_T200MS *	BOOL	-	200ms	가
_T1S *	BOOL	-	1	
_T2S *	BOOL	-	2	Off
_T10S *	BOOL	-	10	_T100MS
_T20S *	BOOL	-	20	50ms 50ms
_T60S *	BOOL	-	60	
_ON *	BOOL	-	On	On
_OFF *	BOOL	-	Off	Off
_10N *	BOOL	-	On	On
_10FF *	BOOL	-	Off	Off
_STOG *	BOOL	-	(Scan Toggle)	On/Off (On)
_INIT_DONE	BOOL	가		가
_RTC_DATE	DATE	-	RTC	1984 1 1
_RTC_TOD	TOD	-	RTC	00:00:00 ms
_RTC_WEEK	UINT	-	RTC	(0: , 1: , 2: , 3: , 4: , 5: , 6:)

1) ?
 / :
 / :

2)

	TYPE	BIT		
_CNF_ER	WORD			()
_CPU_ER	BOOL	BIT 0	CPU	CPU (_SYS_ERR) 가
_IO_TYER	BOOL	BIT 1		1/0 (_IO_TYER_N, _IO_TYER[n])
_IO_DEER	BOOL	BIT 2		(_IO_DEER_N, _IO_DEER[n]]
_FUSE_ER	BOOL	BIT 3		Fuse가 가 (_FUSE_ER_N, _FUSE_ER[n])
_IO_RWER	BOOL	BIT 4	() /	(_IP_RWER_N, _IO_RWER[n])
_SP_IFER	BOOL	BIT 5	/ ()	가 가 (_IP_IFER_N, _IP_IFER[n])
_ANNUN_ER	BOOL	BIT 6		_ANC_ERR[n]
-	-	BIT 7	-	-
_WD_ER	BOOL	BIT 8		Watchdog Time) (Scan
_CODE_ER	BOOL	BIT 9		
_STACK_ER	BOOL	BIT 10		
_P_BCK_ER	BOOL	BIT 11		가 가 (_DOMAIN_ST)

3) (GM4C)

		BIT		
_CNF_ER_M	BYTE		()	
_ANNUN_ER_M	BOOL	BIT 6		_ANNUN_ER

4) (GM4C)

_BASE_M[n]	BOOL ARRAY	n: 0-6		_BASE_M[n] <table border="1" style="display: inline-table; vertical-align: middle;"> <tr> <td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td> </tr> </table>	6	5	4	3	2	1	0																																						
6	5	4	3	2	1	0																																											
_SLOT_M[n]	BYTE ARRAY	n: 0-6		<table border="1" style="display: inline-table; vertical-align: middle;"> <tr> <td></td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td> </tr> <tr> <td>_SLOT_M[0]</td> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td>_SLOT_M[1]</td> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td></td> <td colspan="8" style="text-align: center;"> </td> </tr> <tr> <td>_SLOT_M[6]</td> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> </table> <div style="display: inline-block; vertical-align: middle; margin-left: 10px;"> BASE 0 BASE 1 BASE 6 </div>		7	6	5	4	3	2	1	0	_SLOT_M[0]									_SLOT_M[1]																		_SLOT_M[6]								
	7	6	5	4	3	2	1	0																																									
_SLOT_M[0]																																																	
_SLOT_M[1]																																																	
_SLOT_M[6]																																																	

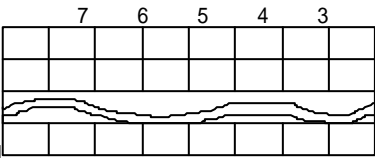
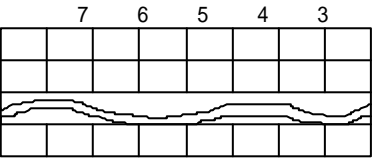
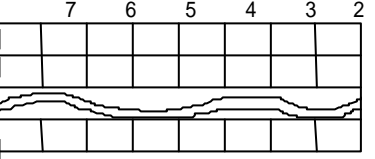
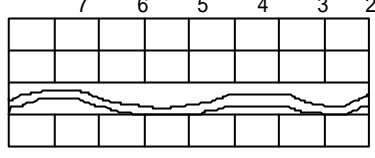
5) (GM4C)

_BASE_S[n]	BOOL ARRAY	n: 0-6		CPU _BASE_S[n] <table border="1" style="display: inline-table; vertical-align: middle;"> <tr> <td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td> </tr> </table>	6	5	4	3	2	1	0																																						
6	5	4	3	2	1	0																																											
_SLOT_S[n]	BYTE ARRAY	n: 0-6		CPU <table border="1" style="display: inline-table; vertical-align: middle;"> <tr> <td></td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td> </tr> <tr> <td>_SLOT_S[0]</td> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td>_SLOT_S[1]</td> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td></td> <td colspan="8" style="text-align: center;"> </td> </tr> <tr> <td>_SLOT_S[6]</td> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> </table> <div style="display: inline-block; vertical-align: middle; margin-left: 10px;"> BASE 0 BASE 1 BASE 6 </div>		7	6	5	4	3	2	1	0	_SLOT_S[0]									_SLOT_S[1]																		_SLOT_S[6]								
	7	6	5	4	3	2	1	0																																									
_SLOT_S[0]																																																	
_SLOT_S[1]																																																	
_SLOT_S[6]																																																	

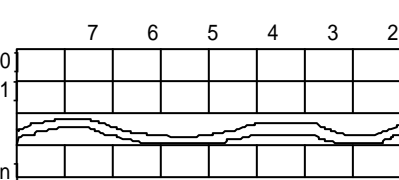
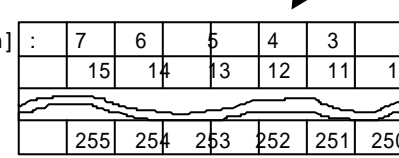
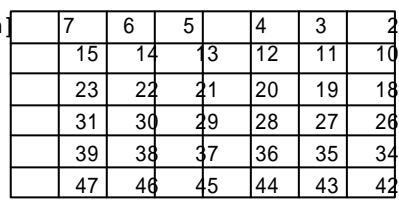
3)

	TYPE	BIT		
_CNF_WAR	WORD		()	.
_RTC_ERR	BOOL	BIT 0	RTC	RTC
_D_BCK_ER	BOOL	BIT 1		가 , , 가
_H_BCK_ER	BOOL	BIT 2	가	가 () 가
_AB_SD_ER	BOOL	BIT 3	(Abnormal Shutdown)	가 ('ESTOP' .)
_TASK_ERR	BOOL	BIT 4	(Task) (,)	가 (_TC_BMAP[n], _TC_CNT[n])
_BAT_ERR	BOOL	BIT 5		
_ANNUN_WR	BOOL	BIT 6		_ANC_WB[n]
-	-	BIT 7	-	-
_HSPMT1_ER	BOOL	BIT 8	1	(Enable) 가 (Disable) CPU GM4A: 1 ~ 2, GM4B: 1 ~ 4, GM4C: 1 ~ 8
_HSPMT2_ER	BOOL	BIT 9	2	
_HSPMT3_ER	BOOL	BIT 10	3	
_HSPMT4_ER	BOOL	BIT 11	4	
_HSPMT5_ER	BOOL	BIT 12	5	
_HSPMT6_ER	BOOL	BIT 13	6	
_HSPMT7_ER	BOOL	BIT 14	7	
_HSPMT8_ER	BOOL	BIT 15	8	

4)

	TYPE			
_IO_TYER_N	UINT	GM4A/B: 0 ~ 31 GM4C: 0 ~ 56		I/O 가
_IO_TYERR[n]	BYTE	n : GM4A/B: 0 ~ 3 GM4C: 0 ~ 6		I/O _IO_TYERR[0] BASE 0 _IO_TYERR[1] BASE 1  _IO_TYERR[n] BASE n
_IO_DEER_N	UINT	GM4A/B: 0 ~ 31 GM4C: 0 ~ 56		PLC 가
_IO_DEERR[n]	BYTE	n : GM4A/B: 0 ~ 3 GM4C: 0 ~ 6		PLC 가 _IO_DEERR[0] BASE 0 _IO_DEERR[1] BASE 1  _IO_DEERR[n] BASE n
_FUSE_ER_N	UINT	GM4A/B: 0 ~ 31 GM4C: 0 ~ 56		가
_FUSE_ERR[n]	BYTE	n : GM4A/B: 0 ~ 3 GM4C: 0 ~ 6		가 _FUSE_ERR[0] BASE 0 _FUSE_ERR[1] BASE 1  _FUSE_ERR[n] BASE n
_IO_RWER_N	UINT	GM4A/B: 0 ~ 31 GM4C: 0 ~ 56	/	가
_IO_RWERR[n]	BYTE	n : GM4A/B: 0 ~ 3 GM4C: 0 ~ 6	/	가 _IO_RWERR[0] BASE 0 _IO_RWERR[1] BASE 1  _IO_RWERR[n] BASE n
_SP_IFER_N	UINT	GM4A/B: 0 ~ 31 GM4C: 0 ~ 56	/	가

4) ()

	TYPE			
_SP_IFERR[n]	BYTE	n : GM4A/B: 0 ~ 3 GM4C: 0 ~ 6	/	가 , 가 _SP_IFERR[0] BASE 0 _SP_IFERR[1] BASE 1  _SP_IFERR[n] BASE n
_ANC_ERR[n]	UINT	n: 0 ~ 15		16 (0)
_ANC_WAR[n]	UINT	n: 0 ~ 7		_ANC_WB[n] 가 , _ANC_WAR[0]
_ANC_WB[n]	BIT	n: 0-255		(0) n=2 _ANC_WB[n] :  249 248
_TC_BMAP[n]	BIT	n: 0 ~ 47 (Task Collision Bit-map) n : 0 ~ 3 n : 32 ~ 47		가 , 가 _TC_BMAP[n]  41 40
_TC_CNT[n]	UINT	n: 0 ~ 47	(Task Collision Counter)	
_BAT_ER_TM	DATE & TIME			가
_AC_F_CNT	UINT	0 ~ 65535		RUN
_AC_F_TM[n]	DATE & TIME	n: 0 ~ 15		RUN 16 가
_ERR_HIS[n]		n: 0 ~ 15		가 16 : DATE&TIME (8 Byte) : UINT (2 Byte)
_MODE_HIS[n]		n: 0 ~ 15		가 16 : DATE&TIME (8 Byte) : UINT (2 Byte) : UINT (2 Byte)

* 가

5)

	TYPE			
_CPU_TYPE	UINT	0 ~ 16		:16 - GM1:0, GM2:1, (GM3:2, GM4:3, GM4C:64, GM5:4), (FSM:5,6),
_VER_NUM	UINT	-	O/S	O/S
_MEM_TYPE	UINT	1 ~ 5 6		(GM4C) (0 : , :1~5)
_SYS_STATE	WORD		PLC	
		BIT 0		GMWIN 가
		BIT 1	STOP	CPU
		BIT 2	RUN	
		BIT 3	PAUSE	
		BIT 4	DEBUG	
		BIT 5		
		BIT 6		GMWIN
		BIT 7		GMWIN
		BIT 8		
		BIT 9	STOP STOP	RUN STOP
		BIT 10		On/Off
		BIT 11		On/Off
		BIT 12	ESTOP STOP	RUN ESTOP
		BIT 13	-	-
		BIT 14		
BIT 15	On			
_PADT_CNF	BYTE		GMWIN	CPU GMWIN
		BIT 0	GMWIN	GMWIN
		BIT 1	GMWIN	GMWIN
		BIT 2		
_RST_TY	BYTE			.()
		BIT 0		4.6.1
		BIT 1		
		BIT 2		
_INIT_RUN	BOOL	BIT 3		가
_SCAN_MAX	UINT	-	(ms)	
_SCAN_MIN	UINT	-	(ms)	
_SCAN_CJR	UINT	-	(ms)	
_RTC_TIME[n]	BYTE	n: 0 ~ 7		RTC BCD (1984 1 1 ~2083 12 31 가) _RTC_TIME[0] : , _RTC_TIME[1]: , _RTC_TIME[2] : , _RTC_TIME[3] : , _RTC_TIME[4] : , _RTC_TIME[5] : , _RTC_TIME[6] : , _RTC_TIME[7] : - 0: , 1: , 2: , 3: , 4: , 5: , 6:
_SYS_ERR	UINT			12.5

6)


(1)

	TYPE	BIT		
_DOMAIN_ST	BYTE		S/W	
		BIT 0		
		BIT 1	I/O	I/O
		BIT 2		
		BIT 3		
		BIT 4		

(2)

	TYPE	BIT		
_KEY_STATE	BYTE			CPU .
		BIT_0	KEY_STOP	가 STOP .
		BIT_1	KEY_RUN	가 RUN .
		BIT_2	KEY_PAUSE/	가 PAUSE/ .

(3)

	TYPE			
_IO_INSTALL[n]	BYTE	n :	I/O	. I/O 7 6 5 4 3 2 1 0 _IO_INSTALL[0] BASE 0 _IO_INSTALL[1] BASE 1  _IO_INSTALL[n] BASE n
		GM4A/B:		
		0 ~ 3		
		GM4C:		
		0 ~ 6		

7)

- GLOFA Fnet / FDEnet / Cnet / Dnet / Pnet / Rnet / Enet / FEnet

(1)

≒ n

(n = GM4A:0 ~ 7, GM4B:0 ~ 31, GM4C:0 ~ 55)

	TYPE	Net		
_CnVERNO	UINT	Fnet/FDEnet Cnet/Dnet/Pnet net/Enet/FEnet		≒ 0/S
_CnSTNOH _CnSTNOL	UDINT UDINT	Fnet/FDEnet Cnet/Dnet/Pnet net/Enet/FEnet		≒ Fnet/Dnet : ≒ Cnet : _CnSTNOH : RS-232C _CnSTNOL : RS-422
_CnTXECNT	UINT	Fnet/FDEnet Cnet/Dnet/Pnet/R net/Enet/FEnet		≒ 1 가 ≒ 가 ≒ Cnet RS-232C RS-422
_CnRXECNT	UINT	Fnet/FDEnet Cnet/Dnet/Pnet/R net/Enet/FEnet		≒ 1 가 ≒ 가 ≒ Cnet RS-232C RS-422
_CnSVCFCNT	UINT	Fnet/FDEnet Cnet/Dnet/Pnet/R net/Enet/FEnet		≒ 1 가 ≒ 가 ≒ 가
_CnSCANMX	UINT	Fnet/FDEnet Cnet/Dnet/Pnet net/Enet/FEnet	(1ms)	≒
_CnSCANAV	UINT	Fnet/FDEnet Cnet/Dnet/Pnet net/Enet/FEnet	(1ms)	≒
_CnSCANMN	UINT	Fnet/FDEnet Cnet/Dnet/Pnet net/Enet/FEnet	(1ms)	≒
_CnLINF _CnLNKMOD _CnINRING _CnIFERR _CnSVBSY _CnCRDER	UINT BOOL BOOL BOOL BOOL BOOL	Fnet/FDEnet Cnet/Dnet/Pnet net/Enet/FEnet	(RUN=1, TEST=0) (IN_RING = 1) (=1) (=1)	≒ 가 TEST ≒ 가 가 ≒ 가 가 ≒ 가 ≒ H/W O/S
_NETn_LIV[k] (k=0 ~ 63 k)	ARRAY OF BOOL	Fnet/FDEnet Cnet/Dnet/Rnet net/FEnet	(1 = , 0 =)	≒ k PLC가 (가)
_NETn_RST[k] (k=0 ~ 63 k)	ARRAY OF BOOL	Fnet/FDEnet Cnet/DnetRnet/ et/FEnet	(1 = , 0 =)	≒ 1' ()
_RCVn_232[k] (k=0 ~ 63 k)	ARRAY OF BOOL	Cnet	(=1)	≒ Cnet RS-232C RS-232C RCV_MSG 가 0 가 1
_RCVn_422[k] (k=0 ~ 63 k)	ARRAY OF BOOL	Cnet	(=1)	≒ Cnet RS-232C RS-422C RCV_MSG 가 0 가 1
_ECMn_CHx_ FLAG[k] (x=0 ~ 15, k=0 k)	ARRAY OF BOOL	Enet/FEnet Fnet/FDEnet	(=1)	? Enet 가 1 가 ()

(1) ()

n

(n = GM4A:0 ~ 7, GM4B:0 ~ 31, GM4C:0 ~ 55)

	TYPE	Net		
_RCVn_ECM[k] (k=0 ~ 15 k)	BIT ARRAY	Enet / FEnet net / FDEnet	(= 1)	? Enet 가 1 가 1 . (_ECMn_Chx_FLAG[k]가 _RCVn_ECM[k] (x≠k))
_SERVn_CH_EN	BIT	Enet / FEnet net / FDEnet	(= 1)	? PC MMI 0 1
_FSMn_reset	BOOL	Fnet / FDEnet	I/O S/W	? I/O FSMn_st_no (가 가)
_FSMn_io_reset	BOOL	Fnet / FDEnet	I/O	? I/O FSMn_st_no (가 가)
_FSMn_hs_reset	BOOL	Fnet / FDEnet	I/O	? I/O Bit 'Off' 1 On 가 On 0 FSMn_st_no 가 가
_FSMn_st_no	USINT	Fnet / FDEnet	_FSMn_reset, _FSMn_io_reset, _FSMn_hs_reset I/O	? _FSMn_reset, _FSMn_io_reset, _FSMn_hs_reset () 00 ~ 63 => 255 =>

(2)

m

(m = GM4A:1 ~ 2GM4B:1 ~ 4, GM4C:1 ~ 8)

	TYPE	BIT		
_HSmRLINK	BOOL	Fnet / FDEnet / Dnet / Pnet / Rn / Enet / FEnet	RUN	? 1) , RUN , On 2) 가 , 3) , On On
_HSmLTRBL	BOOL	Fnet / FDEnet / Dnet / Pnet / Rn / Enet / FEnet	(Link Trouble)	? _HSmRLINK가 On 가 On 1) RUN 가 On , 2) 가 , 3) 가 1), 2), 3) On , 가 Off
_HSmSTATE[k] (K=0 ~ 63 K)	ARRAY OF BOOL	Fnet / FDEnet / Dnet / Pnet / Rn / Enet / FEnet	K	? _HSmSTATE[k] = _HSmMOD[k] & _HSmTRX[k] & _HSmERR[k]
_HSmMOD[k] (K=0 ~ 63 K)	ARRAY OF BOOL	Fnet / FDEnet / Dnet / Pnet / Rn / Enet / FEnet	K (RUN = 1, = 0)	? K
_HSmTRX[k] (K=0 ~ 63 K)	ARRAY OF BOOL	Fnet / FDEnet Dnet / Pnet / Rn Enet / FEnet	K (= 1, = 0)	? K 가
_HSmERR[k] (K=0 ~ 63 K)	ARRAY OF BOOL	Fnet / FDEnet / Dnet / Pnet / Rn / Enet / FEnet	K (= 1, = 0)	? K 가

(3) FEnet Reset

	TYPE	Net		
_FENET_RESET_Mx	BIT ARRAY	FEnet	FEnet x Reset	? FEnet Reset 가 Set
_FENET_RESET_Ex	BIT ARRAY	FEnet	FEnet x Reset	? FEnet Reset '1' Set Reset
_RCV_SERVx_M_CH	BIT ARRAY	FEnet	x	? FEnet Reset FEnet
_RCV_SERVx_E_CH	BIT ARRAY	FEnet	x	
_RCV_SERV_CNT_M	UINT ARRAY	FEnet		
_RCV_SERV_CNT_E	UINT ARRAY	FEnet		

11) (GM4-CPUC)

가 가 .

(1)

		가
	_INIT_DONE, _RTC_DATE, _RTC_TOD, _RTC_WEEK	INIT_DONE 가
	CNF_ER	가
	CNF_ER_M, ANNUN_ER_M, BASE_M[n], SLOT_M[n]	가
	CNF_WAR	가
	_IO_TYER_N, _IO_TYERR[n], _IO_DEER_N, _IO_DEERR[n], _FUSE_ER_N, _FUSE_ERR[n], _IO_RWER_N, _IO_RWERR[n], _SP_IFER_N, _SP_IFERR[n], _ANC_ERR[n], _ANC_WAR[n], _ANC_WB[n], _TC_BMAP[n], _TC_CNT[n], _BAT_ER_TM, _AC_F_CNT, AC_F_TM[n]	_ANC_ERR[n], _ANC_WAR[n], _ANC_WB[n], _TC_BMAP[n], _TC_CNT[n], _AC_F_CNT 가
	_CPU_TYPE, _VER_NUM, _MEM_TYPE, _SYS_STATE, _PADT_CNF, _RST_TY, _INIT_RUN, _SCAN_MAX, _SCAN_MIN, _SCAN_CUR, _STSK_NUM, _STSK_MAX, _STSK_MIN, _STSK_CUR, RTC_TIME[n], _SYS_ERR	_SCAN_MAX, _SCAN_MIN, _SCAN_CUR, _STSK_NUM, _STSK_MAX, _STSK_MIN, _STSK_CUR 가
	DOMAIN_ST, KEY_STATE, CPU_CNF, IO_INSTALL[n]	가
	_FSMn_RESET, _FSMn_IO_RESET, _FSMn_HS_RESET, FSMn_ST_NO	가

(2)

- ☞☞ BOOL (:_ANC_WB[n])
 - : _ANC_WB
 - : _ANC_WB[1]
- ☞☞ BYTE (:_SLOT_S[n])
 - : _SLOT_S
 - : _SLOT_S[1]
 - BIT : _SLOT_[1][0]

3 /

1) (GM4-CPUA/B)

		PB (Byte) ?1	(Byte) ?2	(us) ?3
ABS (int)		24	-	1.2
ADD (int)		24	-	1.7
AND (word)		16	-	4.3
DIV (int)		32	-	32.9
DIV (dint)		32	-	62.9
EQ (int)		30	-	1.6
LIMIT(int)		48	794	11.8
MAX (int)		48	738	12.9
MOVE		8	-	1.0
MUL (dint)		24	-	65.9
MUL (int)		24	-	35.9
MUX (int)		56	682	15.8
MUX (dint)		84	682	53.2
ROL	(Rotate Left)	40	160	9.7
BCD_TO_DINT	BCD DINT	12	300	273.9
BCD_TO_INT	BCD INT	12	200	111.9
BCD_TO_SINT	BCD SINT	12	140	40.9
BYTE_TO_SINT	BYTE SINT	8	-	0.4
DATE_TO_STRING	DATE	48	458	205.9
DINT_TO_INT	DINT INT	8	-	1.3
DINT_TO_BCD	DINT BCD	12	278	446.9
DT_TO_DATE	DT DATE	16	-	3.3
DT_TO_TOD	DT TOD	16	12	4.1
DT_TO_STRING	DT	48	780	524.9
DWORD_TO_WORD	DWORD WORD	8	-	1.3
INT_TO_DINT	INT DINT	12	-	0.9
INT_TO_BCD	INT BCD	12	180	129.9
NUM_TO_STRING(int)		52	808	159.9
SINT_TO_BCD	SINT BCD	12	140	67.9
STRING_TO_INT	INT	16	1308	281.9
CONCAT		72	248	54.9
DELETE		68	298	63.9
EQ(str)		20	788	38.3
FIND		40	222	73.9
INSERT		68	524	418.9
LEFT		56	158	33.4
LEN		16	48	17.5
LIMIT(str)		80	794	80.9
MAX(str)		76	738	68.4
MID		64	236	47.1
REPLACE		73	584	97.9
RIGHT		56	226	53.9
ADD_TIME (time)		40	280	11.6
DIV_TIME (i1=time)		40	266	67.9

1) ?
 ? 1 :
 ? 2 :
 ? 3 : IL () 2 , 10)
 2) IL() LD() EO
 가
 (1) PB 16 Byte
 (2) 0.4?s, 0.8?s

(GM4-CPUC)

		PB	(Byte)		(us)
		* 1	* 2	* 3	
ABS (int)		44	-	1.36	
ADD (int,dint)		36	-	1.12	
ADD (lint)		60	-	2.36	
AND (word,dword)		24	-	0.76	
AND (lword)		28	-	0.88	
DIV (int,dint)		36	-	1.12	
DIV (lint)		48	-	85	
EQ (int,dint)		32	-	1.00	
LIMIT(int,dint)		60	964	7.9	
MAX (int,dint)		60	1120	10.0	
MOVE (int)		20	-	0.64	
MUL (int,dint)		36	-	1.12	
MUL (lint)		40	-	29	
MUX (int,dint)		68	552	9.0	
MUX (lint)		76	-	12.6	
ROL	(Rotate Left)	52	392	2.44	
SUB (int,dint)		36	-	1.12	
SUB (lint)		60	-	2.36	
BCD_TO_DINT	BCD DINT	24	280	10.6	
BCD_TO_INT	BCD INT	24	136	10.6	
BCD_TO_SINT	BCD SINT SINT	24	84	10.6	
BYTE_TO_SINT	BYTE SINT	20	-	0.64	
DATE_TO_STRING	DATE	48	60	360	
DINT_TO_INT	DINT INT	76	-	2.32	
DINT_TO_BCD	DINT BCD	24	172	18.0	
DT_TO_DATE	DT DATE	20	-	0.64	
DT_TO_TOD	DT TOD	24	12	1.24	
DT_TO_STRING	DT	48	60	442	
DWORD_TO_WORD	DWORD WORD	20	-	0.64	
INT_TO_DINT	INT DINT	24	-	0.76	
INT_TO_BCD	INT BCD	24	108	6.6	
NUM_TO_STRING(int)		56	312	280	
SINT_TO_BCD	SINT BCD	24	84	6.6	
STRING_TO_INT	INT	24	48	360	
CONCAT		68	168	32.0	
DELETE		64	328	22.1	
EQ(str)		64	752	14.1	
FIND		48	228	7.7	
INSERT		68	348	41.5	
LEFT		56	140	19.2	
LEN		24	88	7.1	
LIMIT(str)		90	964	39.0	
MAX(str)		76	1120	20.4	
MID		64	208	32.2	
REPLACE		76	424	37.9	
RIGHT		56	220	29.0	
ADD_TIME (time)		52	220	3.7	
DIV_TIME (i1=time)		52	436	7.5	

1) ?

? 1 :

? 2 :

? 3 : IL (2 , 10)

2)

LD()

가 (ADD,MUL, MAX, MIN, MUX,EQ) 2

가 10

MAX ?

(GM4-CPUC)

		PB	(Byte) *1	*2	(us) *3
ABS (real/ lreal)			36	-	7
ACOS (real/ lreal)	Arc Cosine		36	-	150/ 200
ADD (real/ lreal)			36	-	7
ASIN (real/ lreal)	Arc Sine		36	-	120/ 200
ATAN (real/ lreal)	Arc Tangent		36	-	100/ 140
COS (real/ lreal)	Cosine		36	-	75/ 100
DINT_TO_LREAL	DINT LREAL		24	-	7
DINT_TO_REAL	DINT REAL		24	-	3.5
DIV (real/ lreal)			48	-	10/ 12
EQ (real/ lreal)	'		32	-	9
EXP (real/ lreal)			36	-	110
EXPT (real/ lreal)			32	-	230/ 260
INT_TO_LREAL	INT LREAL		24	-	9
INT_TO_REAL	INT REAL		24	-	5
LINT_TO_LREAL	LINT LREAL		24	-	50
LINT_TO_REAL	LINT REAL		24	-	50
LN (real/ lreal)			36	-	50/ 68
LOG (real/ lreal)			36	-	50/ 75
LREAL_TO_DINT	LREAL DINT		36	-	16
LREAL_TO_INT	LREAL INT		36	-	20
LREAL_TO_LINT	LREAL LINT		36	-	80
LREAL_TO_REAL	LREAL REAL		36	-	20
LREAL_TO_SINT	LREAL SINT		36	-	20
LREAL_TO_UDINT	LREAL UDINT		36	-	80
LREAL_TO_UINT	LREAL UINT		36	-	16
LREAL_TO_ULINT	LREAL ULINT		36	-	76
LREAL_TO_USINT	LREAL USINT		36	-	20
MUL (real/ lreal)			48	-	5/ 7
REAL_TO_DINT	REAL DINT		36	-	18
REAL_TO_INT	REAL INT		36	-	14
REAL_TO_LINT	REAL LINT		36	-	80
REAL_TO_LREAL	REAL LREAL		36	-	7
REAL_TO_SINT	REAL SINT		36	-	20
REAL_TO_UDINT	REAL UDINT		36	-	17
REAL_TO_UINT	REAL UINT		36	-	20
REAL_TO_ULINT	REAL ULINT		36	-	76
REAL_TO_USINT	REAL USINT		36	-	20
SIN (real/ lreal)	Sine		36	-	75/ 100
SINT_TO_LREAL	SINT LREAL		24	-	9
SINT_TO_REAL	SINT REAL		24	-	3.5
SQRT (real/ lreal)			36	-	7/ 11
SUB (real/ lreal)			48	-	7/ 9
TAN (real/ lreal)	Tangent		36	-	90/ 120
TRUNC(real/ lreal)			44	-	18/ 74
UDINT_TO_LREAL	UDINT LREAL		24	-	7
UDINT_TO_REAL	UDINT REAL		24	-	7
UINT_TO_LREAL	UINT LREAL		24	-	5

3 /

UINT_TO_REAL	UINT	REAL	24	-	5
ULINT_TO_LREAL	ULINT	LREAL	24	-	50
ULINT_TO_REAL	ULINT	REAL	24	-	55
USINT_TO_LREAL	USINT	LREAL	24	-	5
USINT_TO_REAL	USINT	REAL	24	-	3.5

1) ?
 ? 1 :
 ? 2 :
 ? 3 : IL (2 , 10)

2) LD()
 ## 가 (ADD, MUL, MAX, MIN, MUX, EQ) 2
 ## 가 10
 ## MAX ?

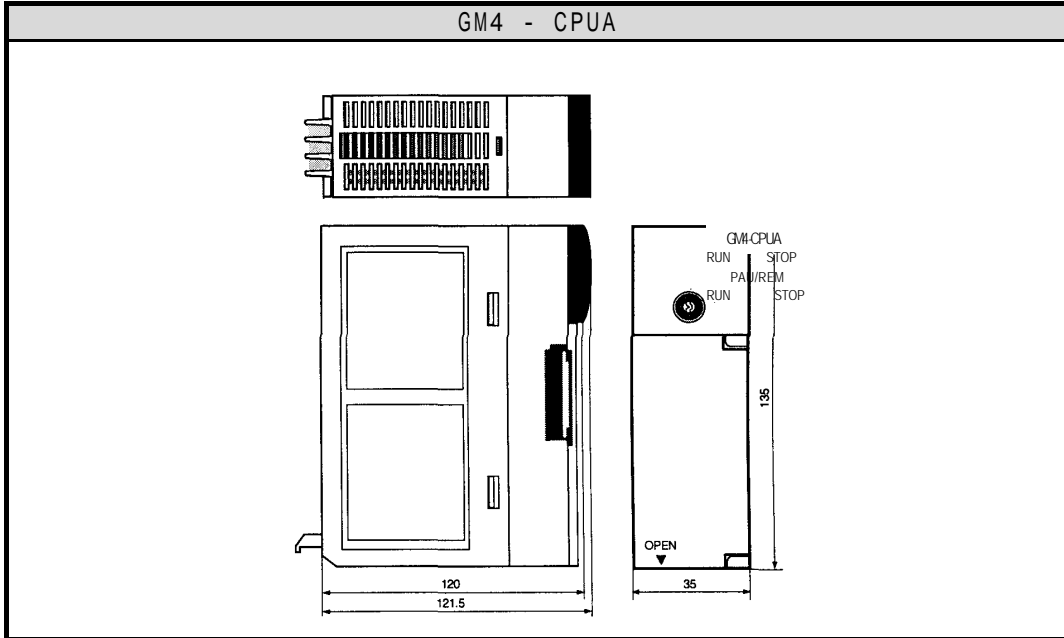
2)

		PB (Byte)?2	(Byte)?3	? 3	(us) *4	
					GM4-CPUA/B	GM4-CPUC
CTU	가	72	110	6	12.8	6.4
CTUD	가	112	186	6	18.4	10.4
F_TRIG		40	38	1	6.6	2.6
RS	Reset Bistable	48	72	2	8.7	2.9
TON	On	56	200	20	11.1	6.2

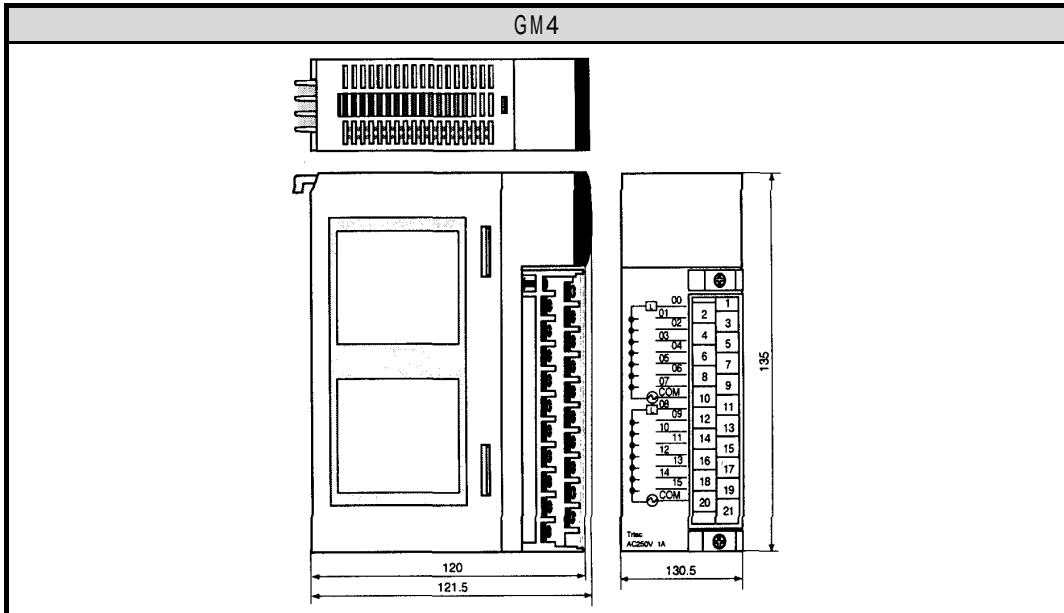
<p>1) ? ? 1 : ? 2 : ? 3 : 2) IL LD</p>
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4 (: mm)

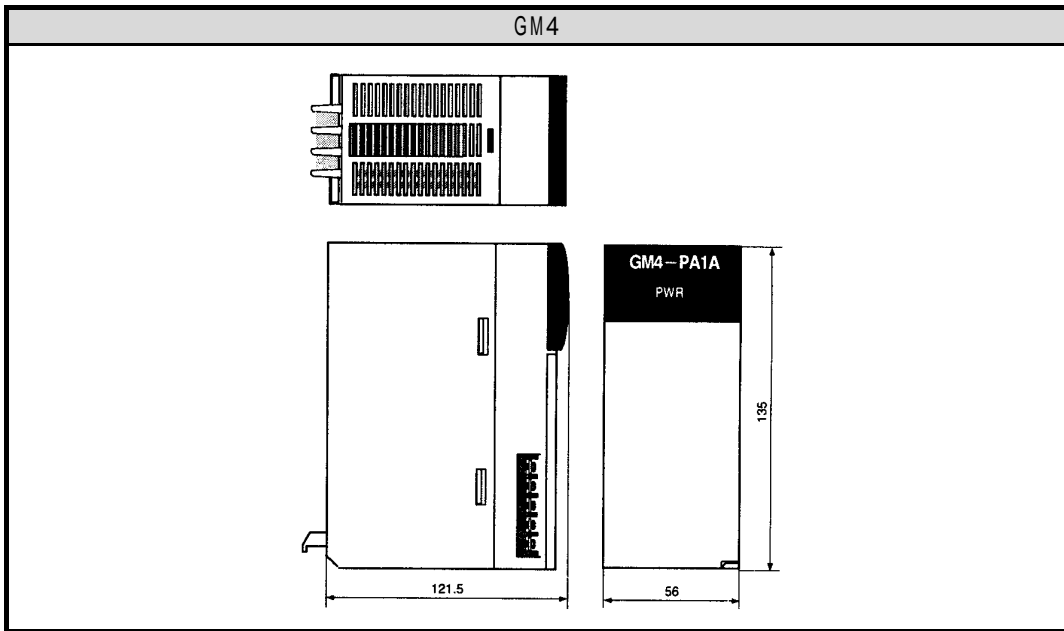
1) CPU



2) 



3)



4) /

